



# **GOVERNMENT POLYTECHNIC, KOLHAPUR**

(An Autonomous Institute of Government of Maharashtra)

## *Curriculum Document*

### **CURRICULUM : MPECS-2016**

(Outcome Based Curriculum)

for

### **DIPLOMA IN ELECTRONICS & TELECOMMUNICATION**

**Secretary**

**Chairman**

Programmewise Board of Studies (PBOS)  
Electronics & Telecommunication Programme  
Government Polytechnic, Kolhapur



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**SECTION – I**

**CURRICULUM PHILOSOPHY  
AND  
STRUCTURE**

## **1. CURRICULUM DEVELOPMENT : INTRODUCTION AND PROCESS OF DEVELOPMENT OF OUTCOME BASED CURRICULUM**

### **Curriculum Design and Development :**

Curriculum is an absolute instructional and effective instrument designed with a student centered approach. It incorporates systematic method of teaching learning process. It is a sequence of planned academic activities; on completion of which the desired programme outcomes are expected to be attained in the student. The curriculum and the course contents are expected to motivate the students to acquire desired level of knowledge and skills. An emphasis and an attempt has been made in the curriculum to get a perfect blending of theoretical concepts and actual requirements of industry. Keen attention has been provided to make it more structured by incorporating the valuable suggestions of industrial experts of PBOs and feed back by the field and academic professionals. An overview of systematic and scientific mode of implementation and evaluation has also been pondered; consequently a practicable model of it has been achieved. It incorporates specific guidelines and assessment criteria for theory/practical/oral modes of evaluation. Specification table for each course has been provided to prepare question paper justifying meticulous coverage.

### **Curriculum philosophy :**

The impact of globalization and rapid changes in the engineering science and technology have been a great cause of comprehensive and noticeable change in engineering fraternity, hence the institutions. Only way to incorporate such a transformation, is to modify the curriculum, preserving the consistency of engineering education. Frequent review and feedback from the experts and the freedom of autonomous status of the institution have encouraged to undertake relevant changes in the curriculum to make it versatile. Consequently the desired competencies and skills are transformed amongst the students in pursuing their preparedness to cope up with the global changes. It aims to promote self reliance and satisfaction of acquiring modern engineering concepts and multi capabilities within the students to make them model technicians.

**“Curriculum is an educational program designed and implemented to achieve specified programme outcomes”**

Hence, in a broad sense, a curriculum incorporates the following :

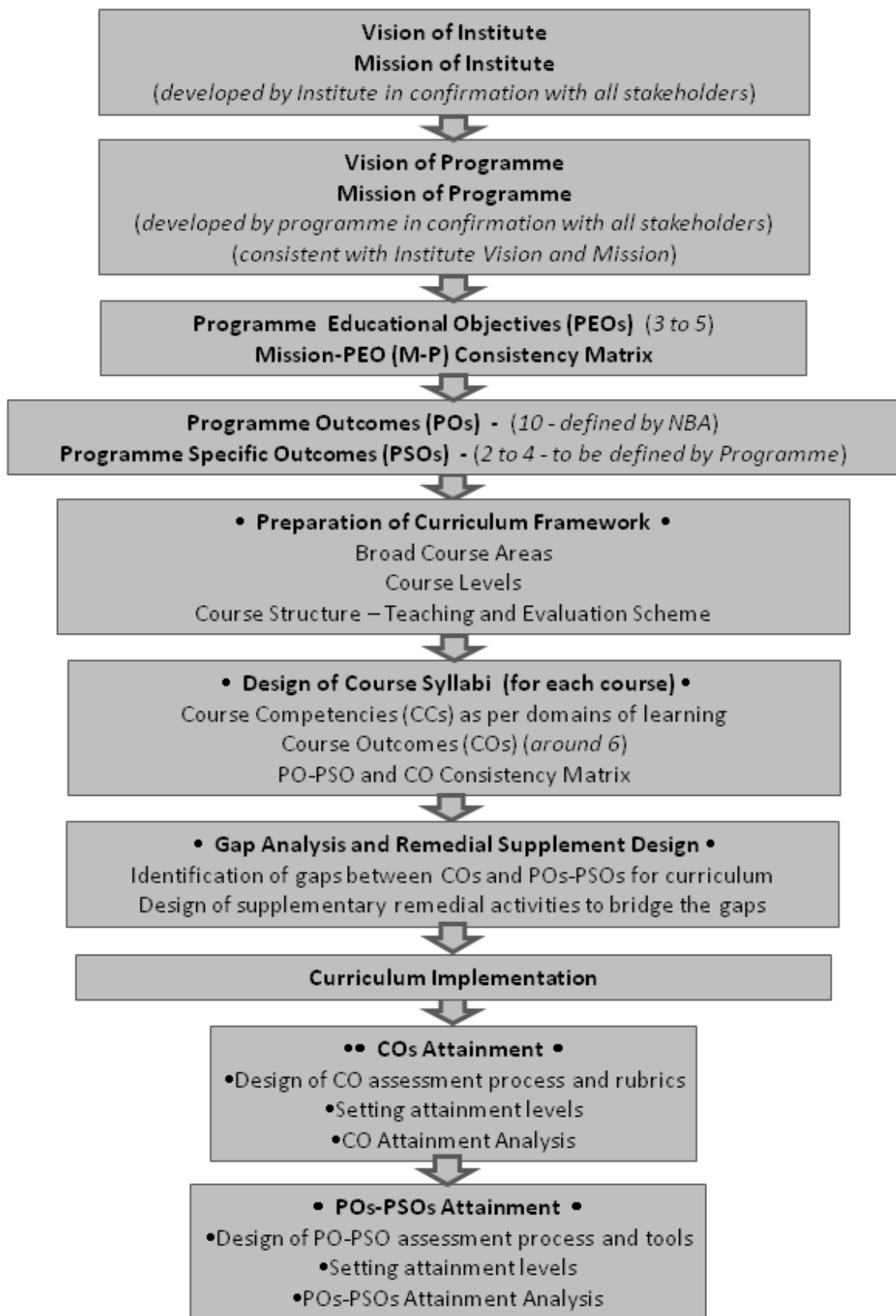
- To define the purpose of education
- To accept systematic planning methods
- To confirm implementation strategies
- To identify and to incorporate needs of industries
- To follow the policy directives
- To cope-up with social concerns
- To aim at personality development of students
- To allow future developments and challenges in emerging science and technology.

### **Outcome-based Curriculum**

Outcome based curriculum is the curriculum based on the concepts of outcome-based education (OBE) philosophy. India is a permanent signatory of the Washington Accord - the international agreement among bodies responsible for accrediting engineering programmes with the National Board of Accreditation (NBA) as the national authority for accrediting degree and diploma programmes in engineering in India. Hence as per the accreditation criteria of the NBA, the curriculum of the Institute is expected to be outcome based.

Outcome Based Education (OBE) is an educational approach in which all the activities of an education system are based on attainment of pre-defined learning outcomes of student. The approach is to be included in the following three aspects of education system :i) Curriculum Design, ii) Curriculum Implementation, iii) Students' Evaluation. The flow diagram shown below summarizes the elements of Outcome-based Education System. The glossary of terms used in academic autonomy and OBE are provided for reference.

OUTCOME BASED EDUCATION SYSTEM



## **Glossary of terms related to Outcome Based Education**

**Outcome-Based Education (OBE)** - It is an educational approach in which all the activities of an education system are based on attainment of pre-defined learning outcomes of student.

The approach is to be included in the following three aspects of education system :i) Curriculum Design, ii) Curriculum Implementation, iii) Students' Evaluation

**Washington Accord and NBA** – It is an International Agreement among bodies responsible for accrediting undergraduate engineering degree programmes. Established in 1989, the signatory countries as of 2014 are Australia, Canada, Taiwan, Hong Kong, India, Ireland, Japan, Korea, Malaysia, New Zealand, Russia, Singapore, South Africa, Sri Lanka, Turkey, the United Kingdom and the United States. National Board of Accreditation (NBA), India has become the permanent signatory member of the Washington Accord on 13<sup>th</sup> June 2014.

The membership of Washington Accord is an international recognition of the quality of undergraduate engineering education offered by the member country and is an avenue to bring it into the world class category. It encourages and facilitates the mobility of engineering graduates and professionals at international level.

NBA accreditation is a quality assurance scheme for higher technical education in India.

The Washington Accord covers engineering degrees and diploma under outcome-based education approach.

**Vision of Institute** - It is a statement that defines concisely the aspirations to be achieved in the near future by the Institute

**Mission of Institute** - It is a set of statements that defines the broad steps to be executed to achieve the vision of the Institute

**Vision of Programme** - It is the vision statement for a particular educational programme (like Civil Engineering Programme, Mechanical Engineering Programme, etc.). Programme Vision should be consistent with the Institute vision

**Mission of Programme** - It is the set of statements that define the broad steps to be executed to achieve the vision of the educational programme

**Programme Educational Objectives (PEOs)** - It is a set of 3 to 5 statements defining the objectives to be attained in order to execute the mission

**Programme Outcomes (POs)** – It is a set of ten generic outcomes, stated by NBA, expected from any engineering diploma-holder in India

**Programme-specific Outcomes (PSOs)** – It is a set of 2 to 4 outcomes to be defined by the programme under consideration in addition to the POs

**Course Outcomes (COs)** – It is a set of about 6 outcomes, expected to be attained by student on learning a course. Course Outcomes shall be defined in curriculum for each course. Course outcomes are worded using action verbs like solve, explain, calculate, compare, distinguish, describe, draw, etc.



**Mission-PEO Consistency Matrix** – It is a matrix showing degree of consistency of PEOs with mission

**PO-CO Consistency Matrix** – It is a matrix showing degree of consistency of COs with POs and PSOs

**Competency** – It is the set of specific abilities, categorized as cognitive, psychomotor and affective domains of learning, from which course outcomes statements are derived

**Cognitive domain** – It is the set of abilities related to thinking

**Bloom's Revised Taxonomy of Cognitive Domain :** It is a six-level cumulative hierarchy of cognitive abilities in the order of increasing complexity as follows :

*Remembering > Understanding > Applying > Analyzing > Evaluating > Creating*

**Psychomotor Domain :** It is the set of abilities related to physical and psychological skills

**Taxonomy of Psychomotor Domain :** It is a six-level cumulative hierarchy of cognitive abilities in the order of increasing complexity as follows :

*Perception > Set > Guided response > Mechanism > Adaptation > Origination*

**Affective Domain :** It is the set of abilities related to attitudinal development

**Taxonomy of Affective Domain :** It is a five-level cumulative hierarchy of affective abilities in the order of increasing complexity as follows :

*Receiving > Responding > Valuing > Organizing > Characterizing*

**Educational Technology :** It is the systematic study of theoretical foundations and material tools to facilitate learning

### **Glossary of terms used in Academic Autonomy and MPECS**

**Academic Autonomy** – It is the freedom and responsibility offered to the Institute by the Government to attain high quality standards in the following three dimensions :

i) Design of own curricula   ii) Conduct of own examinations   iii) Award of own diploma

**Multi-point Entry and Credit System (MPECS)** – It is a system of education in which student can be admitted at different entry levels of qualification and he is offered *credits* along with marks on passing in a course

**Credits** – It is the number of weekly instructional hours provided for a course in the curriculum

**Programme** – It is the particular branch of Engineering in which Diploma is awarded. e.g. Civil Engineering Programme, Mechanical Engineering Programme, etc.

**Curriculum** – It is a document providing plan of the complete academic activity to be conducted by student for award of Diploma in a Programme in tune with the vision of the Institute

**Course** – It is a particular subject defining study and evaluation unit of the curriculum. e.g. Applied Mechanics, Engineering Drawing-1, etc.

**Syllabus** – It is the complete academic information regarding a particular course in a curriculum

**Course Registration (CR)** - It is the procedure to be carried out by every student at the beginning of every semester in which he/she has to declare the courses he/she is going to study in that semester as per academic time table of the Institute. The registration is to be done as per *Rules of Registration* of the Institute.

**Examination Registration (ER)** - It is the procedure to be carried out by every student at the beginning of every semester in which he/she has to declare the courses in which he/she is going appear for examination in that semester as per examination time table of the Institute. The registration is to be done as per *Rules of Registration* of the Institute.

**Curriculum MPECS-2016** - It is the Curriculum of the Institute revised in the year 2016. It is applicable to the students admitted since 2016

**Programme Department** – It is the department of the Institute offering Diploma in a particular Programme. e.g. Civil Engineering Department, Mechanical Engineering Department, etc.

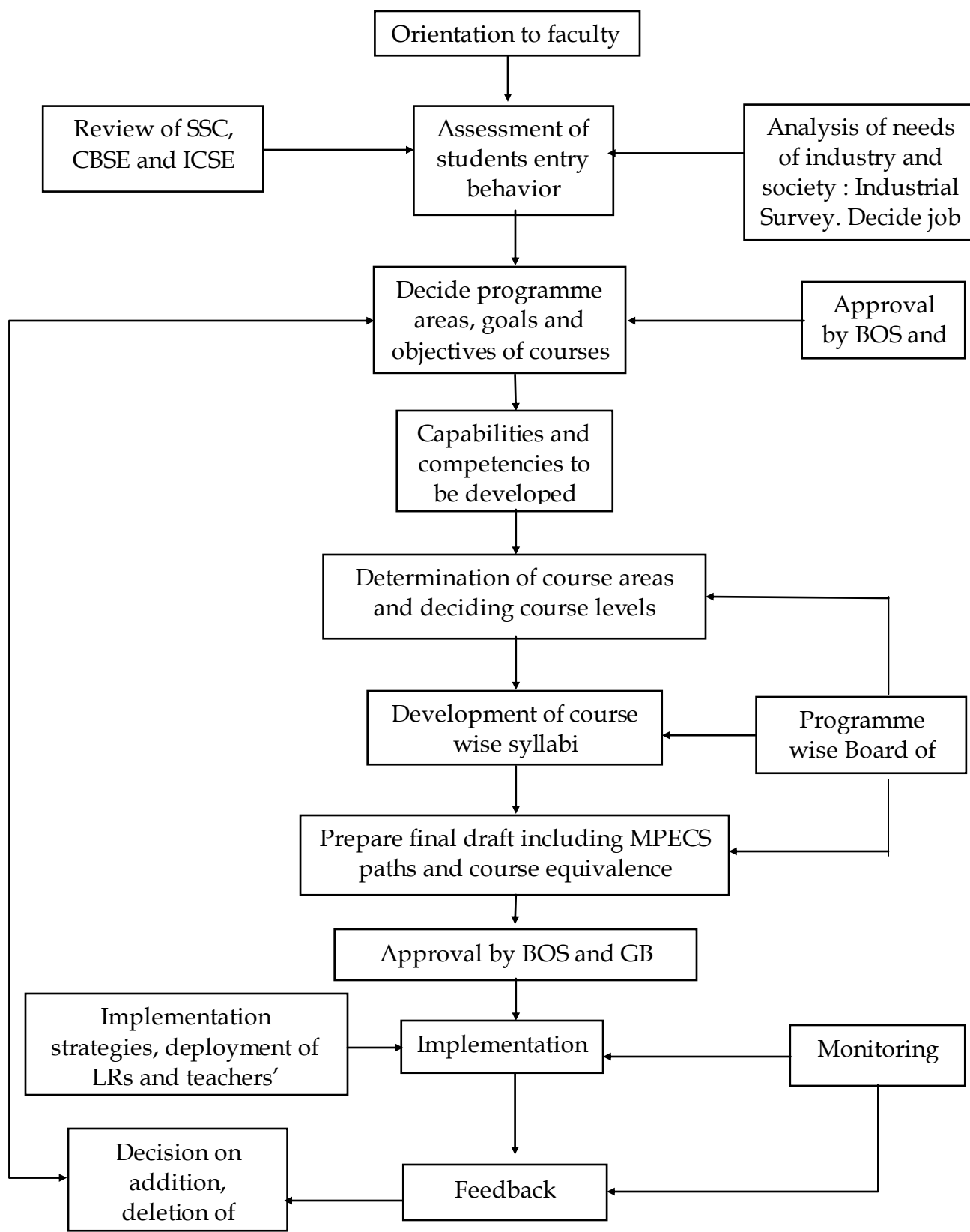
**Programme Dean** – He/she is the Head of Department of a Programme Department

**Allied Department** – It is department that does not award diploma and contributes to curriculum implementation of many Programmes. e.g. Applied Mechanics Department, Workshop Department, Science Department, English Department, Mathematics Department, etc.

#### **Academic Autonomy and MPECS at Government Polytechnic, Kolhapur –**

- Year of award of academic autonomy : 1992    ☐    Year of award of MPECS and Flexibility : 1994  
Government Polytechnic, Kolhapur is the first Government Polytechnic in Maharashtra to have been awarded academic autonomy
- Bodies and Cells under Academic Autonomy :
  - i) Governing Body
  - ii) Board of Studies
  - iii) Programme-wise Boards of Studies
- Examination Committee Curriculum Revisions under Autonomy : 1992, MPECS-2001, MPECS-2006, MPECS-2010, MPECS-2013, MPECS-2016
- Award of Diploma in *Convocation Ceremony* every year

**Curriculum Development Model :**



## **2. VISION, MISSION, PROGRAMME EDUCATIONAL OBJECTIVES (PEOs), PROGRAMME OUTCOMES (POs) AND PROGRAMME-SPECIFIC OUTCOMES (PSOs)**

### **Vision of Institute :**

The institute envisions to be one of the highly recognized institutes for developing competent technicians for quality professional services and entrepreneurship to cater the needs of industry and society.

### **Mission of Institute :**

- To educate and train in multi-disciplinary multi-level programmes to develop competent technicians and skilled manpower for industrial needs
- To ensure employability, encourage entrepreneurship, promote lifelong learning
- To inculcate in students the qualities of a good citizen at individual, social and professional level
- To provide quality management system with focus on effective student-centric education and high recognition.

### **Vision of Programme:**

Program of high recognition and flexibility for the development of Competent technical manpower in the profession of Electronics and Telecommunication .

### **Mission of Programme :**

**M1:** To provide technical education of high recognition to the aspiring learners.

**M2:** To empower student's competency to fulfill nation's project of Digital India

**M3:** To adapt student centric approach in teaching-learning process to mould students for skill oriented professional, social and ethical practices

**M4:** To utilize flexibility in curriculum development to incorporate recent and emerging advancements in the field of Electronics & Telecommunication.

### **Programme Educational Objectives (PEOs):**

Diploma graduates will,

**PEO1.** Apply fundamental knowledge of Basic Sciences, Mathematics and Electronics and Telecommunication engineering in problem solving.

**PEO2.** Operate, demonstrate and debug the systems in the field of Electronics and Telecommunication engineering and to resolve real life problems.

**PEO3.** Attain technical knowledge, skills and attitude to acquire further advancement in technology.

**PEO 4.** Work as a responsible team member of an organization to achieve its goal or can be an individual entrepreneur.

**Programme Outcomes (POs)**

1. **Basic knowledge:** An ability to apply knowledge of basic mathematics, science and engineering to solve the engineering problems.
2. **Discipline knowledge:** An ability to apply discipline - specific knowledge to solve core and/or applied engineering problems.
3. **Experiments and practice:** An ability to plan and perform experiments and practices and to use the results to solve engineering problems.
4. **Engineering Tools:** Apply appropriate technologies and tools with an understanding of the limitations.
5. **The engineer and society:** Demonstrate knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to engineering practice.
6. **Environment and sustainability:** Understand the impact of the engineering solutions in societal and environmental contexts, and demonstrate the knowledge and need for sustainable development.
7. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
8. **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse/multidisciplinary teams.
9. **Communication:** An ability to communicate effectively.
10. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the context of technological changes.

**Programme Specific Outcomes (PSOs)**

**PSO 1: Operate and Maintain:** Competency to apply the concepts of Industrial Electronics in the operation and maintenance of engineering application systems.

**PSO 2: Supervision and providing solution:** Ability to supervise work and reach appropriate solution to simple practical problems in Industrial Electronics engineering industry.

### **3. OVERVIEW AND SALIENT FEATURES OF CURRICULUM: MPECS-2016**

#### **3.1 Overview of Curriculum MPECS-2016**

Total No. of Credits		180
No. of courses offered	Total	37
	Theory	28
Max. no. courses in a semester		07
Total Maximum Marks		4400
Courses in Level IV and V	No.	13
	Credits	65
	Marks	1600
Courses in Level I	No.	10
	Credits	48
	Marks	1125
Courses in Level II	No.	3
	Credits	11
	Marks	225
Courses in Level III	No.	11
	Credits	56
	Marks	1450
Courses in Level IV	No.	06
	Credits	33
	Marks	750
Courses in Level V	No.	07
	Credits	32
	Marks	850
%Ratio of Th:Pr	Marks-wise	62.5:37.5
	Credit-wise	58.88:41.12
No. of Allied Courses		--
Optional Courses	No. of courses	14
	Options/course	03
No. of Practical Exams	Internal	10
	External	08
No. of Orals	Internal	10
	External	03

**Diploma shall be awarded on the basis of marks obtained in Level IV and Level V courses**

### **3.2 Salient Features of Curriculum MPECS-2016**

Addition and deletion of Courses as compared to previous MPECSs with justification:

After considerable discussion with the industry person and PBOS members we found it necessary to add and delete some courses as compared to previous MPECS

#### **Following courses are newly added:-**

1. Embedded Systems
2. Energy conservation

#### **Courses Deleted:**

1. Fundamental of DSP
2. Micro processor and interfacing.
3. Environmental Studies

#### **Major modifications in Course Contents with justification:**

1. Credits of Basic Electrical engg. Course has been reduced.
2. Contents of Physics and Chemistry are modified.
3. Contents of course ECA are modified.
4. Credits of Circuit & Network course are increased.
5. Contents of basic electronics and linear integrated circuits are modified.
6. 8051 microcontroller shifted from level 4 to level 3.
7. Embedded Systems course added to 5<sup>th</sup> semester.
8. Credits of power electronics-I & DTA course are increased.
9. Added term work for LIC and applied electronics

#### **Changes in Implementation Strategy and Treatment:**

1. As per suggestion of Director of MSBTE implant training of 4 weeks after 4<sup>th</sup> semester and two weeks after 2<sup>nd</sup> semester is mandatory. Implant training is now a part of project term work without any marks.
2. In line with the policies of MSBTE there will be no backlog subjects for direct second year admitted students bridge course basic electronics is added without credits with a curriculum to be covered in 24 theory lectures
3. To acquire NBA, outcome based curriculum has been designed.

## TEACHING AND EXAMINATION SCHEME (LEVEL-WISE)

S N	Name of Course	Course Code	Course Abbrevia- tion	L e v e l	Pre-requi- site Course	Teaching Scheme (hours per week)			Examination Scheme (marks)				
						Th	Pract./ Drg./ Tutorial	Cred its	Th	TS	TW	Pr	Or
	Level 1: Foundation Courses												
1.	Electronic Components and Application	EIF 101	FECA	1	--	4	2	6	80	20	25I	-	25 I
2.	Engineering Physics	CCF102	FPHB	1	--	4	2	6	80	20	-	50 I	-
3.	Basic Electronics	EIF 103	FBTX	1	--	4	2	6	80	20		50 I	-
4.	Chemistry Of Engineering Materials	CCF104	FCHB	1	--	4	2	6	80	20	-	50I	-
5.	Basic Mathematics	CCF105	FBMT	1	--	3	1	4	80	20	-	-	-
6.	Engineering Mathematics	CCF106	FEMT	1	CCF105	3	1	4	80	20	-	-	-
7.	Basic Electrical Engg	EIF107	FBEE	1	--	3	2	5	80	20	-	-	25 I
8.	Computer Fundamentals And Applications	EIF108	FCFA	1	--	1	2	3	-	-	25I	50 I	-
9.	Engineering Graphics	CCF109	FEGR	1	--	2	4	6	-	-	25I	50E	
10	Workshop Practice	CCF114	FWSD	1	--	-	2	2	-	-	50I	-	-
	Level 2 : Life Skills and Professional Skills Courses												
11	Generic Skills	CCF201	FGNS	2	--	2	2	4	--	--	25I	50I	--
12	Communication Skills	CCF202	FCMS	2	--	2	2	4	40	10	--	25I	--
13	Professional Practices	CCF203	FPRP	2	--	1	2	3	--	--	25I	--	50I
	Level 3: Basic Technology Courses												
14	Applied Mathematics	EIF301	FAMT	3	CCF106	3	1	4	80	20	--	--	--
15	Applied Electronics	EIF302	FATX	3	EIF 103	3	2	5	80	20	25	50E	--
16	Electronic Measuring Instruments	EIF303	FEMI	3	--	3	2	5	80	20	-	-	25 I
17	C Programming	EIF304	FCPR	3	--	2	2	4	-	-	25I	50E	-
18	Analog Communication	EIF305	FACM	3	--	3	2	5	80	20	-	50 I	-
19	Digital techniques & application	EIF306	FDTA	3	--	4	2	6	80	20	-	50 I	-
20	Linear Integrated Circuits	EIF 307	FLIC	3	EIF 103	4	2	6	80	20	25	50E	
21	Circuit & Network	EIF 308	FCKN	3	--	4	2	6	80	20	-	-	25I
22	8051 Microcontroller	EIF 309	FMCS	3	EIF306	4	2	6	80	20	-	50E	-
23	Digital Communication	ETF310	FDCM	3	--	3	2	5	80	20	-	-	25I
24	Electives -I			3	--	3	1	4	80	20	-	-	-
	Level 4: Applied Technology Courses												
25	Power Electronics-I	EIF 401	FPE1	4	--	4	2	6	80	20	-	50E	-
26	Embedded Systems	EIF 402	FEMB	4	--	4	2	6	80	20	-	50E	-
27	Data Communication & Networking	ETF403	FDCN	4	--	4	2	6	80	20	-	-	25E
28	Principles Of Control Systems	EIF404	FPCS	4	--	4	2	6	80	20		--	25I
29	Simulation Software	EIF405	FSIM	4	--	-	4	4	-	-	25I	50I	-
30	Elective– 3			4	--	3	2	5	80	20	-	-	25I
	Level 5: Management and Diversified Technology Courses												
31	Mobile Communication	ETF 501	FMCM	5	--	4	2	6	80	20	-	-	25E
32	Optical Fiber Communication	ETF502	FOFC	5	--	4	2	6	80	20	-	50 I	-
33	Project I	EIF503	FPR1	5	--	-	2	2	-	-	50I	-	25I
34	Project-II	EIF504	FPR2	5	EIF 503	-	4	4	-	-	50I	-	75E
35	Elective– 2			5	--	3	2	5	80	20	-	-	25I
36	Elective– 4			5	--	3	-	3	80	20	-	-	-
37	Elective– 5			5	--	4	2	6	80	20	-	50E	-



### Optional Courses for Electives

S N	Name of Course	Course Code	Course Abbre- via- tion	L e v e l	Pre- requi- site Course	Teaching Scheme (hours per week)			Examination Scheme (marks)				
						T h	Pract. /Drg. /Tutoria l	Cr ed its	T h	T S	T W	Pr	O r
	<b>Elective – 1</b>												
1.	Non-conventional energy sources	EIF 311	FNCE	3	--	3	1	4	80	20	-	-	-
2.	Higher Mathematics	EIF 312	FHMT	3	EIF301	3	1	4	80	20	-	-	-
	<b>Elective – 2</b>												
3.	Radar & Navigation	ETF 505	FRDN	5	--	3	2	5	80	20	-	-	25I
4.	Audio & Video Engg	ETF 506	FAVE	5	--	3	2	5	80	20	-	-	25I
5.	Energy conservation	EIF 507	FENC	5	--	3	2	5	80	20	-	-	25I
	<b>Elective – 3</b>												
6.	Electronics Circuit Design	EIF 406	FECD	4	--	3	2	5	80	20	-	-	25I
7.	Signals and systems	ETF 407	FSAS	4	--	3	2	5	80	20	-	-	25I
8.	Satellite Communication	ETF 408	FSAT	4	--	3	2	5	80	20	-	-	25I
	<b>Elective – 4</b>												
9.	Industrial Organization Management	CCF 501	FIOM	5	--	3	-	3	80	20	-	-	-
10.	Marketing Management	EIF 509	FMRM	5	--	3	-	3	80	20	-	-	-
11.	Entrepreneurship Development	EIF 510	FETD	5	--	3	-	3	80	20	-	-	-
	<b>Elective – 5</b>												
12.	PLC and Drives	ETF 511	FPLD	5	--	4	2	6	80	20	-	50E	-
13.	PIC Microcontroller	EIF 512	FPIC	5	--	4	2	6	80	20	-	50E	-
14.	VLSI	EIF 513	FVLS	5	--	4	2	6	80	20	-	50E	-

**PATH-WISE COURSE STRUCTURES**  
**Path-1 : Students admitted to First Year - X std. pass outs**

S N	Name of Course	Course Code	Course Abbrevi- a-tion	Le ve l	Pre- re- qui- site Course	Teaching Scheme (hours per week)			Examination Scheme (Marks)				
						Th	Pract. / Drg. / Tutorial	Cr edi ts	Th	TS	TW	Pr	Or
	Semester 1												
1	Electronics Components & Application	EIF101	FECA	1	--	4	2	6	80	20	25 I	--	25I
2	Engineering Physics	CCF102	FPHB	1	--	4	2	6	80	20	-	50 I	-
3	Basic Mathematics	CCF105	FBMT	1	--	3	1	4	80	20	-	-	-
4	Engineering Graphics	CCF109	FEGR	1	--	2	4	6	-	-	25 I	50E	--
5	Generic Skills	CCF201	FGNS	2	--	2	2	4	-	-	25I	50I	--
6	Workshop Practice	CCF114	FWSD	1	--	0	2	2	-	-	50 I	-	-
	Semester 2												
7	Basic Electronics	EIF103	FBTX	1	--	4	2	6	80	20		50 I	-
8	Chemistry Of Engineering Materials .	CCF104	FCHB	1	--	4	2	6	80	20	-	50 I	-
9	Engineering Mathematics	CCF106	FEMT	1	CCF105	3	1	4	80	20	-	-	-
10	Basic Electrical Engg	EIF107	FBEE	1	--	3	2	5	80	20	-	-	25 I
11	Computer Fundamentals And Applications	EIF108	FCFA	1	--	1	2	3	-	-	25 I	50 I	-
12	Communication Skills	CCF202	FCMS	2	--	2	2	4	40	10	--	25 I	--
	Semester 3												
13	Applied Mathematics	EIF 301	FAMT	3	CCF106	3	1	4	80	20	--	--	--
14	Applied Electronics	EIF 302	FATX	3	EIF 103	3	2	5	80	20	25	50E	--
15	Electronic Measuring Instruments	EIF 303	FEMI	3	--	3	2	5	80	20	-	-	25 I
16	C Programming	EIF304	FCPR	3	--	2	2	4	-	-	25 I	50E	-
17	Analog Communication	EIF305	FACM	3	--	3	2	5	80	20	-	50 I	-
18	Digital techniques & application	EIF306	FDTA	3	--	4	2	6	80	20	-	50 I	-
19	Professional Practices	CCF203	FPRP	2	--	1	2	3			25 I	-	50I
	Semester 4												
20	Linear Integrated Circuits	EIF307	FLIC	3	EIF 103	4	2	6	80	20	25	50E	
21	Circuit & Network	EIF308	FCKN	3		4	2	6	80	20	-	-	25I
22	8051 Microcontroller	EIF309	FMCS	3	EIF306	4	2	6	80	20	-	50E	-
23	Digital Communication	ETF310	FDCM	3	--	3	2	5	80	20	-	-	25I
24	Simulation Software	EIF405	FSIM	4	--	-	4	4	-	-	25I	50I	-
25	Electives-1			3	--	3	1	4	80	20	-	-	-
	Semester 5												
26	Power Electronics-I	EIF401	FPE1	4	--	4	2	6	80	20	-	50E	-
27	Embedded Systems	EIF402	FEMB	4	--	4	2	6	80	20	-	50E	-
28	Optical Fiber Communication	ETF502	FOFC	5	--	4	2	6	80	20	-	50 I	-
29	Project I	EIF503	FPR1	5	--	-	2	2	-	-	50I	-	25I
30	Elective- 2			5	--	3	2	5	80	20	-	-	25I
31	Elective- 3			4	--	3	2	5	80	20	-	-	25I
	Semester 6												
30	Data Communication & Networking	ETF403	FDCN	4	--	4	2	6	80	20	-	-	25E
31	Mobile Communication	ETF 501	FMCM	5	--	4	2	6	80	20	25I	-	25E
32	Principles Of Control Systems	EIF404	FPCS	4	--	4	2	6	80	20		50I	
33	Project-II	EIF504	FPR2	5	EIF 503	-	4	4	-	-	50I	-	75E
34	Elective- 4			5	--	3	-	3	80	20	-	-	-
35	Elective- 5			5	--	4	2	6	80	20	-	50E	-

**Path-wise Course Structure**  
**Path-2 : Students admitted directly to Second Year with XII Science (PCM/PCMB)**

S N	Name of Course	Course Code	Course Abbrevi a-tion	Le ve l	Pre- re- qui- site Course	Teaching Scheme (hours per week)			Examination Scheme (Marks)				
						Th	Pract. / Drg. / Tutorial	Cr edi ts	Th	TS	TW	Pr	Or
	Semester 3												
13	Applied Mathematics	EIF 301	FAMT	3	CCF106	3	1	4	80	20	--	--	--
14	Applied Electronics	EIF 302	FATX	3	EIF 103	3	2	5	80	20	25	50E	--
15	Electronic Measuring Instruments	EIF 303	FEMI	3	--	3	2	5	80	20	-	-	25 I
16	C Programming	EIF 304	FCPR	3	--	2	2	4	-	-	25 I	50E	-
17	Analog Communication	EIF 305	FACM	3	--	3	2	5	80	20	-	50 I	-
18	Digital techniques & application	EIF 306	FDTA	3	--	4	2	6	80	20	-	50 I	-
19	Professional Practices	CCF 203	FPRP	2	--	1	2	3			25 I	-	50I
	Semester 4												
20	Linear Integrated Circuits	EIF 307	FLIC	3	EIF 103	4	2	6	80	20	25	50E	
21	Circuit & Network	EIF 308	FCKN	3		4	2	6	80	20	-	-	25I
22	8051 Microcontroller	EIF 309	FMCS	3	EIF306	4	2	6	80	20	-	50E	-
23	Digital Communication	ETF 310	FDCM	3	--	3	2	5	80	20	-	-	25I
24	Simulation Soft ware	EIF 405	FSIM	4	--	-	4	4	-	-	25I	50I	-
25	Electives-1			3	--	3	1	4	80	20	-	-	-
	Semester 5												
26	Power Electronics-I	EIF 401	FPE1	4	--	4	2	6	80	20	-	50E	-
27	Embedded Systems	EIF 402	FEMB	4	--	4	2	6	80	20	-	50E	-
28	Optical Fiber Communication	ETF 502	FOFC	5	--	4	2	6	80	20	-	50 I	-
29	Project I	EIF 503	FPR1	5	--	-	2	2	-	-	50I	-	25I
30	Elective- 2			5	--	3	2	5	80	20	-	-	25I
31	Elective- 3			4	--	3	2	5	80	20	-	-	25I
	Semester 6												
30	Data Communication & Networking	ETF403	FDCN	4	--	4	2	6	80	20	-	-	25E
31	Mobile Communication	ETF 501	FMCN	5	--	4	2	6	80	20	25I	-	25E
32	Principles Of Control Systems	EIF 404	FPCS	4	--	4	2	6	80	20		25I	
33	Project-II	EIF 504	FPR2	5	EIF 503	-	4	4	-	-	50I	-	75E
34	Elective- 4			5	--	3	-	3	80	20	-	-	-
35	Elective- 5			5	--	4	2	6	80	20	-	50E	-

Note : Separate *Supplementary Input Sessions* for necessary content of First Year courses shall be designed and arranged for these students so as to bridge the gap of FY courses

## 6. EXEMPTIONS FOR COURSES

**Eligibility for Exemptions for First and Second Semester Courses of MPECS-2016 for students admitted on X-pass basis**

S N	Name of Course	Course Code	Whether eligible for exemption ? ( Yes / No )				
			XII Science	XII Tech.	XII MCVC	XII Voc.	ITI
1	Engineering Physics (CE/ME/SM/MT)	CCF101	YES	YES	No	No	No
2	Engineering Physics (EE/IE/ET/IT)	CCF102	YES	YES	No	No	No
3	Engineering Chemistry (CE/ME/SM/MT)	CCF103	No	No	No	No	No
4	Engineering Chemistry (EE/IE/ET/IT)	CCF104	No	No	No	No	No
5	Basic Mathematics	CCF105	YES	YES	No	YES	No
6	Engineering Mathematics	CCF106	YES	YES	No	YES	No
7	Engineering Drawing-1 (CE/ME/MT)	CCF107	No	YES	No	No	No
8	Engineering Drawing-1(SM)	CCF117	No	YES	No	No	No
9	Engineering Drawing-2 (CE/ME/SM/MT)	CCF108	No	YES	No	No	No
10	Engineering Graphics (EE/IT/ IE/ET)	CCF109	No	YES	No	No	No
11	Applied Mechanics	CCF110	No	No	No	No	No
12	Workshop Practices-1 (CE)	CCF111	No	YES	YES	YES	YES
13	Workshop Practices-1 (ME, SM,MT)	CCF112	No	YES	YES	YES	YES
14	Workshop Practices (EE)	CCF113	No	YES	YES	YES	YES
15	Workshop Practices (IE, ET)	CCF114	No	YES	YES	YES	YES
16	Workshop Practices -2 (CE)	CCF115	No	YES	YES	YES	YES
17	Workshop Practices -2 (ME, SM,MT)	CCF116	No	YES	YES	YES	YES
18	Generic Skills	CCF201	No	No	No	No	No
19	Communication Skills	CCF202	No	No	No	No	No

Note : The above eligibility is subject to condition that the student has secured at least 40 % marks in the respective subject.

Students seeking exemption for any other subjects should contact Academic Coordinator / Controller of Examinations.

## 9. COURSE EQUIVALENCE FOR PREVIOUS MPECSs

SN	MPECS-2009	MPECS-2010	MPECS-2013	MPECS-2016
1.	<b>R101</b> -Generic Skill	<b>X101</b> -Generic Skill	<b>CCE201</b> -Gen. Skills	<b>CCF201</b> -Gen. Skills
2.	<b>ET103</b> - Basic Physics	<b>X102</b> -Basic Physics	<b>CCE102</b> -Engineering Physics	<b>CCF102</b> -Engineering Physics
3.	<b>ET104</b> - Basic Chemistry	<b>X103</b> -Applied Chemistry	<b>CCE104</b> -Engineering Chemistry	<b>CCF104</b> - Chemistry of Engineering materials
4.	<b>R107</b> -Basic Mathematics	<b>X104</b> -Basic Mathematics	<b>CCE105</b> -Basic Mathematics	<b>CCF105</b> -Basic Mathematics
5.	ET109-Engg. Drawing	IX/EJ 105-Engineering Drawing	-----	-----
6.	<b>R102</b> -Comm.Skills	<b>X106</b> -Comm.Skills	<b>CCE202</b> -Com.Skills	<b>CCF202</b> -Com.Skills
7.	<b>ET113</b> -Workshop Practice	<b>IX/EJ107</b> -Basic workshop practice	<b>CCF114</b> -Workshop Practice	<b>CCF114</b> -Workshop Practice
8.	<b>ET105</b> -Engg. Science	<b>X108</b> -Engg. Science	---	
9.	<b>ET106</b> -Electronic Components and application	<b>IX/EJ 109</b> -Electronic Components and Applications	<b>IEE/ETE103</b> -Electronic Components and Applications	<b>EIF 101</b> -Electronics Components &Application
10.	<b>R108</b> -Engg Mathematics	<b>X110</b> -Engineering Mathematics	<b>CCE106</b> - Engineering Mathematics	<b>CCF 106</b> - Engineering Mathematics
11.	<b>ET111</b> -Computer Fundamental & Application	----	<b>IEE/ETE108</b> -Computer Fundamental & Application	<b>EIF 108</b> - Computer Fundamental & Application
12.	<b>ET112</b> -Basic Electronics	<b>IX/EJ111</b> Basic Electronics	<b>IEE/ETE103</b> -Basic Electronics	<b>EIF 103</b> -Basic Electronics
13.	-----	-----	<b>EIF107</b> - Basic Electrical Engg	<b>EIF 107</b> - Basic Electrical Engg
	-----	-----	<b>CCE 109</b> -Engineering Graphics	<b>CCF 109</b> -Engineering Graphics
14.	-----	<b>IX/EJ112</b> Circuit & Network	<b>IEE/ETE308</b> Circuit & Network	<b>EIF 308</b> - Circuit & Network
15.	----	----	<b>CCE203</b> - Professional Practices	<b>CCF203</b> - Professional Practices
16.	----	----	<b>CCE204</b> -Environmental Studies	--
17.	<b>ET114</b> -Electronics Manufacturing Graphics & Workshop	---	---	---
18.	<b>ET201</b> - Applied Mathematics	<b>ET201</b> - Applied Mathematics	<b>IEE/ETE301</b> - Applied Mathematics	<b>EIF 301</b> - Applied Mathematics
19.	<b>ET202</b> - Applied Electronics	<b>ET202</b> - Applied Electronics	<b>IEE/ETE302</b> - Applied Electronics	<b>EIF 302</b> -Applied Electronics
20.	<b>ET203</b> - Personality Development	----	----	----
21.	<b>ET204</b> - Electronics Measuring Instruments	<b>IX/EJ203</b> -Electronics Measuring	<b>IEE/ETE303</b> -Electronics Measuring	<b>EIF 303</b> - Electronics Measuring Instruments

		Instruments	Instruments	
22.	<b>ET110-C</b> Programming	<b>IX/EJ204 C</b> Programming	<b>IEE/ETE304- C</b> Programming	<b>EIF 304-C</b> Programming
23.	<b>ET205-</b> Electrical Engineering	---	---	----
24.	<b>ET206-</b> Analog Communication	<b>IX/EJ205-</b> Analog Communication	<b>IEE/ETE305-</b> Analog Communication	<b>EIF 305-</b> Analog Communication
25.	<b>ET207-</b> Linear Integrated Circuits	<b>IX/EJ206-</b> Linear Integrated Circuits	<b>IEE/ETE307-</b> Linear Integrated Circuits	<b>EIF 307-</b> Linear Integrated Circuits
26.	<b>ET208-</b> Digital Techniques	<b>IX/EJ207-</b> Digital Techniques	<b>IEE/ETE306-</b> Digital Techniques & Application	<b>EIF 306-</b> Digital Techniques &Application
27.	<b>ET209-</b> Digital Communication	<b>IX/EJ208-</b> Digital Communication	<b>ETE310-</b> Digital Communication	<b>ETF310-</b> Digital Communication
28.	<b>ET210-</b> Microprocessor & Interfacing	<b>IX/EJ209-</b> Microprocessor & Interfacing	<b>IEE/ETE309-</b> Microprocessor & Interfacing	----
29.	<b>ET211-</b> Project & Seminar-I	---	<b>IEE/ETE503-</b> Project-1	<b>EIF 503-</b> Project-1
30.	<b>R222-</b> Electrical CKTS. & M/Cs	---	---	---
31.	<b>R223-</b> Basic Electronics	---	---	---
32.	<b>R227-</b> Non Conv Energy Sources	---	<b>IEE/ETE311-</b> Non Conv Energy Sources	<b>EIF 311-</b> Non Conv Energy Sources
33.	<b>R228-</b> Higher Maths	<b>IX/EJ210-</b> Higher Engineering Maths	<b>IEE/ETE312-</b> Higher Mathematics	<b>EIF 312-</b> Higher Mathematics
34.	<b>ET302-</b> Industrial Electronics	<b>IX/EJ211-</b> Industrial Electronics	<b>IEE/ETE401-</b> Power Electronics- 1	<b>EIF 401-</b> Power Electronics-1
35.	<b>ET301-</b> Advance Communication System	<b>EJ301-</b> Advance Communication System	---	---
36.	<b>ET303-</b> 8051 Microcontroller	<b>EJ302-</b> 8051 Microcontroller	<b>IEE/ETE402-</b> 8051 Microcontroller	<b>EIF 309-</b> 8051 Microcontroller
37.	<b>ET304-</b> Control System	<b>EJ303-</b> Feedback Control System	<b>IEE/ETE404-</b> Principles of Control System	<b>EIF 404-</b> Principles Of control System
38.	<b>ET306-</b> Instrumentation	<b>EJ304-</b> Instrumentation	---	---
39.	<b>ET308-</b> Optoelectronics	<b>EJ 305-</b> Optoelectronics	---	---
40.	<b>ET309-</b> Medical Electronics	<b>EJ306-</b> Medical Electronics	---	---
41.	<b>ET310-</b> Optical Fiber Communication	<b>EJ307-</b> Optical Fiber Communication	<b>ETE502-</b> Optical Fiber Communication	<b>ETF502-</b> Optical Fiber Communication
42.	<b>ET311-</b> Electronics Circuit Design	<b>EJ308-</b> Electronics Circuit Design	<b>IEE/ETE406-</b> Electronics Circuit Design	<b>EIF 406-</b> Electronics Circuit Design

43.	<b>ET312-</b> Signals & Systems	<b>EJ309-</b> Signals & Systems	<b>ETE507-</b> Signals & Systems	<b>ETF 407-</b> Signals & Systems
44.	<b>ET401-</b> Mobile Communication	<b>EJ401-</b> Mobile Communication	<b>ETE501-</b> Mobile Communication	<b>ETF501-</b> Mobile Communication
45.	<b>ET402-</b> PC Hardware & Maintenance	----	----	----
46.	<b>ET403-</b> Introduction To Matlab & OrCAD	<b>EJ402-</b> Introduction To Matlab & OrCAD	<b>IEE/ETE405-</b> Simulation Software	<b>EIF 405-</b> Simulation Software
47.	<b>ET305-</b> Project & Seminar-II	<b>EJ403-</b> Project	<b>IEE/ETE504-</b> Project II	<b>EIF 504-</b> Project II
48.	<b>ET404-</b> Industrial Organization & Management	<b>EJ404-</b> Industrial Organization & Management	<b>IEE/ETE508-</b> Industrial Organization & Management	<b>CCF 501 -</b> Industrial Organization & Management
49.	<b>ET405-</b> Marketing Management	<b>EJ405-</b> Marketing Management	<b>IEE/ETE509-</b> Marketing Management	<b>EIF 509-</b> Marketing Management
50.	<b>ET406-</b> Project Management	-----	----	----
51.	<b>ET307-</b> Entrepreneurship Development	<b>EJ406-</b> Entrepreneurship Development	<b>IEE/ETE510-</b> Entrepreneurship Development	<b>EIF 510-</b> Entrepreneurship Development
52.	<b>ET407-</b> VLSI Design	<b>EJ407-</b> VLSI Design	<b>IEE/ETE 407-</b> VLSI	<b>EIF 513-</b> VLSI
53.	<b>ET408-</b> Embedded System	<b>EJ408-</b> Embedded System	---	<b>EIF 402-</b> Embedded Systems
54.	<b>ET412-</b> Data Communication & Networks	<b>EJ409-</b> Data Communication & Networks	<b>ETE403-</b> Data Communication & Networks	<b>ETF403-</b> Data Communication & Networks
55.	<b>ET410-</b> Audio Video Engineering	<b>EJ410-</b> Audio Video Engineering	<b>ETE 506-</b> Audio Video Engineering	<b>ETF506-</b> Audio Video Engineering
56.	<b>ET411-</b> Microwave Engineering	<b>EJ411-</b> Microwave Engineering	----	----
57.	<b>ET409-</b> Introduction TO DSP	<b>EJ412-</b> DSP	<b>ETE513-</b> Fundamental of DSP	----
58.	<b>ET413-</b> Mobile Phone Servicing	<b>EJ413-</b> Mobile Phone Servicing	---	---
59.	<b>ET414-</b> Computer Networking	<b>EJ414-</b> Computer Networking	----	----
60.	<b>ET415-</b> Visual Basic & MS ACCESS	<b>EJ415-</b> VB & MS ACCESS	----	----
61.	----	----	<b>ETE505-</b> Radar & Navigation	<b>ETF505-</b> Radar & Navigation
62.	----	----	<b>ETE408-</b> Satellite Communication	<b>ETF408-</b> Satellite Communication
63.	----	----	<b>ETE 511-</b> PLC & Drives	<b>ETF511-</b> PLC & Drives
64.	----	----	<b>ETE 512-</b> PIC Microcontroller	<b>EIF 512-</b> PIC Microcontroller
65.	----	----	----	<b>EIF 507-</b> Energy Conservation

## **8. PROFORMAS FOR EVALUATION OF TERM WORK, ORALS AND PRACTICALS**



## PROFORMA - I

**GOVERNMENT POLYTECHNIC, KOLHAPUR**

### Performance for Final Assessment of ORAL/PRACTICAL

**By Internal & External Examiner**

( For subject having ONLY ORAL / PRACTICAL )

Course Code & Course Name -- \_\_\_\_\_

Programme - \_\_\_\_\_

Summer / Winter Exam - Date -

[illegible]

**Name and Signature of Internal Examiner      Name and Signature of External Examiner**

Summer / Winter Exam - \_\_\_\_\_ Date - \_\_\_\_\_

[illegible]

## PROFORMA-III

**GOVERNMENT POLYTECHNIC, KOLHAPUR**

## Performance for Final Assessment of TERMWORK

**By Internal Examiner**

**( For subject having ONLY TERMWORK )**

Course Code & Course Name -- \_\_\_\_\_

Programme - \_\_\_\_\_

Summer / Winter Exam - \_\_\_\_\_ Date - \_\_\_\_\_

[illegible]

**Name and Signature of Internal Examiner**

### PROFORMA – IV

**GOVERNMENT POLYTECHNIC, KOLHAPUR**

### Performance for Final Assessment of ORAL / PRACTICAL

### By Internal & External Examiner

(For subject having ORAL / PR & TW)

Course Code & Course Name -- \_\_\_\_\_

Programme -

Summer / Winter Exam - \_\_\_\_\_ Date - \_\_\_\_\_

[illegible]

**Name and Signature of Internal Examiner**

**Name and Signature of  
External Examiner**

Summer / Winter Exam - \_\_\_\_\_ Date - \_\_\_\_\_

[illegible]

29

Summer / Winter Exam - \_\_\_\_\_ Date - \_\_\_\_\_

[illegible]

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# **LEVEL- I**

# **FOUNDATION COURSES**



**Course ID:**

**Course Name : Electronic Components and Applications**  
**Course : EIF 101**  
**Course Abbreviation: FECA**

**TEACHING AND EVALUATION SCHEME :**

**Prerequisites : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	4	6
Practical	2	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	oral	
<b>Details of Evaluation</b>	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per proforma VI	Internal Oral Exam *	
<b>Marks</b>	<b>20</b>	<b>--</b>	<b>80</b>	<b>25</b>	<b>25 I</b>	<b>150</b>

I-Internal Examination

\* Assessment as per Pro-forma V

**Term Work Assessment as per Pro-forma VI.**

**RATIONALE:**

Material science plays vital role in Technology. Due to rapid up gradation and modification in the use of electronic components, every engineer should have the knowledge of components. This subject gives clear idea of all fundamentals of electronic components and their practical applications. This subject includes method of fabrication, working, testing, characteristics, specifications, assembling, designing. And fault finding. In industrial applications, students will be able to understand and visualize other electronic circuits and devices.

**COMPETENCY:**

Understanding and visualizing electronic circuits and devices.

**Cognitive: List** passive components; types of cables, connectors, switches, relays displays and design of PCB.

**Psychomotor: Calculate** values of resistors, capacitors, inductors and making of PCB.

**Affective :Attitude of** i) Logic ii) accuracy iii) precision) punctuality

**COURSE OUTCOMES:**

**EIF101-1** Illustrate the use of components based on the functions and the specifications in the problem solving.

**EIF101-2** Explain electronics components with respect to its classifications, functions, specifications and applications.

**EIF101-3** Identify and test electronic components in the given circuit.

**EIF101-4** Develop PCB layout for the given circuit..

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Understanding and visualizing electronic circuits and devices	2	-	2	-	-	-	-	-	-	-	3	2
<b>EIF101-1</b>	2	-	1	-	-	-	-	-	-	-	-	-
<b>EIF101-2</b>	2	-	1	-	-	-	-	-	-	-	-	-
<b>EIF101-3</b>	2	-	1	-	-	-	-	-	-	-	-	-
<b>EIF101-4</b>	1	-	3	2	-	-	-	1	-	1	3	2

**CONTENT :**

**A) THEORY :**

**Section I**

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<i><b>EIF101-1</b> Identify different types of resistors and calculate its values.</i>			
01	<b>Resistors:</b> 1.1 Components-discrete,nondiscrete, Active, passive components. 1.2 Concept of Resistors, types of resistors, Materials used for resistors, Definition of Linear and nonlinear resistors 1.3 Resistors general specification: - maximum voltage rating, power rating, temperature coefficient, tolerance, Ohmic range, voltage coefficient, operating temperature 1.4 Construction, specification, application of Carbon film resistors, Colour Coding with three, four and five bands, Equivalent circuit of resistors 1.5 Standard wire wound register construction, working, specification, application, characteristic curves of TDR, LDR 1.6 Concept of linear & logarithmic potentiometer, Comparison between Linear and Logarithmic Potentiometer, rheostat, cermet trimmer.	12	14
<i><b>EIF101-2</b> Identify different types of capacitors and calculate its values.</i>			
02	<b>Capacitors:</b> 2.1 Classification of capacitors, Materials used for capacitors, Type of dielectrics 2.2 capacitors specification :- capacitor working voltage, Insulation resistance, c/v ratio, power factor 2.3 Fixed Capacitor- construction, specification ,application of Disc Ceramic capacitor, 2.4 Aluminum electrolytic capacitor, Tantalum electrolytic capacitor 2.5 Variable capacitor, Requirements of variable condenser Construction, working, specification, applicationof AirGang, PVC gang capacitor, Trimmer capacitor – concentric cylinder type.Color coding of capacitors, Equivalent circuit of capacitors	12	14

<b>EIF101-3 Identify different types of inductors and calculate its values.</b>			
03	<b>Inductors:</b> 3.1 Inductor Specifications :- self inductance ,mutual inductance, coefficient of coupling ,leakage inductance, operation at low & high frequency, Q factor, Inductive Reactance. 3.2 Construction, application of Air core, iron core, ferrite core, frequency range Inductors(A.F.,R.F.,I.F.),filter choke, toroidal Inductor 3.3 Construction, working specification, application of Slug tuned Inductor, Tapped Inductor. 3.4 colors coding of Inductor using color band system. Equivalent circuit of Inductor.	8	12

### Section-II

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<b>EIF101-4 Identify different types of cables and connectors and also specify their applications.</b>			
04	<b>Cables:</b> 4.1 General specifications of cables- characteristic impedance, current carrying capacity, flexibility. 4.2 Types of cables- construction, specifications and applications of coaxial cable, telephone cable, FRC cable, Twin core cable(Twisted & Shielded type)cable used for CRO, optical Fiber Cable. <b>Connectors:</b> 4.3 general specifications of connectors- contact resistance, breakdown voltage, insulation resistance 4.4 Type of connectors – construction, specifications and applications of BNC, TNC, RF, D series, Audio, Video, printer, edge, FRC connectors, Phone Plug & Jacks	10	14
<b>EIF101-5 Identify different types of switches, relays, displays and also specify their applications.</b>			
05	<b>Switches ,Relays And Displays</b> <b>Switches:</b> 5.1 Switch Specifications - voltage rating, contact current rating, contact resistance, life. 5.2 Characteristics of switch , electrical life, mechanical life	14	16

	<p>5.3 construction , specification ,application of Toggle, Rotary, push to on &amp; push to off, Rocker switch, slide switch, MCB,LCB switches</p> <p><b>Relays</b></p> <p>5.4 Define NO,NC contact, Characteristics of relay</p> <p>5.5 construction, working, specification, application of General purpose relay ,dry reed ,mercury wetted Reed relay</p> <p>5.6 Difference between switch &amp; relay Displays ,contactors.</p> <p><b>Displays:</b></p> <p>5.7 types of displays</p> <p>5.8 LED construction, operation &amp; application of Bicolor LED, seven segment display common cathode &amp; common anode display, Dot matrix array, sixteen, fourteen segment display</p> <p>5.9 construction, operation &amp; application of liquid crystal display, (LCD)</p>		
<b>EIF101-6 Design PCB, mount components on PCB and use it for a specific application.</b>			
6	<p><b>Printed Circuit Board</b></p> <p>6.1 Introduction to PCB ,Advantages &amp; disadvantages of PCB, Types of PCB</p> <p>6.2 Base &amp; Conducting material, types of laminates, Flowchart for preparation of PCB.</p> <p>6.3 Screen printing ,photo printing method</p> <p>6.4 Drilling, Mounting of components</p> <p>6.5 Soldering technique: Methods of soldering, Dip, wave, Hand, Necessary conditions for soldering</p> <p>6.6 Final protection, Safety, health &amp; Medical aspects of soldering</p> <p>6.7 Introduction to SMD</p>	08	10

**Specification Table For Question Paper of Theory Examination:**

Section / Topic no.	Name Of the Topic	Distribution Of Marks (Level Wise)			Course outcomes	Total Marks
		Knowledge	Comprehension	Applications		
I/1	Resistors	8	4	2	EIF101-1,2,3	14
I/2	Capacitors	8	4	2	EIF101-1,2,3	14
I/3	Inductors	8	2	2	EIF101-1,2,3	12
I/4	Cables	8	4	2	EIF101-1,2,3	14
II/5	Switches, relays and displays	8	4	4	EIF101-1,2,3	16
II/6	Printed Circuit Board	6	2	2	EIF 101-4	10
				<b>TOTAL</b>		<b>80</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK** Term work shall consist of the following :

**(Minimum Eight From List)**

**Laboratory experiments and related skills to be developed :**

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Resistance and its types	<ul style="list-style-type: none"> <li>Testing &amp; identification of different types of resistors.</li> <li>Find out tolerance &amp; wattage of resistors depending on size.</li> <li>Measure the value of resistor using multimeter</li> </ul>	EIF101-1,2,3
2.	Potentiometer	<ul style="list-style-type: none"> <li>Test variation of resistance in linear, logarithmic potentiometer.</li> <li>Draw Graph of potentiometer.</li> </ul>	EIF101-1,2,3
3.	TDR ,LDR	<ul style="list-style-type: none"> <li>Testing of TDR ,LDR</li> </ul>	EIF101-1,2,3
4.	Capacitor and its types	<ul style="list-style-type: none"> <li>Identify different types of fixed capacitors.</li> <li>Test the values of capacitors using Colour coding and by printed values.</li> </ul>	EIF101-1,2,3
5.	Inductor and its types	<ul style="list-style-type: none"> <li>Identify the type of inductor.</li> <li>find out the value of inductance using color code</li> </ul>	EIF101-1,2,3
6.	Types of cables	<ul style="list-style-type: none"> <li>To identify different types of cables.</li> </ul>	EIF101-1,2,3
7.	Connectors and its types	<ul style="list-style-type: none"> <li>To identify different types of connectors.</li> </ul>	EIF101-1,2,3
8.	Switches and its Types	<ul style="list-style-type: none"> <li>To identify different types of switches,</li> <li>Test the switches.</li> <li>Measurement of contact resistance of switches.</li> </ul>	EIF101-1,2,3
9.	PCB Designing	<ul style="list-style-type: none"> <li>To prepare one electronic project on single sided PCB &amp; test it</li> </ul>	EIF101-4
10.	Visit	<ul style="list-style-type: none"> <li>Visit to any PCB manufacturing industry &amp; prepare visit report.</li> </ul>	EIF 101-4
11.	Mini Project(Compulsory)	<ul style="list-style-type: none"> <li>Group of 3 students will build a mini project as guided by teacher.</li> </ul>	EIF101-1,2,3,4

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

### a) Assessment Criteria for Term work :

#### i) Continuous Assessment of Practical Assignments :

Every practical assignment shall be assessed for 25 marks as per given criteria .

#### ii) Progressive Skill Test :

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

#### Criteria for Continuous Assessment of Practical work and Progressive skill Test :

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	10
2	Observations & Result Table	10
3	Sample Calculations with relevant Formulae.	10
4	Proper Graphs & Procedure / workmanship Safety measures	10
5	Oral Based on Term Work	10
	Total	50

#### Criteria for assessment at semester end oral exam :

Every student has to perform one practical within 2 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for oral	10
2	Correct figures / diagrams	10
3	Presentation	05
	Total	25

## INSTRUCTIONAL STRATEGIES :

### Instructional Methods :

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

### Teaching and Learning resources:

1. Chalk board    2. Video clips    3. Slides      4. Item Bank      5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr.No	Author	Title	Publisher
1	Dhir	Electronic Components and Materials	Tata McGraw Hill
2	Grover &Jamwal	Electronic Components and Materials	Dhanpat Rai & Sons,
3	Walter C.Bosshart	Printed Circuit Boards	Tata McGraw Hill
4	Madhuri Joshi	Electronic Components and Materials	Shroff Publishers & Distributors private ltd.
5	Williams	Build your own printed circuit board with CD	Tata McGraw-Hill
6	Thomas H.Jones	Electronic Components Handbook	Reston Publishing Company
7	Harper (Charles A.)	Handbook of components for electronics	Laxmi Enterprises ,Bombay

**b) Websites:**

- 1) <http://www.electronica-india.com/>
- 2) <http://electronicsclub.info/>
- 3) <http://nptel.ac.i>

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**COURSE ID :**

**Course Name : ENGINEERING PHYSICS (EE/IE/IF/ET)**  
**Course Code : CCF102**  
**Course Abbreviation : FPHB**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical iv. One PST of 25 marks	One paper (3 hour)	Practical (3 hours)	--	
<b>Marks</b>	20	--	80	50 I	--	150

\* Assessment as per pro-forma II

I – Internal Examination

**RATIONALE :**

Physics is the foundation of engineering and technology. The development of all engineering areas requires good understanding of fundamental principles in physics. Studying physics develops scientific methodology and technical aptitude in the students. Applications of principles of physics in engineering fields create interest and motivate the students.

**COMPETENCY :**

Apply principles of Physics to solve engineering problems as follows :

**Cognitive :** i) Understanding and applying principles and laws of Physics to simple practical problems/ situations. ii) Observing iii) Classifying iv) Interpreting

**Psychomotor :** Handling of instruments, apparatus and tools

**Affective :** Skill of i) working in team ii) curiosity, interest and self-confidence

**COURSE OUTCOMES :**

**CCF102-1** Select proper material in engineering industry by analysis of its physical properties

**CCF102-2** Use basic principles of wave motion for related engineering applications

**CCF102-3** Use nanotechnology for quality improvement of materials

**CCF102-4** Apply principles of optics, electricity to solve engineering problems

**CCF102-5** Use LASERs, X-rays and photocell based equipments

**CCF102-6** Apply principles of fiber optics for related engineering applications

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs										PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency:</b> Apply principles of Physics to solve engineering problems.	3	-	2	-	-	-	-	-	-	-	1	1
<b>CCF102-1</b> Select proper material in engineering industry by analysis of its physical properties	3	-	2	-	-	-	-	-	-	-	1	1
<b>CCF102-2</b> Use basic principles of wave motion for related engineering applications	3	-	2	-	-	-	-	-	-	-	1	1
<b>CCF102-3</b> Use nanotechnology for quality improvement of materials	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF102-4</b> Apply principles of optics, electricity to solve engineering problems	3	-	3	-	-	-	-	-	-	-	1	1
<b>CCF102-5</b> Use LASERs, X-rays and photocell based equipments	3	1	2	-	-	-	-	-	-	-	1	1
<b>CCF102-6</b> Apply principles of fiber optics for related engineering applications	3	1	2	-	-	-	-	-	-	-	1	1



<b><i>CCF102-2 Use basic principles of wave motion for related engineering applications</i></b>			
<b>3</b>	<b>WAVE MOTION</b> 3.1 Definitions of periodic motion, Linear S. H. M. 3.2 Parameters of linear SHM : Amplitudes, Period, Frequency and Phase 3.3 Characteristics of linear SHM 3.4 Concept and definition of wave 3.5 Parameters of wave- Frequency, periodic time, phase and wavelength 3.6 Types of waves (transverse and longitudinal) and their characteristics 3.7 Free and forced oscillations 3.8 Phenomenon of resonance and its applications <b>No numericals on above topic</b>	<b>06</b>	<b>08</b>
<b><i>CCF102-3 Use nanotechnology for quality improvement of materials</i></b>			
<b>4</b>	<b>INTRODUCTION TO NANOTECHNOLOGY</b> 4.1 Definition of nanoscale, nanometer, nanoparticle 4.2 Definition and examples of nanostructured materials 4.3 Applications of nanotechnology in electronics, automobile, textile, space, medicine, cosmetics and environment <b>No numericals on above topic</b>	<b>04</b>	<b>06</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

[illegible]

Sr. No.	Topics / Subtopics	Lectures (Hours)	Theory Evaluation (Marks)
<b>CCF102-6 Apply principles of fiber optics for related engineering applications</b>			
<b>8</b>	<b>FIBER OPTICS</b> 8.1 Optical communication link 8.2 Principle of optical fiber (TIR) 8.3 Structure of optical fiber 8.4 Propagation of light in optical fiber 8.5 Advantages of optical fibers over conventional metal conductors 8.6 Applications of optical fibers <b>No numericals on above topic</b>	<b>06</b>	<b>08</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Section / Topic no.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total marks
		Remember	Understand	Application		
I/1	Elasticity	2	4	2	CCF102-1	08
I/2	Properties of liquids	10	6	2	CCF102-1	18
I/3	Wave motion	4	2	2	CCF102-2	08
I/4	Nanotechnology	2	4	-	CCF102-3	06
II/5	Properties of light	2	2	2	CCF102-4	06
II/6	Electricity	2	2	4	CCF102-4	08
II/7	Modern Physics	8	8	2	CCF102-5	18
II/8	Fiber Optics	2	4	2	CCF102-6	08
	Total	32	32	16		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK** Term work shall consist of the following :

**Laboratory experiments and related skills to be developed :**

Sr. No.	Title of Experiment	Skills to be developed	Course Outcome
1	Overview of Field Applications of Physics	i) Information search ii) Information presentation	CCF102-1 To CCF102-6
<b>( Any 10 of the following experiments )</b>			
2	To measure dimensions of given objects by using Vernier Caliper	i) Determine least count and zero error in the measuring instrument. ii) Measuring internal and external dimensions of given objects iii) Handling the measuring instruments for measuring depth, thickness etc. iv) Tabulating observations.	CCF102-1
3	To measure the diameter of bob and thickness of plate by using Micrometer screw gauge	i) Determine least count and zero error in the measuring instrument. ii) Measuring dimensions of given objects iii) Handling the measuring instruments for measuring depth, thickness etc. iv) Tabulating observations.	CCF102-1
4	To determine the viscosity of liquid by Stokes method.	i) Measuring diameter of steel ball using micrometer screw gauge. ii) Measuring terminal velocity of steel ball in the liquid column. iii) Use of stop watch for measurement of time. iv) Tabulating observations.	CCF102-1
5	To determine the surface tension of liquid by capillary rise method	i) Focusing the microscope properly in order to get clear image. ii) Adjusting cross wires of microscope at particular place. iii) Taking readings for main scale and Vernier scale of traveling microscope. iv) Tabulating observations.	CCF102-1
6	To measure unknown resistance of wire by Ammeter – Voltmeter method.	i) Drawing the circuit diagram of the required experiment. ii) Connecting the instruments as per circuit diagram. iii) Measuring the value of potential difference & current in the circuit. iv) Tabulating observations.	CCF102-4
7	To verify Snell's law using glass slab	i) Drawing necessary ray diagram ii) Measuring angles of incidence and refraction iii) Tabulating observations.	CCF102-4
8	To determine refractive index of prism by pin method	i) Removing parallax between the images and pins by observing the refracted ray through prism. ii) Measuring the angle of refraction correctly iii) Drawing the path of refracted ray through prism iv) Drawing inference regarding relation between	CCF102-4

		angle of incidence & angle of refraction from $\delta$ graph v) Tabulating observations.	
9	To determine velocity of sound by resonance tube	i) Adjusting the resonating length by discriminating resonating sound from sound produced by the tuning fork. ii) Measuring internal diameter of resonating tube using vernier caliper iii) Drawing inference & confirming Law $n\lambda = \text{constant}$ iv) Tabulating observations.	CCF102-2
10	To study characteristics of photocell	i) Drawing circuit diagram ii) Handling different delicate instruments. iii) Tabulating observations iv) Drawing graph	CCF102-5
11	To determine the acceleration due to gravity by 'g' by simple pendulum	i) Measuring length of pendulum ii) Finding least count of stopwatch iii) Measuring periodic time with the help of stop watch iv) Tabulating observations.	CCF102-2
12	To measure unknown resistance by Wheatstone's meter bridge.	i) Drawing the circuit diagram for series connections of the resistances. ii) Connecting the resistances for series method as per circuit diagram. iii) Finding the correct position of null point & measuring correct balancing lengths on Meter bridge. iv) Tabulating observations.	CCF102-4

### C) INDUSTRIAL EXPOSURE

Sr. No.	Mode of Exposure (Visit/Exp.Lect/Ind.Survey/...)	Topic
1.	Field applications in theory lectures in every topic	All topics in course syllabus
2.	Practical exercise on overview of field applications of Physics	Part of term work

Report of each Industrial Exposure Activity shall be submitted by student as a part of term work for evaluation.

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### b) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments :

Every practical assignment shall be assessed for 25 marks as per given criteria .

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Observations, calculations & Result table	05
Psychomotor	Operating Skills	05
	Neat & complete circuit Diagram / schematic Diagram.	05



Affective	Discipline and punctuality Decency and presentation	5
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test :**

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

**Criteria for Continuous Assessment of Practical work and Progressive skill Test :**

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

**c) Criteria for assessment at semester end PRACTICLE exam :**

Every student has to perform one practical within 2 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions    2. Regular Home Assignments.    3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

<b>Sr. o.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	B.G. Bhandarkar	Applied Physics	Nirali publications
2.	Manikpure – Deshpande	Applied Physics	S. Chand publications
3	Narkhede, Pawar, Sutar	Applied Science	Nirali publications
4	Shelake, Shinde, Adwankar	Applied Science	Vision publications
5	B.L. Theraja	Engineering Physics	S. Chand Publishers – New Delhi
6	Beiser	Concept of modern physics	Tata Mc-Graw Hill
7	E. Zebro Wski	Physics for Technicians	Tata Mc-Graw Hill
8	V. Rajendran	Engineering Physics	Tata McGraw-Hill Publications

**b) Websites**

- i) <http://www.physicsclassroom.com>
- ii) <http://scienceworld.wolfram.com/physics/>
- iii) <http://physics.about.com/>

\* \* \*

**COURSE ID:**

**Course Name : BASIC ELECTRONICS**  
**Course Code : EIF 103**  
**Course Abbreviation : FBTX**

**TEACHING AND EVALUATION SCHEME:**

**Prerequisites : NIL**

**Teaching Scheme:**

Scheme Component	Hours/week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Practical (3 hours)	--	
<b>Marks</b>	20	--	80	50 I	--	150

I\* Assessment as per pro-forma II

I – Internal Examination

**RATIONALE:**

Although industrial electronics is specialized field of electronics engineering, a grasp of certain fundamental principles and concepts are essential pre- requisitions for it. This subject deals with the most basic devices and circuits on which the further development of subject depends.

**COMPETENCY:**

Understand fundamental principle and concept of basic electronic devices.

**Cognitive :** Understand symbol, characteristics of diode and transistor.

**Psychomotor :** Build and operate simple basic electronics circuit.

**Affective :** Attitude of i) Identify ii) Draw iii) Operate v)Test

### COURSE OUTCOMES:

- EIF 103-1** Differentiate conductor insulator and semiconductor in engineering industry.  
**EIF 103-2** Identify and operate rectifier and filter for engineering applications  
**EIF 103-3** Analyze different configuration and biasing methods of BJT to solve engineering problem  
**EIF 103-4** Draw frequency response of BJT amplifier for different engineering applications  
**EIF 103-5** Classify and operate FET in engineering industry.  
**EIF 103-6** Illustrate different regulated power supplies to solve engineering problem.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Understand fundamental principle and concept of basic electronic devices	-	3	3	-	-	-	-	1	-	-	1	1
<b>EIF103-1</b>	2	-	-	-	-	-	-	1	-	-	1	1
<b>EIF103-2</b>	1	2	2	-	-	-	-	1	-	-	1	1
<b>EIF103-3</b>	1	2	2	-	-	-	-	1	-	-	1	1
<b>EIF103-4</b>	1	1	2	-	-	-	-	1	-	-	1	1
<b>EIF103-5</b>	1	1	2	-	-	-	-	1	-	-	1	1
<b>EIF103-6</b>	-	3	3	-	-	-	-	1	-	-	1	1

### SECTION-I

Sr. No.	Topics	Teaching hours	Marks
<b><i>EIF103-1 Differentiate conductor insulator and semiconductor.</i></b>			
<b>1.</b>	<b>Semiconductor Diode</b> 1.0 Conductor , Insulator, semiconductor 1.0.1 Band theory 1.0.2 Intrinsic semiconductor : Si , Ge 1.0.3 Doping 1.0.4 Extrinsic semiconductor : P type , N type 1.1 P.N. junction diode – Ge & Si 1.1.1 Constructional features. 1.1.2 Operating principle. 1.1.3 Characteristics. 1.1.4 Applications. 1.1.5 Specifications. 1.2 Zener diode 1.2.1 Constructional features. 1.2.2 Operating principles. 1.2.3 Characteristics 1.2.4 Specifications. 1.2.5 Applications	<b>10</b>	<b>14</b>
<b><i>EIF103-2 Identify and operate rectifier and filter.</i></b>			
<b>2.</b>	<b>Rectifier, Filter</b> 2.0 Half wave rectifier - working, Ripple factor, Efficiency Consideration. 2.1 Full wave rectifier - classification, working ripple factor, efficiency consideration. comparison of all rectifier. 2.2 Filter - study of shunt capacitor, series inductor, LC, Filter., CLC filter.	<b>10</b>	<b>12</b>
<b><i>EIF103-3 Analyze different configuration and biasing methods of BJT</i></b>			
<b>3.</b>	<b>Bipolar Junction Transistor(BJT)</b> 3.0 Introduction. 3.1 Constructional features. 3.2 Operating principles of NPN & PNP Transistor 3.3 Transistor configurations & Modes of operation 3.4 Transistor input & output characteristic of CE & CB configuration. 3.5 Specifications of transistor and relation between $\alpha$ & $\beta$ 3.6 Switching action of transistor (NUMERICALS BASED ON RELATION OF $I_C$ , $I_E$ & $I_B$ )	<b>12</b>	<b>14</b>

### SECTION II

Sr. No.	Topics	Teaching hours	Marks
<b><i>EIF103-4 Draw frequency response of BJT amplifier.</i></b>			
<b>4.</b>	<b>Biasing of transistor and Single amplifier</b> 4.1 Q Point & stability 4.2 DC & AC Load Line 4.3 Transistor Biasing Methods-Circuit ,Equations, advantages & disadvantages Of 4.3.1 Fixed Bias Circuit 4.3.2 Fixed Bias with Emitter Resistor 4.3.3 Collector to Base Bias Circuit 4.3.4 Voltage Divider Bias Circuit 4.4 Study of single stage amplifier. 4.4.1 Diagram 4.4.2 Working 4.4.3 Input Output Waveform	<b>12</b>	<b>14</b>
<b><i>EIF103-5 Classify and operate FET</i></b>			
<b>5</b>	<b>Field Effect Transistor (FET)</b> 5.0 Classification of FET 5.1 Study of JFET 5.1.1 Construction details. 5.1.2 Working principle 5.1.3 Characteristics 5.1.4 JFET parameters and relation between $\mu_{rd}$ & $g_m$ 5.2 Comparison between JFET and BJT 5.3 Study of MOSFET:-Types,symbol,working principle, applications ○ Specifications of FET	<b>12</b>	<b>14</b>
<b><i>EIF103-6 Illustrate different regulated power supplies</i></b>			
<b>6.</b>	<b>Regulated Power Supply</b> 6.1 Block diagram of Regulated power supply. 6.2 Zener diode as a voltage regulator, Emitter follower regulator. 6.3 Transistor Series Voltage Regulator 6.4 Transistor Shunt Voltage Regulator 6.5 Study of IC 78xx & IC 79xx series of voltage regulators 6.6. Study of a LM317 and IC 723 internal block diagram and pinout features.	<b>08</b>	<b>12</b>
	<b>Total</b>	<b>64</b>	<b>80</b>

**NOTE→** For setting of question paper, marks per topic should be 1.5 times the allotted Marks

**Specification Table for Question Paper of Theory Examination:**

SR. NO.	Name of the Topic	Distribution Of Marks (level wise)			Course Outcome	Total Marks
		Knowledge	Comprehension	Applications		
1	Semiconductor diode	08	02	02	EIF103-1	12
2	Rectifier, filter	08	04	02	EIF103-2	14
3	Bipolar junction Transistor(BJT)	10	02	02	EIF103-3	14
4	Single & Multistage Amplifier	12	02	-----	EIF103-4	14
5	Field effect Transistor.(FET)	12	-----	02	EIF103-5	14
6	Regulated Power supply	08	02	02	EIF103-6	12
				<b>TOTAL</b>		<b>80</b>

**B)TERMWORK :** Term work shall consist of the following :

**(Minimum Eight From List)**

**Laboratory experiences and related skills developed**

Sr.no	Laboratory experiments	Skills developed	Course Outcome
1	Characteristics of semiconductor junction diode.	1) Connect the various component as per the circuit diagrams using proper size wires 2) Write the forward & reverse voltage & current 3) Draw the forward & reverse characteristics	EIF103-1
2	Characteristics of Zener diode.	1) Connect the various component as per the circuit diagrams using proper size wires 2) Write the forward & reverse voltage & current 3) Draw the forward & reverse characteristics	EIF103-1
3	Zener diode as voltage regulator and regulation characteristics	1) Connect the various component as per the circuit diagrams using proper size wires 2) Write the percentage of regulation	EIF103-1
4	Input, Output characteristics of common base configuration.	1) Write input & output voltage & current 2) Make proper connection as per circuit diagram 3) Draw the input & output characteristics	EIF103-3
5	Input, output characteristics of common emitter configuration.	1) Write input & output voltage & current 2) Make proper connection as per circuit diagram	EIF103-3

		3) Draw the input & output characteristics	
<b>6</b>	Waveform observation, Vdc, ripple calculation, of half wave rectifier.	1) Make proper connection as per circuit diagram 2) Write Vdc & ripple voltage 3) To show the waveform on C.R.O.	EIF103-2
<b>7</b>	Waveform observation, Vdc, ripple calculation of centre - tapped full wave rectifier	1) Make proper connection as per circuit diagram 2) Write Vdc & ripple voltage 3) To show the waveform on C.R.O.	EIF103-2
<b>8</b>	Waveform observation, Vdc, ripple calculation of Bridge - full wave rectifier	1) Make proper connection as per circuit diagram 2) Write Vdc & ripple voltage 3) To show the waveform on C.R.O.	EIF103-2
<b>9</b>	To Study RC coupled Amplifier	1) To observe the frequency response of RC coupled amplifier.	EIF103-4
<b>10</b>	Static characteristics of FET	1) Make proper connection as per circuit diagram 2) Measure drain voltage & current and also measure Vgs & Vds draw characteristics	EIF103-5
<b>11</b>	Mini Project(Compulsory)	Build any circuit mounted on general PCB in a group of 3 students	EIF103-1,2,3,4,5.6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### d) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments :

Every practical assignment shall be assessed for 25 marks as per given criteria .

##### ii) Progressive Skill Test :

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

#### Criteria for Continuous Assessment of Practical work and Progressive skill Test :

Particulars	Marks
1. Attendance	05
2. Correct figures/circuit diagrams/drawings	04
3. Proper observations and result table	04
4. Sample calculations with relevant formulae	04
5. Proper graphs and phasor diagrams	04
6. Procedure/workmanship/safety	04
<b>Total</b>	<b>25</b>



**e) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Correct figures / diagrams/ Flow chart	20
2	Result table / calculations / graphs	20
3	Safety / use of proper tools / workmanship	10
	Total	50

\*Assessment at semester end practical exam as per Pro-forma II.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides      4. Item Bank      5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	A. Motershed	Electronics Devices & Circuits	PHI Publication
2.	Malvino	Electronics Principles	McGraw Hill
3	V. K. Mehta	Principles of Electronics	S.Chand
4	G. K. Mithal	Applied Electronics	Khanna Publication
5	B. L. Theraja	Basic Electronics	S.Chand

**b) Websites:**

- 1) [www.allaboutcircuits.com](http://www.allaboutcircuits.com)  
2) [www.electronicstheory.com](http://www.electronicstheory.com)  
3) [www.electronicstutorial.com](http://www.electronicstutorial.com)

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**COURSE ID :**

**Course Name : CHEMISTRY OF ENGINEERING MATERIALS**  
**Course Code : CCF104**  
**Course Abbreviation : FCHB**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : <nil>**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Practical (2 hours)	--	
<b>Marks</b>	20	--	80	50 I	--	150

**\* Assessment as per pro-forma II.**

**RATIONALE:**

This course provides knowledge of chemical properties of materials and selection of appropriate material for specific applications in the field of engineering. Study of different polymers, insulators or dielectrics, adhesives and their applications in electrical appliances, electronic industries etc., study of corrosion and methods of prevention will make students realize importance of care and maintenance of machines and equipments. The contents of this subject are designed to enhance student's reasoning capacity and capabilities in solving challenging problems in engineering field.

**COMPETENCY:**

Apply principles of advanced chemistry to solve engineering problems.

**Cognitive:** Understanding concepts of chemistry for applications in the area of engineering.

**Psychomotor:**

- i) Sketching and labeling the diagrams for extraction of copper
- ii) Experimentally analyzing the water samples for preparing potable water by different methods.
- iii) Preparing chart of showing percentage, composition, properties and industrial applications of solders.

**Affective:** Attitude of i) Accuracy ii) Safety iii) Punctuality

**COURSE OUTCOMES:**

- CCF104-1** Apply the basic principles of chemistry in Engineering field.
- CCF104-2** Use electrochemistry for electroplating and electro-refining as industrial applications.
- CCF104-3** Interpret the reasons of corrosion suggesting remedies using appropriate techniques.
- CCF104-4** Use relevant water treatment process to solve industry problems.
- CCF104-5** Select proper type of cell based on the requirement in electrical/ electronic and computer engineering.
- CCF104-6** Assist in monitoring extraction of copper.
- CCF104-7** Select insulators, polymer, adhesives, composite materials for different applications in electronics engineering.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
<b>Competency:</b> Apply principles of advanced chemistry to solve engineering problems	3	-	3	-	-	-	-	-	-	-	1	1
<b>1</b> Apply the basic principles of chemistry in Engineering field.	3	-	3	-	-	-	-	-	-	-	-	-
<b>CCFI04-2</b> Use electrochemistry for electroplating and electro-refining as industrial applications.	3	-	3	-	-	-	-	-	-	-	-	-
<b>CCFI04-3</b> Interpret the reasons of corrosion suggesting remedies using appropriate techniques.	3	-	3	-	-	-	-	-	-	-	-	-
<b>CCFI04-4</b> Use relevant water treatment process to solve industry problems	3	-	3	-	-	-	-	2	-	-	-	-
<b>CCFI04-5</b> Select proper type of cell based on the requirement in electronic and computer engineering	3	-	3	-	-	-	-	1	-	-	-	-
<b>CCFI04-6</b> Assist in monitoring extraction of copper.	3	-	3	-	-	-	-	3	-	1	-	-
<b>CCFI04-7</b> Select insulators, polymer, adhesives, composite materials for different applications in electronics engineering.	3	-	2	-	-	-	-	2	-	1	1	1

**CONTENT:**

**A. THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>CCF104-1 Apply the basic principles of chemistry in Engineering field.</i></b>			
<b>1</b>	<b>ATOMIC STRUCTURE</b> 1.1 Atom :Fundamental particles 1.2 Atomic Number, Mass Number, Atomic weight, Isotopes and isobars 1.3 Hund's rule of maximum multiplicity 1.4 Pauli's exclusion principle 1.5 Aufbau's principle 1.6 Rules of distribution of planetary electrons 1.7 Electronic configuration of atoms with atomic number 1-30 1.8 Electronic configuration of Inert gases and their characteristics 1.9 Lewis and Langmuir's concept of stable electronic configuration 1.10 Electovalency and Co-valency 1.11 Formation Of electrovalent compounds- NaCl, MgO 1.12 Formation of Covalent compounds-H <sub>2</sub> O,CO <sub>2</sub>	<b>07</b>	<b>08</b>
<b><i>CCF104-2 Use electrochemistry for electroplating and electro-refining as industrial applications.</i></b>			
<b>2</b>	<b>ELECTROCHEMISTRY</b> 2.1 Definitions- Conductor, Electrolyte,Electrode 2.2 Difference between metallic conduction and electrolytic conduction 2.3 Distinguish between Atom & Ion 2.4 Arrhenius Theory Of Ionisation 2.5 Degree of Ionisation & Factors affecting degree of ionisation 2.6 Electrolysis of CuSO <sub>4</sub> solution by using a) Pt electrodes b) Cu-electrodes 2.7 Industrial applications of electrolysis 2.7.1 Electroplating	<b>07</b>	<b>08</b>

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
	2.7.2 Electro refining of Cu 2.8 Faraday's Laws of Electrolysis 2.9 Numerical problems based on Faraday's laws		
<b>CCF104-3 Interpret the reasons of corrosion suggesting remedies using appropriate techniques.</b>			
<b>3.</b>	<b>CORROSION AND PROTECTIVE COATING</b> 3.1 Introduction 3.2 Definition 3.3 Types of corrosion 3.4 Dry or Atmospheric corrosion 3.4.1 Oxide Film Formation & its types 3.4.2 Factors affecting atmospheric corrosion 3.5 Wet or electrochemical corrosion 3.5.1 Galvanic Corrosion 3.5.2 Factors influencing immersed corrosion 3.5.3 Nature of the Metal 3.5.4 Nature of Corroding environment 3.5.5 $P^H$ value 3.6 Methods of protection of metal from corrosion 3.6.1. Hot dipping (Galvanizing & Tinning). 3.6.2 Metal spraying. 3.6.3 Metal cladding. 3.6.4 Cementation or Sherardizing	<b>07</b>	<b>08</b>
<b>CCF104-4 Use relevant water treatment process to solve industry problems.</b>			
<b>4</b>	<b>WATER</b> 4.1 Impurities in natural water 4.2 Hard water & Soft water 4.3 Hardness of water- Temporary & Permanent, its units 4.4 Reactions of hard water with soap 4.5 Disadvantages of hard water for domestic purpose 4.6 Disadvantages of hard water for Industrial purpose 4.6.1 Textile Industry 4.6.2 Sugar Industry 4.6.3 Paper Industry 4.6.4 Dying Industry	<b>11</b>	<b>16</b>

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
	4.7 scale & Sludge formation 4.8 Causes of scale formation in boilers , it's disadvantages & removal of scale 4.9 Sterilization of water 4.9.1 Chlorination –by $\text{Cl}_2$ , bleaching powder, chloramine with chemical reactions 5.0 Ion Exchange method to remove total hardness of water 5.1 pH definition, pH scale , applications of pH in boiler, sugar industry & sewage		
	<b>TOTAL:</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



**Section II**

Sr. No .	Topics / Subtopics	Lectures (Hours)	Theory Evaluation (Marks)
<b>CCF104-5 Select proper type of cell based on the requirement in electrical/ electronic and computer engineering.</b>			
<b>5</b>	<b>CELL AND BATTERIES</b> 5.1 Definition of Electrochemical cell, Battery, Charge, Discharge, Closed Circuit Voltage, Electrochemical couple, Internal resistance, Open Circuit Voltage, Separator, E.M.F. 5.2 Classification of Batteries such as – Primary, Secondary and Reserve Batteries 5.3 Construction, Working and Applications of a Primary Cell such as Dry Cell , Secondary Cell such as Lead Acid Storage Cell 5.4 Charging and Discharging of Lead Acid Storage Cell 5.5 Hydrogen-Oxygen fuel cell, its chemical reactions & advantages 5.6 Introduction of solar cell	08	08
<b>CCF104-6 Assist in monitoring extraction of copper.</b>			
<b>6</b>	<b>METALLIC CONDUCTORS</b> 6.. 1 Occurrence of metals 6.2 Distinction between mineral & ore 6.3 Definition of flux, Gangue & Slag 6.4 Steps involved in metallurgy-Flow chart 6.4.1 Concentration of ores—Physical Methods 6.4.2 Gravity Separation Method 6.4.3 Electromagnetic separation 6.4.4 Froth floatation method 6.5 Chemical Methods 6.5.1 Calcination & Roasting . 6.6 Important ores of copper Metallurgy of copper-Extraction of copper from copper pyrites by concentration , roasting, smelting , Bessemerisation ,Electrorefining 6.7 Physical chemical properties (action of air ,water & acids) 6.8. Uses of Copper	12	14
<b>7</b>	<b>SOLDERS</b> 7.1 Definition of alloy , classification of alloys & purposes of making alloy	03	06

	7.2 Composition, properties & applications of 7.2.1 Soft solder. 7.2.2 Tinmann's solder, 7.2.3 Brazing alloy , 7.2.4 Plumber's solder 7.2.5 Rose metal .		
<b>8</b>	<b>SEMICONDUCTORS</b> 8.1 Definition of semiconductor 8.2 Properties & Applications of Semiconductors such as 8.2.1 Silicon 8.2.2 Germanium 8.2.3 Selenium 8.2.4 Graphite 8.2.5 Silicon carbide 8.2.6 Cadmium sulphide	<b>02</b>	<b>04</b>
<b><i>CCF104-7 Select insulators, polymer, adhesives, composite materials for different applications in electronics engineering.</i></b>			
<b>9</b>	<b>CHEMISTRY OF NONMETALLIC ENGINEERING MATERIALS</b> <b>INSULATORS</b> 9.1.1 Definition of insulator, Dielectrics 9.1.2 Characteristics of good insulator 9.1.3 Classification of insulating materials-solid , liquid , gases 9.1.4 Preparation, properties & uses of glass wool, Thermocole 9.1.5 Properties & uses of Asbestos , Ceramics , glass, mica <b>9.2 POLYMERS</b> 9.2.1 Definition of Polymer , Polymerization , types of polymerisation 9.2.2 Preparation , properties & uses of Teflon & Epoxy resin <b>9.3 ADHESIVES</b> 9.3.1 Definition of Adhesives 9.3.2 Characteristics of good Adhesives 9.3.3 Properties & uses of Adhesives. <b>9.4 COMPOSITE MATERIALS</b> 9.4.1 Introduction 9.4.2 Definition 9.4.3 Classification 9.4.4 Properties 9.4.5 Application	<b>07</b>	<b>08</b>
	<b>TOTAL</b>	<b>32</b>	<b>40</b>

**Specification table for setting question paper for semester end theory examination :**

Section / Topic no.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total marks
		Remember	Understand	Application		
I / 1	Atomic structure	06	02	-	CCF104-1	08
I / 2	Electrochemistry	02	02	04	CCF104-2	08
I / 3	Corrosion & protective coating	04	02	02	CCF104-3	08
I / 4	Water	08	04	04	CCF104-4	16
II/5	Cell & Batteries	04	02	02	CCF104-5	08
II/6	Metallic conductors	08	03	03	CCF104-6	14
II/7	Solders	02	02	02	CCF104-6	06
II/8	Semiconductors	02	02	-	CCF104-6	04
II/9	Chemistry of nonmetallic engg. materials	02	02	04	CCF104-7	08
	Total					80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B. TERM WORK** Term work shall consist of the following :

**Laboratory experiments and related skills to be developed :**

Sr. No.	Title of Experiment	Skills/Competencies to be developed	Course Outcome
1	Introduction to Chemistry laboratory	Awareness of chemicals glasswares & instruments used in chemistry laboratory	CCF104-1
2	Preparation of 1 N, 0.5 N & 0.1 N Solutions of different chemicals like NaOH, HCl, Oxalic acid, FeSO <sub>4</sub> , etc.	Skill of weighing, handling Glassware & measuring solutions	CCF104-1
3	Titration of strong acid and strong bases ( HCl X NaOH)	Skills of determining accurate end point of titration & development of measurement skills.	CCF104-1
4	Titration of strong acid, strong base & weak acid (HCl X NaOH X H <sub>2</sub> C <sub>2</sub> O <sub>4</sub> .H <sub>2</sub> O)	Skills of determining accurate end point of titration & development of measurement skills.	CCF104-1

5	Titration of weak base , strong acid & strong base (Na <sub>2</sub> CO <sub>3</sub> X H <sub>2</sub> SO <sub>4</sub> X KOH	Skills of determining accurate end point of titration & development of measurement skills.	CCF104-1
6	Estimation of chloride content in water by Mohr's method	Measurement skill utilization of practical data for testing & estimation	CCF104-4
7	Determination of amount of Ca and Mg ions present in given sample of water by E.D.T.A method	Measurement skill utilization of practical data for testing & estimation	CCF104-4
8	Estimation of viscosity of oils by Ostwald's method	Measurement skill utilization of practical data for testing & estimation	CCF104-1
9	Estimation of Ca in limestone.	Measurement skill utilization of practical data for testing & estimation	CCF104-6
10	Estimation of % of Fe in given sample of steel	Measurement skill utilization of practical data for testing & estimation	CCF104-6
11	Report of expert lectures demo	Application of chemistry in engineering field	CCF104-6
	Report of market survey	Collection of data	CCF104-4

#### Criteria for Continuous Assessment of Practical work

Every practical assignment shall be assessed for 50 marks as per following criteria :

Domain	Particulars	Marks out of 50
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	10
	Writing skills	10
Affective	Discipline and punctuality	10
	Timeliness and accuracy	10
<b>TOTAL</b>		<b>50</b>

#### Progressive Skill Test:

One mid-term *Progressive Skill Test* of 25 marks as per following criteria.

Domain	Particulars	Marks out of 50
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Writing skills	05
Affective	Discipline and punctuality	05
	Timeliness and accuracy	05
<b>TOTAL</b>		<b>25</b>

Final marks of term work shall be awarded as per *Assessment Pro-forma II*.

**C) INDUSTRIAL EXPOSURE :**

(Included in *Laboratory Manual for Applied Mechanics*)

SN	Mode of Exposure	Topic
1.	Lecture demos by industry experts	Chapter of theory syllabus
2.	Market survey of apparatus and chemicals	Term work assignment

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Demonstrations
2. Classroom practices
3. Home Assignments

**Teaching and Learning resources :**

1. Chalk board
2. LCD presentations
3. Audio presentations
4. Item Bank

**REFERENCE MATERIAL :a) Books / IS Codes**

Sr. No.	Author	Title	Publisher
1.	Jain & Jain	Engineering chemistry	Dhanpatrai publishing co.
2.	S. C. Rangawala	Engineering materials	Engineerin publication
3.	Jain & Agarwal	Metallurgical Analysis	Agarwal publications
4.	O. P. Khanna	Material science & technology	Khanna publication on 2006
5.	Rollason	Metallurgy for Engineers	ASM publication
6.	J. C. Kuriacose	Chemistry in Engineering & Vol. 1 & 11	
7.	P. C. Jain	Chemistry of Engineering Materials	
8	S. S. Dara	A text of Engineering Chemistry	

**b) Websites**

- i) [www.substech.com](http://www.substech.com)
- ii) [www.kentchemistry.com](http://www.kentchemistry.com)
- iii) [www.chemcollective.org](http://www.chemcollective.org)
- iv) [www.wqa.org](http://www.wqa.org)
- v) [www.chemistryteaching.com](http://www.chemistryteaching.com)

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**COURSE ID :**

**Course Name : BASIC MATHEMATICS**  
**Course Code : CCF105**  
**Course Abbreviation : FBM T**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : < nil >**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	04
Practical	01	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	<b>NIL</b>	--	
<b>Marks</b>	20	--	80	<b>NIL</b>	--	150

**RATIONALE:**

Mathematics is an important prerequisite for the development and understanding of engineering and technological concepts. For an engineer and technologist, knowledge of mathematics is an effective tool to pursue and master the applications in the engineering and technological fields. Algebra provides the language and abstract symbols of mathematics. The topics Matrices and Determinants are helpful for finding optimum solution of system of simultaneous equations which are formed in the various branches of engineering using different parameters . Trigonometry is the study of triangles and angles. Contents of this subject will form foundation for further study in mathematics.

**Competency:**

Apply principles of Basic Mathematics to solve mathematical problems as follows -

- 1.Cognitive:** To understand the mathematical concepts
- 2. Psychomotor:** Proper handling of scientific calculator
- 3. Affective :** Attitude of accuracy, punctuality, proper reasoning and presentation

**Course Outcomes(CO's) :**

**CCF105-1 :** To solve simultaneous equations using Cramer's rule.

**CCF105-2 :** To resolve a given function into partial fractions.

**CCF105-3 :** To solve simultaneous equations by using inverse of matrix method.

**CCF105-4 :** To expand any binomial expression for positive integral index.

**CCF105-5 :** To memorize and solve problems using trigonometric formulae.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
<b>Competency:</b> Apply principles of Basic Mathematics to solve mathematical problems	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF105-1 :</b> To solve simultaneous equations using Cramer's rule.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF105-2 :</b> To resolve a given function into partial fractions.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF105-3 :</b> To solve simultaneous equations by using inverse of matrix method.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF105-4 :</b> To expand any binomial expression for positive integral index.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF105-5 :</b> To memorize and solve problems using trigonometric formulae.	3	-	1	-	-	-	-	-	-	-	1	1



**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<b><i>CCF105-1 : To solve simultaneous equations using Cramer's rule</i></b>			
<b>1</b>	<b>Determinants</b> 1.1 Definition of nth order determinant 1.2 Expansion of second and third order determinants 1.3 To solve simultaneous equations having 3 unknowns using Crammer's Rule 1.4 Consistency of equations using Determinants	<b>04</b>	<b>06</b>
<b><i>CCF105-2 : To resolve a given function into partial fractions</i></b>			
<b>2</b>	<b>Partial Fractions</b> 2.1 Definition of rational, proper and improper fractions 2.2 Various cases of Partial fractions and Examples	<b>06</b>	<b>12</b>
<b><i>CCF105-3 : To solve simultaneous equations by using inverse of matrix method</i></b>			
<b>3</b>	<b>Matrices</b> 3.1 Definition of a matrix, Types of matrices 3.2 Algebra of matrices 3.3 Equality of two matrices, Transpose of a matrix 3.4 Minor and Co-factor of an element of a matrix 3.5 Adjoint and Inverse of a matrix 3.6 Solution of simultaneous equations by Inverse of a matrix method	<b>10</b>	<b>16</b>
<b><i>CCF105-4 : To expand any binomial expression for positive integral index.</i></b>			
<b>4</b>	<b>Binomial Theorem</b> 4.1 Statement of theorem for positive integral power 4.2 Expansion 4.3 Simple Examples on expansion	<b>04</b>	<b>06</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
<p>1.Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.</p> <p>2.In each topic, corresponding applications will be explained</p>			

### Section II

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<i>CCF105-5 : To memorize and solve problems using trigonometric formulae.</i>			
<b>5</b>	<b>Trigonometric Ratios and Identities</b> 5.1 Fundamental Identities(Simple examples) 5.2 Definition of radian measure 5.3 Conversion of degree into radian and vice versa of standard angles	<b>02</b>	<b>04</b>
<b>6</b>	<b>Trigonometric ratios of Compound and Allied Angles</b>  6.1 Proofs of sine ,cosine and tan of (A+B) and (A-B) 6.2 Examples	<b>06</b>	<b>08</b>
<b>7</b>	<b>Trigonometric ratios of Multiple Angles</b> 7.1 Proofs of sine, cosine and tangent of $2\theta$ , $3\theta$ 7.2 Examples	<b>05</b>	<b>10</b>
<b>8</b>	<b>Factorization and Defactorization Formulae</b>  8.1 Proofs of above formulae 8.2 Examples	<b>04</b>	<b>08</b>
<b>9</b>	<b>Inverse Trigonometric Ratios</b> 9.1 Definition 9.2 Principle value 9.3 Proof of standard formulae 9.4 Examples	<b>07</b>	<b>10</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
<p>1.Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.</p> <p>2.In each topic corresponding applications will be explained</p>			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (level wise)			Course Outcomes	Total Marks
		Knowledge	Comprehension	Application		
1	Determinants	-	2	4	CCF105-1	06
2	Partial Fractions	2	2	8	CCF105-2	12
3	Matrices	2	2	12	CCF105-3	16
4	Binomial Theorem	2	-	4	CCF105-4	06
5	Trigonometric Ratios and Identities	2	-	2	CCF105-5	04
6	Allied Angles	2	2	4	CCF105-5	08
7	Compound Angles	2	-	8	CCF105-5	10
8	Factorisation & De-factorisation angles	2	-	6	CCF105-5	08
9	Inverse Trigonometric ratios	2	2	6	CCF105-5	10
TOTAL		16	10	54		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TUTORIALS**

Sr.No	Topics	Tutorial Content (10 problems in each tutorial)
1	Determinants	Examples on expansion of determinants, Cramer's rule, consistency of equations.
2	Partial Fractions	To resolve given function into partial fractions-Different cases
3	Matrices	Examples on addition, Subtraction and Multiplication of Matrix
4	Matrices	To find adjoint, Inverse of a given matrix, To solve simultaneous equation by Matrix method
5	Binomial Theorem	To expand $(x+y)^n$ by Binomial theorem,
6	Trigonometric Ratios and Identities	Examples on conversion of degree to radian and vice versa, simple examples on trigonometry.
7	Allied Angles	Examples on Allied angles

8	Compound Angles	Examples on Compound angles
9	Factorisation & De-factorisation angles	Examples on Examples on Allied angles
10	Inverse Trigonometric Ratios	Examples on principle value and trigonometrics functions

**INSTRUCTIONA STRATEGIES :**

**Instructional Methods:**

1. Lectures cum Demonstrations
2. Tutorials

**Teaching and Learning resources:**

1. Chalk board
2. Item Bank

**REFERENCE MATERIAL :**

**a) Books:**

Sr. No.	Author	Title	Publisher
1.	G.V. Kumbhojkar	A Text Book on Engineering Mathematics (First Year Diploma	Phadake Prakashan, Kolhapur
2.	Patel, Rawal and others	Basic Mathematics	Nirali Prakashan, Pune
3.	P.M. Patil and Others	Basic Mathematics	Vision Prakashan, Pune
4.	Engineering Mathematics	S. S. Sastry	Prentice Hall of India
5.	S.P. Deshpande	Mathematics for polytechnic	Pune Vidyarthi Griha, Pune

**b) Website**

- i) [www.khanacademy.org](http://www.khanacademy.org)
- ii) [www.easycalculation.com](http://www.easycalculation.com)
- iii) [www.math-magic.com](http://www.math-magic.com)

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**COURSE ID :**

**Course Name : ENGINEERING MATHEMATICS**  
**Course Code : CCF106**  
**Course Abbreviation : FEMT**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : CCF105 Basic Mathematics**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	04
Practical	01	

**Evaluation Scheme :**

...

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each)	i. 25 marks for each practical iv. One PST of 25 marks	One paper (3 hour)	---	--	
<b>Marks</b>	20	--	80	---	--	100

**RATIONALE:**

This subject is an extension of Basic mathematics of first semester and a bridge to further study of applied mathematics. The knowledge of mathematics is useful in other technical areas. Differential calculus has applications in different engineering branches. For example concepts such as bending moment, curvature, maxima and minima. Numerical methods are used in programming as an essential part of computer engineering. In Metrology and quality control statistical methods are used to determine the quality and suitability of components. Engineering mathematics lays the foundation to understand technical principles in various fields.

**COMPETENCY:**

Apply principles of Engineering Mathematics to solve Engineering problems as follows-

- 1.Cognitive:** Understanding and applying principles of Engineering Mathematics to Engineering problems
- 2. Psychomotor:** a) Use of co-ordinate geometry in animation, autocad, computer graphics etc.  
b) Proper handling of calculator.
- 3. Affective :** Attitude of accuracy, punctuality, presentation, visualization.

**Course Outcomes(CO's) :**

**CCF106-1 :** To solve problems on two dimensional co-ordinate geometry for straight line and circles.

**CCF106-2 :** To find approximate solution of algebraic equations and simultaneous equations by various methods.

**CCF106-3 :** To find limits of different types of functions using various methods.

**CCF106-4 :** To solve the problems of maxima, minima and geometrical applications.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Apply principles of Engineering Mathematics to solve Engineering problems	3	-	1	-	-	-	-	-	-	-	2	2
<b>CCF106-1 :</b> To solve problems on two dimensional co-ordinate geometry for straight line and circles.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF106-2 :</b> To find approximate solution of algebraic equations and simultaneous equations by various methods.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF106-3 :</b> To find limits of different types of functions using various methods.	3	-	1	-	-	-	-	-	-	-	1	1
<b>CCF106-4 :</b> To solve the problems of maxima, minima and geometrical applications.	3	-	1	-	-	-	-	-	-	-	1	1

**CONTENT:  
THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	<b>CCF106-1 : To solve problems on two dimensional co-ordinate geometry for straight line and circles.</b>		
<b>1</b>	<b>Point and Distances</b> 1.1 Distance formula (Only mention, No examples ) 1.2 Section formula & midpoint formula (No Examples & without proof) 1.3 Centroid of a triangle & Area of Triangle 1.4 Collinearity	<b>02</b>	<b>04</b>
<b>2</b>	<b>The Straight line</b> 2.1 Slope, intercepts & various methods of finding slope 2.2 Conditions for two straight lines to be parallel and Perpendicular to each others 2.3 Various forms of equations of straight line 2.4 Perpendicular distance of a point from a line 2.5 Distance between two parallel lines 2.6 Angle between two straight lines 2.7 Intersection of two straight lines & the equation of line passing through this point of intersection	<b>06</b>	<b>08</b>
<b>3</b>	<b>Circle</b> 3.1 Equations of Circle ( various forms ) 3.2 Examples to find equation of circles	<b>04</b>	<b>08</b>
	<b>CCF106-2 : To find approximate solution of algebraic equations and simultaneous equations by various methods.</b>		
<b>4</b>	<b>Numerical solution of Algebraic Equations</b> 4.1 Bisection Method 4.2 Regula- Falsi Method	<b>06</b>	<b>10</b>
<b>5</b>	<b>Numerical solution to simultaneous equations</b> 5.1 Jacobi's Method 5.2 Gauss-Seidel method .	<b>06</b>	<b>10</b>
	<b>Total</b>	<b>24</b>	<b>40</b>

**Section II**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	<b>CCF106-3 : To find limits of different types of functions using various methods.</b>		
<b>6</b>	<b>Functions</b> 6.1 Definition and Concept of function 6.2 Definition of Odd & Even functions, Explicit & implicit functions, Composite functions, Parametric functions 6.3 Value of a function 6.4 Examples on value of functions, Odd & Even functions, Composite functions	<b>03</b>	<b>06</b>
<b>7</b>	<b>Limits</b> 7.1 Definition 7.2 Limits of algebraic functions by factorization, simplification, rationalization, Limit as $x \rightarrow \infty$ 7.3 Limits of trigonometric functions by factorization, formula $\frac{\sin x}{x}$ as $x \rightarrow 0$ , substitution.	<b>06</b>	<b>08</b>
	<b>CCF106-4 : To solve the problems of maxima, minima and geometrical applications.</b>		
<b>8</b>	<b>Differentiation</b> 8.1 Definition, Derivative of standard functions (without proof), 8.2 Derivative of sum, difference, product and quotient of two or more functions 8.3 Derivative of composite functions 8.4 Derivative of Inverse functions 8.5 Derivative of Implicit functions 8.6 Derivative of Parametric functions 8.7 Derivative of exponential and logarithmic functions 8.8 Logarithmic differentiation 8.9 Differentiation of second order	<b>12</b>	<b>20</b>
<b>9</b>	<b>Applications Of Derivatives</b> 9.1 Geometrical meaning of derivative (To find equation of Tangent and normal ) 9.2 Maxima and minima of functions	<b>03</b>	<b>06</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
1.Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	<i>CCF106-3 : To find limits of different types of functions using various methods.</i>		
2. In each topic corresponding applications will be explained			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (level wise)			Total Marks
		Knowledge	Comprehension	Application	
1	Point and Distances	2	--	2	4
2	Straight line	2	2	4	8
3	Circle	2	2	4	8
4	Numerical solution of Algebraic Equations and simultaneous Equations	2	2	16	20
5					
6	Functions	2	-	4	6
7	Limits	2	2	4	8
8	Differentiation	4	4	12	20
9					
10	Applications Of Derivatives	--	--	6	6
Total		16	12	52	80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**A. TUTORIALS :** Note - Tutorials are to be used to get enough practice

Sr No.	Topic	Tutorial Content (10 problems in each tutorial)
1	Point and Distances	Examples on Centroid of triangle, area of triangle, collinearity
2	Straight line	Examples on different cases of straight line, To find perpendicular distance of a point from a line, angle between two lines, intersection of lines.
3	Circle	To find equation of Circle- Different forms
4	Num. solution of Algebraic & simultaneous Eq	Numerical solution of algebraic equations.
5		Numerical solution of simultaneous equations
6	Functions	Examples on functions

7	Limits	Evaluation of limits by Factorisation, Rationalization, Simplification, Infinity method Evaluation of limits of Trigonometric functions
8	. Differentiation	To find derivatives by product rule, quotient rule, Chain rule, Inverse function, Implicit function
9	. Differentiation	To find derivatives of Parametric function, Logarithmic function, Derivatives of second order
10	Applications of Derivatives.	To find equation of Tangent, Normal & To find Maxima and Minima of a function.

### **INSTRUCTIONAL STRATEGIES:**

#### **Instructional Methods:**

1. Lectures cum Demonstrations
2. Tutorials

#### **Teaching and Learning resources:**

1. Chalk board
2. Item Bank

### **REFERENCE MATERIAL:**

#### **a) Books:**

<b>Sr. No.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	G.V. Kumbhojkar	Engineering Mathematics III	Phadake Prakashan, Kolhapur
2.	Patel, Rawal,	Engineering Mathematics	Nirali Prakashan, Pune
3.	Mathematics for Polytechnic	S. P. Deshpande	Pune Vidyarthi Griha Prakashan
4.	Sameer Shah	Engineering Mathematics	Tech-Max Publication, Pune
5.	A.M. Vaidya	Applied Mathematics	Central Techno

- b) Websites**
- i) [www.khanacademy.org](http://www.khanacademy.org)
  - ii) [www.easycalculation.com](http://www.easycalculation.com)
  - iii) [www.math-magic.com](http://www.math-magic.com)

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**COURSE ID :**

**Course Name** : Basic Electrical Engineering  
**Course Code** : EIF107  
**Course Abbreviation** : FBEE  
**Pre-requisite Course(s)** : Nil

**TEACHING AND EVALUATION SCHEME :**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	3	05
Practical	2	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Oral*	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Internal Oral Exam	--	
<b>Marks</b>	20	--	80	25I	--	125

I – Internal Examination    \*\* Assessment as per Pro-forma I

**Rationale:**

The Diploma Course in Electronics & Telecommunication Engineering/Industrial Electronics involves the study of the basic concepts, rules and laws of Electric and Magnetic Circuits Electrical machines, equipments and instruments.

In industry maximum electrical equipments and machines are combined with electronics as well as remotely operated controlling. Hence the subject deals in understanding the working principle, construction, operation and applications of the various equipments and instruments; and Electrical machines.

**Competency :** Apply the basic principles of electrical engineering to solve electronic engineering problems.

**Cognitive :** i) Understanding and applying principles and laws of electrical engineering to simple practical problems/ situations. ii) Observing iii) Classifying iv) Interpreting

**Psychomotor :** Handling of instruments, apparatus and tools

**Affective :** Skill of i) Safety ii) curiosity, interest and self-confidence iii) working in team

**Course outcomes:**

**EIF107-1** Apply basic laws and principles of electrical engineering to electrical applications.

**EIF107-2** Use magnetic principles to work on electrical devices.

**EIF107-3** Use electromagnetic principles to make devices work.

**EIF107-4** Use AC circuits in electronic devices.

**EIF107-5** Use series and parallel AC circuits in applications.

**EIF107-6** Assist work on three phase systems including transformers.

**Competency, course outcomes and programme outcomes (cp-co-po) matrix :**

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
Competency: Understand fundamental principle and concept of basic electronic devices	2	-	3	-	-	-	-	-	-	-	1	1
EIF107-1	2	-	2	-	-	-	-	-	-	-	1	1
EIF107-2	2	-	2	-	-	-	-	-	-	-	1	1
EIF107-3	2	-	2	-	-	-	-	-	-	-	2	2
EIF107-4	2	1	2	-	-	-	-	-	-	-	2	2
EIF107-5	2	1	2	-	-	-	-	-	-	-	2	2
EIF107-6	2	1	2	-	-	-	-	-	-	-	1	1

**CONTENT :**

**A) THEORY :**

**Section I**

Chapter	Name of the Topics	Theory hours	Marks
<b>EIF107-1 Apply basic laws and principles of electrical engineering to electrical applications.</b>			
<b>01</b>	<b>Basic Concepts:</b> 1.1 Definition of Electric Current, Voltage. 1.2 Concept of Resistance, - Laws of Resistance, - Concept of Resistivity and Conductivity, 1.3 Classification of Electric Current: - Direct Current (DC) - Alternating Current (AC) 1.4 Ohm's Law, Concept of Voltage drop and Terminal Voltage 1.5 Kirchhoff's Laws - Kirchhoff's Current Law - Kirchhoff's Voltage Law ( <b>Simple Numerical with maximum two equations</b> )	<b>08</b>	<b>12</b>
<b>EIF107-2 Use magnetic principles to work on electrical devices.</b>			
<b>02</b>	<b>Magnetic Circuits:</b> 2.1 Magnetic Circuit - Ohm's law of Magnetic Circuit. 2.2 Definitions Concerning Magnetic Circuit. - Magneto-Motive-Force (MMF), Ampere Turns (AT), Reluctance, Permeance, Reluctivity. 2.3 Comparison Between Electric and Magnetic circuit. 2.4 Calculations of Amp.-Turns for simple Series, (Simple Numerical) 2.5 Concept of Leakage Flux, Useful Flux & Fringing. 2.6 Magnetization Curve (B - H Curve) - Magnetization Curve for Magnetic and Non-Magnetic Materials. - Magnetic Hysteresis, Hysteresis Loop. - Hysteresis Loops for Hard & Soft Magnetic Materials. - Area of Hysteresis Loop, Hysteresis Loss. (No Derivation and No Numerical) 2.7 Types of Magnets and their applications. - Permanent Magnet, Electromagnet.	<b>08</b>	<b>16</b>
<b>EIF107-3 Use electromagnetic principles to make devices work.</b>			

<b>03</b>	<b>Electromagnetic Induction:</b> 3.1- Faraday's Laws of Electromagnetic Induction. Faraday's First Law, Faraday's Second Law (No Numerical) 3.2 Induced E.M.F: Statically Induced E.M.F., Dynamically Induced E.M.F. (Simple Numerical) 3.3 Direction of Induced E.M.F. and Currents. - Fleming's Right Hand Rule, - Lenz's Law Self Induced E.M.F., Mutually Induced E.M.F. 3.4 Self Inductance 3.5 Coefficient of Self-induction (L), (Simple Numerical) 3.6 Mutual Inductance 3.7 Coefficient of Mutual Inductance (M) (Simple Numerical) 3.8 Energy Stored in Magnetic Field (No Derivation and No Numerical)	<b>08</b>	<b>12</b>
	<b>TOTAL:</b>	<b>24</b>	<b>40</b>

**Section – II**

Chapter	Name of the Topics	Theory hours	Marks
<b>EIF107-4</b> Use AC circuits in electronic devices.			
<b>04</b>	<p style="text-align: center;"><b>A.C. Fundamentals</b></p> <p>4.1 Introduction.  4.2 Generation of Alternating EMFs.  4.3 Elementary Alternator.  4.4 Some important Terms.: cycle, time period, frequency, amplitude, average values  4.5 Equations of Alternating Voltages and Currents.  4.6 Effective or Root Mean Square (R.M.S.) Value of Sinusoidal Current or Voltage.  4.7 Peak Factor and Form Factor.  4.8 Phasor Representation of Alternating Quantities.  4.9 Phase and Phase Difference.  4.10 Phasor Diagrams.  4.11 Addition and Subtraction of Sinusoidal Alternating Quantities.(Simple Numerical)  4.12 Concept Of Lagging and Leading</p>	<b>09</b>	<b>14</b>
<b>EIF107-5</b> Use series and parallel AC circuits in applications.			
<b>05</b>	<p><b>Series and Parallel AC Circuits</b></p> <p>5.1 Vector algebra-Representation of vector in rectangular form &amp; polar form, conversion from rectangular to polar &amp; polar to rectangular, addition, subtraction, multiplication &amp; division of vector.  5.2 Series A.C. Circuits  5.2.1 Purely Resistive A.C. Circuit.  5.2.2 Purely Inductive A.C. Circuit.  5.2.3 Purely Capacitive A.C. Circuit.  5.2.4 Circuit with Resistance and Inductance in Series with  concept of power factor  5.2.5 Circuit with Resistance and Capacitance in Series with  concept of power factor  5.2.6 Circuit with Resistance, Inductance and Capacitance in  Series with concept of power factor  5.2.7 Active and reactive power in single phase series A.C. Circuits</p>	<b>08</b>	<b>14</b>
<b>EIF107-6</b> Assist work on three phase systems including transformers.			

<b>06</b>	<b>Three Phase A. C. Circuits.</b> 6.1 Polyphase System 6.2 Three Phase Supply Systems. 6.3.1 Phase Sequence 6.3.2 Three-Phase Three-Wire System. 6.3.3 Three-Phase Four-Wire System. 6.4 Types of connection of load star and delta	<b>03</b>	<b>06</b>
<b>07</b>	<b>Transformer</b> 7.1 Transformer: Definition 7.2 Working principle of transformer 7.3 Types of transformer according to a) Construction b) Function c) Number of phases 7.4 Applications of Transformer in Electronic Circuit <b>7.5 Earthing</b> 7.5.1 Necessity of Earthing 7.5.2 Types of Earthing- Pipe earthing, Plate earthing	<b>04</b>	<b>06</b>
<b>Total</b>		<b>24</b>	<b>40</b>

**B) TERM WORK: Laboratory experiments and related skills to be developed :  
(Minimum Eight From List)**

Sr. no	Laboratory experience	Skills developed	Course outcomes
1.	Study the effect of temperature on resistance of copper	1. Connect the various components as per the circuit diagrams by using wires 2. Calculate the resistance from the readings	EIF107-1
2.	To identify the nature of D.C. and A.C. voltage	To identify the nature of D.C. and A.C. voltage	EIF107-1
3.	Use of Rheostat as a current regulator & potential divider	1. Use the rheostat as a current regulator i.e. change the current in the circuit 2. Use the rheostat as a potential divider i.e. change the voltage across the circuit.	EIF107-1
4.	To plot the B H curve for magnetic material and determine the relative Permeability	1. Connect the various components as per the circuit diagrams by using wires 2. Plot the B-H curve from the readings 3. Determine the relative Permeability of the material.	EIF107-4



<b>Sr. no</b>	<b>Laboratory experience</b>	<b>Skills developed</b>	<b>Course outcomes</b>
5.	To verify Faraday's First Law of Electromagnetic Induction (For Dynamically & Statically Induced EMF)	1. Connect the apparatus as per the circuit diagrams . 2. Observe the deflection of galvanometer with respect to magnitude & direction. 3. Analyze the observations with law.	EIF107-5
6.	To find resistance and inductance of a choke coil	1. Connect the apparatus as per the circuit diagrams 2. To find resistance and inductance of a choke coil	EIF107-6
7.	To study R-L-C series circuit and R-L& C parallel circuit	1. Connect the apparatus as per the circuit diagrams 2. To study R-L-C series circuit and R-L& C parallel circuit	EIF107-6
8.	Study of Construction details & application of single phase transformer.	1. Observe the single phase transformer as per the construction. 2. Connect the single phase transformer to know the working.	EIF107-6

**Specification Table For Question Paper of Theory Examination:**

<b>Section / Topic no.</b>	<b>Name Of the Topic</b>	<b>Distribution Of Marks (Level Wise)</b>			<b>Total Marks</b>	<b>Course outcomes</b>
		<b>Remember</b>	<b>Understanding</b>	<b>Applications</b>		
I/1	Basic Concepts	04	04	04	12	<b>EIF107-1</b>
I/2	Magnetic Circuits	04	06	06	16	<b>EIF107-2</b>
I/3	Electromagnetic Induction	04	04	04	12	<b>EIF107-3</b>
I/4	A.C. Fundamentals	04	04	08	16	<b>EIF107-4</b>
II/5	Series and Parallel AC Circuits	04	04	08	16	<b>EIF107-5</b>
II/6	Three Phase A. C. Circuits.	02	--	06	08	<b>EIF107-6</b>
				<b>TOTAL</b>	<b>80</b>	

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods:**

1. Lectures cum Discussions    2. Regular Home Assignments.    3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slide    4. Question Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes:**

Sr. No.	Name of Book	Author	Publisher
1.	A Text Book of Electrical Technology Vol-I (Basic Electrical Engg.)	B. L. Theraja A. K. Theraja	S. Chand and Co.
2.	Basic Electrical Engg.	V. N. Mittle	Tata McGraw-Hill
3.	Electrical Technology	Edward Hughes	Pearson Education, New Delhi
4	Electrical Technology	V.K.Mehta	S. Chand and Co.

**b) Websites**

- 1) [www.ece.rice.edu](http://www.ece.rice.edu)  
2) [igs.nigc.ir/STANDS/BOOK/Electrical-Eng-HB.pdf](http://igs.nigc.ir/STANDS/BOOK/Electrical-Eng-HB.pdf)  
3) [aggregate.org/hankd/piaee12.pdf](http://aggregate.org/hankd/piaee12.pdf)

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**COURSE ID**

**Course Name : COMPUTER FUNDAMENTALS AND APPLICATIONS**  
**Course Code : EIF 108**  
**Course Abbreviation : FCFA**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : NIL**  
**Teaching Scheme: MPECS 2016**

Scheme component	Hours / week	Credits
Theory	1	3
Practical	2	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Term Work	Practical Examination (Internal)	
Details of Evaluation	--	i. 25 marks for each practical ii. One PST of 25 marks	--	As per Proforma-V	As per Proforma-VI	
Marks	--	--	--	25	50I	75

**RATIONALE:**

Computers play a vital role in various fields like business, academics, defense, budget research, engineering, medicine. In the present Industrial & commercial environment, the technician is expected to use computers skillfully. The primary purpose of this course is to give an elementary but sound fundamental understanding of how computers work, its basic hardware software components, what basic applications of computer technology currently exist, how they work and basic knowledge and applications of Internet.

**COMPETENCY**

**Cognitive :** i) Identify the basic parts of a computer system and relationships among component.  
 ii) Describe characteristics and functions of CPU's, motherboard, RAM, expansion connection, hard drives and CD-ROM drives.

**Psychomotor :** i) Investigate computer system, Network & computer program ii) drawing flow chart for computer programs constructions

**Affective :** Attitude of i) precision ii) accuracy iii) safety iv) punctuality

**COURSE OUTCOMES:**

**EIF 108-1:** State types of computers & its application

**EIF 108-2:** Relate functions of hardware & software components of a computer system

**EIF 108-3:** Compare basic differences of among operating systems

**EIF 108-4:** Illustrate computer programs, tools & languages

**EIF 108-5:** Demonstrate & Classify computer networks

**EIF 108-6:** Discover the importance of Internet and be able to safely surf on the Internet

**EIF 108-7:** Design files of word processors, spreadsheets, presentation software, and database application

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs										PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency:</b>	2	1	1	-	-	-	-	-	-	1	3	3
<b>EIF 108-1:</b> State types of computers & its application	1	-	-	-	-	-	-	-	-	-	2	2
<b>EIF 108-2:</b> Relate functions of hardware & software components of a computer system	1	-	1	-	-	-	-	-	-	1	2	2
<b>EIF 108-3:</b> Compare basic differences of among operating systems	-	1	1	-	-	-	-	-	-	1	3	3
<b>EIF 108-4:</b> Illustrate computer programs, tools & languages	-	2	2	-	-	-	-	-	-	1	2	3
<b>EIF 108-5:</b> Demonstrate & Classify computer networks	2	1	1	-	-	-	-	-	-	-	2	2
<b>EIF 108-6:</b> Discover the importance of Internet and be able to safely surf on the Internet	2	-	1	-	-	-	-	-	-	1	2	2
<b>EIF 108-7:</b> Design files of word processors, spreadsheets, presentation software, and database application	1	-	2	-	-	-	-	1	-	-	1	3

**CONTENT:**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF 108 -1 : State types of computers &amp; its application</i></b>			
<b>1</b>	<b>INTRODUCTION TO COMPUTERS</b>  1.1 History of computers 1.2 Types of computers 1.3 Applications of computers –Education, Business, Medical, Engineering etc.	1	--
<b><i>EIF 108 -2 : Relate functions of hardware &amp; software components of a computer system</i></b>			
<b>2</b>	<b>SYSTEM UNIT</b>  2.1 System Board 2.2 Microprocessor 2.3 Memory and its types 2.4 Expansion cards	1	--
<b>3</b>	<b>HARDWARE COMPONENTS</b> 3.1 Input devices and its connections: Keyboard, Mouse, Scanner, Microphone 3.2 Output devices and its connections: Monitors, Printers, Projectors, Speakers 3.3 Storage devices: Hard disks, Magnetic Tapes, Optical Discs, Pen drive 3.4 Tips on “How to buy a computer”.	2	--
<b><i>EIF 108 -3 : Compare basic differences of among operating systems</i></b>			
<b>4</b>	<b>INTRODUCTION TO SOFTWARE</b> 4.1 Types of software 4.1.1 System software 4.1.2 Application Software 4.2 Introduction to Operating System 4.2.1 Definition: Operating System 4.2.2 Role of Operating System 4.2.3 Various Examples of Operating Systems	2	--
<b><i>EIF 108 -4 : Illustrate computer programs, tools &amp; languages</i></b>			
<b>5</b>	<b>COMPUTER PROGRAM</b> 5.1 Purpose of program planning	2	--

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
	5.2 Algorithm 5.3 Flowchart 5.4 Pseudocode 5.5 Plan the logic of a computer program 5.6 Commonly used tools for program planning and their use		
<b>6</b>	<b>COMPUTER LANGUAGES</b>  6.1 Computer languages or programming languages 6.2 Three broad categories of programming languages- machine, assembly, and high-level languages 6.3 programming language tools - assembler, compiler, linker, and interpreter 6.4 Concepts of object-oriented programming languages	2	--
<b><i>EIF 108 -5 : Demonstrate &amp; Classify computer networks</i></b>			
<b>7</b>	<b>COMPUTER NETWORKS</b>  7.1 Basic elements of a communication system 7.2 Data transmission modes 7.3 Data transmission speed & category 7.4 Data transmission media 7.5 Digital & Analog data transmission 7.6 Concept : Network 7.7 Types of Networks : LAN, MAN, WAN	2	--
<b><i>EIF 108 -6 : Discover the importance of Internet and be able to safely surf on the Internet.</i></b>			
<b>8</b>	<b>INTERNET &amp; CYBER LAWS</b> 8.1 Internet basic terminology 8.2 Client, server concepts 8.3 Applications of Internet 8.4 Hardware & software requirements for internet connection 8.4 Various examples of Browsers 8.5 Browsing 8.6 Search Engines 8.7 Virus, Types of Viruses, Virus Protection 8.8 Introduction to Cyber Law 8.9 Information Technology Act of India 2000	2	--
<b><i>EIF 108 -7 : Design files of word processors, spreadsheets, presentation software, database application</i></b>			

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
<b>9</b>	<b>OFFICE AUTOMATION TOOLS</b>  9.1 MS-Word – Opening menus, toolbars, opening and closing documents, clipboard concept 9.2 MS – Excel – Working and manipulating data with excel, formulas, functions, chart and its types 9.3 MS – PowerPoint – Working with PowerPoint and presentation ,Changing layout, Graphs , Auto content wizard ,Slide show, Animation effects,Normal, outline, Slide sorter, Reading view.	2	--
	<b>Total:</b>	16	--

**Laboratory experiments and related skills to be developed :  
(Minimum Eight From List)**

Sr. No.	Title of Experiment	Skills to be developed	Course outcome
1.	Understanding PC(system unit and connections of internal components)	<ol style="list-style-type: none"> <li>1. Identify the front and rear panel components of CPU</li> <li>2. Identify different components inside the CPU cabinet               <ol style="list-style-type: none"> <li>2.1 Identify different components on motherboard.</li> <li>2.2 Motherboard connection.</li> <li>2.3 Graphics card connection.</li> <li>2.4 Network interface card connection.</li> </ol> </li> </ol>	EIF108-1
2.	Understanding the storage devices	<ol style="list-style-type: none"> <li>1. Study various secondary storage devices along with their capacities.</li> <li>2. Connecting HDD, and CD, DVD drives.</li> <li>3. Attaching USB devices.</li> </ol> <p>Care of the above devices.</p>	EIF108-2
3.	Understanding the input/output devices and their connections	<ol style="list-style-type: none"> <li>1. Study of connections of mouse, keyboard, monitor, printer.</li> <li>2. Install driver software for a printer, Scanner</li> <li>3. Set up a printer &amp; scanner</li> </ol> <p>Scan a page, print a test page</p>	EIF108-2
4.	Study of system software with basics of OS	<ol style="list-style-type: none"> <li>1. Understanding the concept of system and application software.</li> <li>2. Examples of system software.</li> <li>3. Study of application software.</li> <li>4. Understand the concept &amp; functions of Operating system, Examples of Operating system</li> </ol> <p>Overview of Windows OS</p>	EIF108-3
5.	Creating and Editing a word document	<ol style="list-style-type: none"> <li>1. Use of menus and submenus.</li> <li>2. Type and format the text matter in paragraphs.</li> <li>3. Set up page size, margins</li> <li>4. Insert headers and footers, bullets.</li> <li>5. Use of borders and shading</li> <li>6. Format picture, word-art, text box etc.</li> <li>7. Typing text in multi-columns</li> </ol>	EIF108-4



		Use of equation editor	
6.	Inserting table and Mail-Merge	<p>Table:</p> <ol style="list-style-type: none"> <li>1. Insert,format Table.</li> <li>2. Sort data in table</li> </ol> <p>Mail-Merge:</p> <ol style="list-style-type: none"> <li>1. Understand the mail-mergeFacility.</li> <li>2. Create main document and edit it</li> <li>3. Create &amp; edit data source</li> <li>4. Merge the main document anddata source.</li> <li>5. Merge to file and merge to print.</li> </ol>	EIF108-5
7.	Creating and Editing a work-book	<ol style="list-style-type: none"> <li>1. Use of menus and submenus.</li> <li>2. Enter the data in worksheet.</li> <li>3. Creating a table in worksheet.</li> <li>4. Use of editing commands.</li> <li>5. Fill series by auto-fill handle, Insert / delete rows, columns and worksheet.</li> </ol> <p>Set up page size, margins.&amp; set the print area.</p>	EIF108-6
8	Understanding the basics of presentation software & Creating a new presentation	<ol style="list-style-type: none"> <li>1. Insert new / duplicate slides</li> <li>2. Create objects on a slide and use general editing operations.</li> <li>3. Use of different views in presentation</li> <li>4. Use standard templates for slides.</li> </ol> <p>Use preset animation, slide transition and Prepare speaker notes.</p>	EIF108-7
9	Using advanced features of slide-show	<ol style="list-style-type: none"> <li>1. Use of custom animation effect</li> <li>2. Use of action buttons on slides</li> <li>3. Rehearse time-setting of slide show</li> </ol>	EIF108-7
10	Making use of Internet (Email, Chat, virus protection.)	<ol style="list-style-type: none"> <li>1. Study of different types of networks.</li> <li>2. Visit the website.</li> <li>3. Using search engines.</li> <li>4. Register online for e-mail ID.</li> <li>5. Communicate with others using e-mail</li> <li>6. Chatting</li> </ol>	EIF108-8

		7. Installation, use and update of Anti-virus software Removing detected viruses	
11	Mini Project	Mini Project based on Microsoft office suite which incorporates presentation, database & spreadsheet handling, word processing skills.	EIF108-1 to EIF108-8

**Progressive Skills Test :**

**Criteria for Continuous Assessment of Practical work and Progressive skill Test:**

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	10
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & computer handling skill	05
5	Use of toolbar, menu bar and short cut keys.	05
6	Logical thinking and approach	10
7	Oral Based on Lab work and completion of task	10
TOTAL		50

Assessment at semester end practical exam as per Pro-forma II.

**Criteria for assessment at semester end practical exam:**

Sr. no	Criteria	Marks allotted
1.	Technical ability	20
2.	Communication skill	10
3.	Logical approach	20
	<b>TOTAL.</b>	50

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Discussions
2. Regular Home Assignments.
3. Laboratory experiences and laboratory interactive sessions

**Teaching and Learning resources:**

1. Chalk board   2. O.H.P.      3. Slides      4. Self-learning Tutors

**REFERENCE MATERIAL:**

**a) Books / Codes**

<b>Sr. No.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	Sanjay Saxena	A first course in Computers 2003 edition	Vikas Publishing House Pvt Limited
2.	Anita Goel	Computer Fundamentals	Pearson Education India
3.	Sudipto Das	A Complete Guide to Computer Fundamentals	Laxmi Publications
4.	P.K.Sinha	Computer Fundamentals	BPB Publication

**b) Websites**

- i) <http://mv.safaribooksonline.com>
- ii) <http://www.edulearn.com>
- iii) <http://kvsecontents.in/computer-fundamentals>

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**COURSE ID :**

**Course Name : ENGINEERING GRAPHICS**  
**Course Code : CCF109**  
**Course Abbreviation : FEGR**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : <nil >**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	02	06
Practical	04	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	--	i. 25 marks for each practical ii. One PST of 25 marks	--	External Practical Exam (2 Hours Duration)	As per Proforma VI	
<b>Marks</b>	--	--	--	50 E	25	75

\* Assessment as per Pro-forma – IV

E-External Examination

**RATIONALE :**

Engineering Graphics is one of the ways of communication among engineering professionals. It describes scientific facts, concepts, principles and techniques of drawing in any engineering fields to express the ideas and conveying the instructions which are use for carrying out tasks at work place. This preliminary course aims at building a foundation for the further course in drawing and other allied subjects. This subject is useful in developing, drafting and sketching skills of students. So it is necessary to all programmes.

**COMPETENCY:**Read, draw & Interpret the engineering drawing of simple objects.

**Cognitive :**Understand various drawing procedures.

**Psychomotor :**Produce engineering drawing from the given problem.

**Affective :**Attitude of using i) Procedures ii) Practices iii) Drawing Instruments iv) Accuracy v) Drafting Skill

**COURSE OUTCOMES :**

**CCF109-1** Understand various fundamentals in engineering drawing.

**CCF109-2** Produce the projection of point, lines & planes inclined to one reference plane.

**CCF109-3** Produce orthographic drawing from given pictorial view.

**CCF109-4** Produce sectional orthographic drawing from given pictorial view.

**CCF109-5** Visualize & draw accordingly the pictorial view by correlating the given views.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs										PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work	PO 9 Communication	PO 10 Life-long learning		
Competency:	1	-	1	-	-	-	-	-	-	-	3	2
CCF109-1	1	-	1	1	-	-	-	-	-	-	3	-
CCF109-2	1	-	1	-	-	-	-	-	-	-	-	-
CCF109-3	1	-	1	-	-	-	-	-	-	-	3	-
CCF109-4	1	-	1	-	-	-	-	-	-	-	3	-
CCF109-5	1	-	1	-	-	-	-	-	-	-	3	-

**CONTENT :**

**A) THEORY :**

**SECTION -I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
	<b><i>CCF109-1Understand various fundamentals in engineering drawing</i></b>		
<b>1</b>	<b>Introduction To Engineering Drawing</b> 1.1 Drawing Instruments and their uses 1.2 Standard sizes of drawing sheets 1.3 Letters and numbers (single stroke vertical) 1.4 Convention of lines and their applications 1.5 Scale (reduced, enlarged & full size) Plain scale and Diagonal scale. 1.6 Dimensioning technique as per SP-46 (Latest Edition) Types and applications of chain, parallel and Co-ordinate dimensioning 1.7 Introduction to CAD software (Basic commands like Draw, modify). 1.8 Advantages of CAD, 1.9.Geometrical constructions	<b>06</b>	<b>10</b>
	<b><i>CCF109-2Produce the projection of point, lines&amp; planes inclined to one reference plane</i></b>		
<b>2</b>	<b>Projection Of Point And Lines</b> 2.1 Projection of points when point is in first quadrant Only 2.2 Projection of Line inclined to one Reference plane and Parallel to other Reference Plane (Both ends of line should be in first quadrant )	<b>04</b>	<b>06</b>
	<b><i>CCF109-2Produce the projection of point, lines&amp; planes inclined to one reference plane.</i></b>		
<b>3</b>	<b>Projection Of Planes</b> 3.1 Projection of Planes of Circular , Square, Triangular,Rectangular , Pentagonal , Hexagonal Shapes Inclined To One Reference Plane And perpendicular to other Reference Plane. ( Planes in First Quadrant Only)	<b>06</b>	<b>06</b>

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>CCF109-3Produce orthographic drawing from given pictorial view .</b>			
<b>4</b>	Orthographic Projection 4.1 Introduction of Orthographic Projection-First and Third angle Projection Method 4.2 Conversion of Pictorial view into Orthographic Views. (First angle Projection Method Only) 4.3 Dimensioning Technique as per SP-46	<b>06</b>	<b>16</b>
<b>CCF109-4 Produce sectional orthographic drawing from given pictorial view.</b>			
<b>5</b>	<b>Sectional Views.</b>  5.1 Types of sections 5.2 Conversion of pictorial view into sectional Orthographic views. (First Angle Projection Method only)	<b>04</b>	
<b>CCF109-5Visualize &amp; draw accordingly the pictorial view by correlating the given views.</b>			
<b>6</b>	<b>Isometric Projection</b>  6.1 Introduction 6.2 Isometric Axis 6.3 Isometric scale 6.4 Drawing of Isometric view and Projection. 6.5 Conversion of Orthographic Views into Isometric view/projection(Including rectangular, cylindrical objects, representation of slots on sloping as well as plane surfaces)	<b>06</b>	<b>12</b>
	<b>Total</b>	<b>32</b>	<b>50</b>
Semester end Practical exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



**Specification table for setting question paper for semester end Practical examination :**

Topic No.	Name of topic	Distribution of marks			Total marks
		Knowledge	Comprehension	Application	
1	Introduction To Engineering Drawing	04	02	04	10
2	Projection of Point And Lines	02	02	02	06
3	Projection of Planes	02	02	02	06
4	Orthographic projection	04	04	08	16
5	Sectional Views.				
6	Isometric Projection	04	02	06	12
	<b>TOTAL</b>	<b>16</b>	<b>12</b>	<b>22</b>	<b>50</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**Practical:**

**List of Practical:**

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1	Geometrical Constructions Using CAD (1 Sheet)	To develop drawing skill	<b>CCF109-1</b>
2	Projections of line (1 Sheet)	To develop drawing ability in Projections of line	<b>CCF109-2</b>
3	Projections of Planes (1 Sheet)	To develop drawing ability in Projections of Planes	<b>CCF109-2</b>
4	Orthographic projection(1 Sheet)	To develop drawing ability to draw Orthographic projection	<b>CCF109-3</b>
5	Sectional Views. (1 Sheet)	To develop drawing ability in sectional views	<b>CCF109-4</b>
6	Isometric Projection (2 Sheet) Isometric views of two objects – 1 sheet Isometric Projections of two objects – 1 sheet	To develop ability to draw Isometric projection	<b>CCF109-5</b>

**ASSESSMENT CRITERIA FOR TERM WORK**

**a) Continuous Assessment of Drawing Practical**

Every practical Sheet shall be assessed for **25** marks as per criteria given below:

Sr No.	Criteria	Marks allotted
1	Attendance	05
2	Preparedness	05
3	Correctness and understanding	10
4	Line work and neatness	05
	Total	25

One mid-term *Progressive Skill Test* of **50** marks shall be conducted as per criteria given below:

Sr No.	Criteria	Marks allotted
1	Correctness and understanding	20
2	Line work and neatness	10
3	Dimensioning and judgment without measurement	10
4	Proper use of instrument	10
	Total	50

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Demonstrations
2. Classroom practices

**Teaching and Learning resources :**

1. Chalk board
2. LCD presentations
3. Audio presentations
4. Computer, printer etc.
5. Question Bank

**a) Reference Books**

Sr. No.	Author	Title	Publisher
1.	N. D. Bhatt	Engineering Drawing	Charotar Publishing House 2010
2.	Amar Pathak	Engineering Drawing	Dreamtech Press, 2010
3.	D.Jolhe	Engineering Drawing	Tata McGraw Hill Edu., 2010
4.	M.B.Shah, B.C.Rana	Engineering Drawing	Pearson, 2010
5.	K. Venugopal	Engineering Drawing and Graphics + AutoCAD	New Age Publication, Reprint 2006
6.	IS Code, SP – 46	Engineering Drawing Practice	--

**b) Web References :**

- 1) <http://www.design-technology.info/IndProd/drawings/>
- 2) <http://graphicalcommunication.skola.edu.mt/syllabus/engineering-drawing/>
- 3) [http://en.wikipedia.org/wiki/Engineering\\_drawing](http://en.wikipedia.org/wiki/Engineering_drawing)
- 4) <http://www.engineeringdrawing.org/>
- 5) [http://www.teachengineering.org/view\\_activity](http://www.teachengineering.org/view_activity)
- 6) [www.howtoread.co.in/2013/06/how-to-read-ed.html](http://www.howtoread.co.in/2013/06/how-to-read-ed.html)
- 7) <http://www.slideshare.net/akhilrocker143/edp>
- 8) <http://www.24framesdigital.com/pstulpule>

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**COURSE ID :**

**Course Name : WORKSHOP PRACTICES (ET)**  
**Course Code : CCF114**  
**Course**  
**Abbreviation : FWSD**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	Nil	02
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Term Work	Oral Examination	
Details of Evaluation	Average of two tests of 20 marks each	i) 25 marks for each practical ii) One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma III	-	
Marks	Nil	--	-	50	--	50

**RATIONALE:**

Workshop practices mainly deals with various trades such as Wood working, Fitting and Sheet metal. A technician has to work in such environment with his peers, superiors and subordinates for a major part of his life. Therefore the emphasis on the practical work is needed for the primary experience of working in the team.

**COMPETENCY :**

Prepare a simple job using wood working, fitting and sheet metal trade.

**Cognitive :** Understand different types of tools in wood working, sheet metal and fitting trade.

**Psychomotor :** Prepare a simple job using wood working, fitting and sheet metal trade.

**Affective :** Develop attitude of i) Interpret drawing ii) Safety

**COURSE OUTCOMES :**

**CCF 114-1** Select different types of wood material.

**CCF 114-2** Select different types of tools used in workshop.

**CCF 114-3** Preparing simple components in workshop.

**CCF 114-4** Interpret drawing.

**CCF 114-5** Practicing safety in workshop.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiences and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and teamwork	PO 9 Communication	PO 10 Life-long learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
Competency:	1	-	3	-	-	-	-	3	-	-	2	-
CCFI14-1	1	-	3	-	-	-	-	3	-	-	2	-
CCFI14-2	1	-	3	-	-	-	-	3	-	-	2	-
CCFI14-3	1	-	3	-	-	-	-	3	-	-	2	-
CCFI14-4	1	-	3	-	-	-	-	3	-	-	2	-
CCFI14-5	1	-	3	-	-	-	-	3	-	-	2	-

**Course Contents :- TERM WORK**

Sr. No.	Topics/ Sub-Topics	Practical (Hours)/ Evaluation (Marks)	Skills/ Competencies to be developed	Course outcome
<b>1</b>	<b>Wood Working shop :-</b> a)Any one composite job from the following involving different operations, joints, turning & planing, surface finishing by emery paper, varnishing etc. i)Switch board. ii)Computer table. iii)Printer Table	10/18	a)Study of carpentry tools, Identifying materials b)Measuring dimensions c)Interpretation of drawing d) Operating on planing, cutting, drilling machines e) Time management and observing safety habits f)Prepare furniture or article with carpentry joints	CCF1 to CCF 5
<b>2</b>	<b>FITTING</b> a ) Demonstrations of different fitting tools & drilling machine and power tools b)Demonstrations of different operations like marking, filing, cutting, drilling and tapping c)One simple fitting job (male female assembly type) involving practice of filing drilling cutting tapping etc.	12/16	a)Studying fitting tools, Identifying materials b)Measuring dimensions c)Interpretation of drawing d)Operating drill, saw machines e) Time management and observing safety habits	CCF 2 to CCF 5
<b>3</b>	<b>Sheet Metal shop:</b> a) Demonstrations of different sheet metal tools & Machines b) Demonstrations of different sheet metal operations like sheet cutting, bending, edging, end cutting, Lancing, soldering, riveting. c) To select proper sheet gauge and types of G.I. Sheet required for the job d) One simple job involving sheet metal operations, soldering and riveting e) One composite job from the following 1)Dustbin 2) Letter box 3) Grain container 4)Bucket 5) Tray 6) Trunk 7) Tin box Batch size should be selected depending volume of work	10/16	a)Studying sheet metal tools, Identifying materials b)Measuring dimensions c)Interpretation of drawing d) Operating sheet cutting bending machines e) Time management and observing safety habits f) Prepare utility article	CCF2 to CCF 5

**The students will submit the following.**

Workshop record book showing the details of the job viz. Drawing, Raw material size, time required completing the job.

The journal consisting of the neat sketches, specifications use of the hand tool, and hand operations based on the demonstration in all the trades during the practical work.

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments :

Every practical assignment shall be assessed for 50 marks as per following criteria :

Domain	Particulars	Marks out of 50
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	10
	Drawing / drafting skills	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
TOTAL		50

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 50 marks shall be conducted.

Final marks of term work shall be awarded as per *Assessment Pro-forma III*.

#### Instructional Strategies :-

Demonstration during Practicals

Workshop Record Book, maintenance record book.

Workshop Journal.

#### Teaching and learning resources:-

Shop Demonstration

Hands on training on machine

#### Reference Books :-

Author	Title	Publisher
S. K. Hajra Chaudhary, Bose, Roy	Elements of workshop Technology – Volume I & II	Media Promoters and Publishers limited
B.S. Raghuvanshi	Elements of workshop Technology – Volume I & II	Dhanpat Rai & Co.

#### Websites:

- 1) <http://nptel.ac.in>
- 2) [www.egr.msu.edu/~pkwon/me478](http://www.egr.msu.edu/~pkwon/me478)

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# **LEVEL -II**

## **LIFE SKILLS AND PROFESSIONAL SKILLS COURSES**



**COURSE ID :**

Course Name : GENERIC SKILLS  
Course Code : CCF201  
Course Abbreviation : FGNS

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s)** : <nil >

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	02	04
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory	Practical **	TW	
<b>Detailsof Evaluation</b>	- Nil -	One mid-semester Skill Test(2 hrs) * of 25 marks	- Nil -	Term End Practical Exam (2 hrs)	Pro-forma VI	
<b>Marks</b>	- Nil -	--	- Nil -	50	25	75

\*\* Practical Examination to be conducted by internal examiner (course teacher) and external examiner (course teacher of different classfrom the Institute) and marks to be entered as per Proforma V.

**RATIONALE:**

Acquisition of technical and entrepreneurial competencies is founded on certain generic skills that are fundamentally essential for all disciplines of technology. Considering the age group and socio-economical background of the students of the Institute, a set of minimum essential generic skills has been identified and categorized as i) Concentration skills, ii) Language skills, iii) Learning Skills, iv) Aesthetic Skills, v) Behavioral Skills and vi) Creativity Skills. These generic skills will be studied and practiced in this course. Communication Skills form another major category of generic skills which shall be studied in separate course named *Communication Skills*. For mastery and perfection in these skills, consistent practice and an integrated application is necessary in all subjects of the Programme. Generic skills are essential to improve the overall quality of learning of the student for all the subjects.

**COMPETENCY :**

Apply generic skills to achieve refinement in overall development of personality as follows:

**Cognitive :** Understanding and applying generic skills in various situations

**Psychomotor :** i) Use of proper concentration ii)analyzing routine activity for formal and informal learning  
iii) Use of correct vocabulary.iv) use of aesthetic skills in all dimensions of life.

**Affective :** Attitude of i) concentration ii)confidence iii) manners iv) neatness v) aesthetic presentation

## **COURSE OUTCOMES :**

- CCF201-1 Apply concentration skills in various tasks.  
 CCF201-2 Apply learning skills to gain new knowledge, skills and techniques.  
 CCF201-3 Make use of language skills for effective interaction.  
 CCF201-4 Organize studying skills, self motivation for best performance..  
 CCF201-5 Adapt behavioral and aesthetic skills.  
 CCF201-6 Adapt creativity skills for doing work creatively.

## **COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation]

Competency and COs	Programme Outcomes POs and PSOs										PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency:</b> Apply generic skills to learn to achieve refinement in overall development of personality as follows:	-	-	-	-	1	-	1	-	1	1		
CCF201-1 Enlist and appreciate generic skills necessary for a technician	-	-	-	-	1	-	1	-	1	1		
CCF201-2 Attain concentration through thought analysis, omkar, pranayam, prayer and meditation	-	-	-	-	2	1	-	-	-	1		
CCF201-3 Analyze his own learning process with reference to domain analysis and FIPN model	-	-	-	-	-	1	-	-	-	2		
CCF201-4 Exhibit language skills viz. vocabulary, recitation, sentence making skills	-	-	-	-	1	-	1	-	2	-		
CCF201-5 Exhibit learning skills, studying skills and technical skills viz. calculating, graphic skills	-	-	-	-	-	-	1	-	-	1		
CCF201-6 Exhibit aesthetic skills, behavioral skills and creativity skills	-	-	-	-	2	-	1	-	1	2		

**CONTENT:**

**THEORY :**

Sr. No.	Topics / Sub-topics	Lectures (Hours)
<b><i>CCF201-1 Enlist and appreciate generic skills necessary for a technician</i></b>		
<b>1</b>	<b>Overview of generic skills</b> 1.1 Definition of generic skills, life skills, soft skills. Difference between generic skills and specialized skills 1.2 Important generic skills for technicians: Concentration skills, learning skills, language skills, communication skills, aesthetic skills, behavioral skills, creativity skills 1.3 Importance of generic skills	<b>02</b>
<b><i>CCF201-2 Attain concentration through thought analysis, omkar, pranayam, prayer and meditation</i></b>		
<b>2</b>	<b>Concentration Skills</b> 2.1 Concentration of mind : Meaning and importance. Hurdles and common remedies. 2.2 Thoughts : Intensity, speed and duration of thoughts. Positive, negative and neutral thoughts. Emotions. Management of thoughts. 2.3 Concentration skills : Breathing exercises and <i>pranayam</i> 2.4 Concentration skills : Chanting <i>omkar</i> 2.5 Concentration skills : Prayer - Daily input of positive Thoughts 2.6 Concentration skills : Meditation	<b>06</b>
<b><i>CCF201-3 Analyze his own learning process with reference to domain analysis and FIPN model</i></b>		
<b>3</b>	<b>Learning Skills</b> <b>3.1 Fundamentals of Learning :</b> Definition, characteristics and rewards of learning. Affective, cognitive and psychomotor domains of learning. Barriers in learning. FIPN analysis. <b>3.2 Process of Learning :</b> Reception, understanding, consolidation, retrieval, internalization, application, reinforcement and enhancement <b>3.3 Learning Skills:</b> Skills of observing, listening, reading, notes taking, memorizing, problem solving, graphic, experimenting, surveying, calculating skills, Cognitive skills. <b>3.4 Studying skills :</b> Planning and scheduling, Methods of study as per nature of subject content. <b>3.5 Self-motivation:</b> Meaning and importance. Improving self-motivation through activities like inspiring case studies, web search & presentation, technical quiz/games, group studying, making videos, industry exposure	<b>08</b>
<b><i>CCF201-4 Exhibit language skills viz. vocabulary, recitation, sentence making skills</i></b>		
<b>4</b>	<b>Language Skills</b> 4.1 Vocabulary. Pronunciation. Spellings. Recitation. 4.2 Listening and recitation. 4.3 Word games.	<b>06</b>

	<b>CCF201-6</b> <i>Exhibit aesthetic skills, behavioral skills and creativity skills</i>	
<b>5</b>	<b>Aesthetic Skills</b>  5.1 Sense of aesthetics. Appearance. Neatness. Decency. Sense of colours and graphics 5.2 Application of aesthetics in appearance, work, note book and paper writing, submission work	<b>02</b>
<b>CCF201-6</b> <i>Exhibit aesthetic skills, behavioral skills and creativity skills</i>		
<b>6</b>	<b>Behavioral Skills</b>  6.1 Manners and etiquettes. Discipline. Sincerity. Morales. Politeness. Social and civic sense. Assertion without aggression.	<b>04</b>
<b>CCF201-6</b> <i>Exhibit aesthetic skills, behavioral skills and creativity skills</i>		
<b>7</b>	<b>Creativity Skills</b>  7.1 Meaning and importance of creativity. 7.2 Doing things creatively.	<b>04</b>
	<b>TOTAL</b>	<b>32</b>

**B. TERM WORK**  
**(Minimum Eight From List)**

**Practical Exercises and related skills to be developed:**

The following practical exercises shall be conducted as Term Work detailed in the *Workbook on Generic Skills* developed by the Institute in practical sessions of batches of about 22 students:

<b>Sr. No.</b>	<b>Title of the Lab work</b>	<b>Skills / Competencies to be developed</b>	<b>Course Outcome</b>
1.	Introduction Game	Self-expression, inter-personal rapport	CCF201-1
2.	Concentration Skills - 1: Chanting of Omkar	Concentration	CCF201-2
3.	Concentration Skills - 2: Breathing exercises	Concentration	CCF201-2
4.	Concentration Skills - 3: Prayer	Concentration, recitation, positive thinking	CCF201-2
5.	Concentration Skills - 4: Meditation	Concentration, thought management	CCF201-2
6.	Language Skills - 1 : Vocabulary Exercise	Vocabulary improvement	CCF201-4
7.	Language Skills - 2 : Recitation Exercise	Pronunciation, language acquaintance	CCF201-3 & 4
8.	Language Skills - 3 : Grammar	Language skills	CCF201-3 & 4
9.	Learning Skills - 1 : Domain Analysis of an activity	Understanding learning domains	CCF201-3 & 4
10.	Learning Skills - 2 : FIPN Analysis of Learning	Learning analysis	CCF201-3 & 4
11.	Learning Skills - 3 : Reading and Notes taking	Effective reading and notes taking	CCF201-3 & 4
12.	Learning Skills - 4 : Listening and Notes taking	Effective listening and notes taking	CCF201-3 & 4

13.	Learning Skills - 5 : Studying Skills	Effective self-studying	CCF201-5
14.	Technical Skills - 1 : Calculating Skills	Efficient use of calculator	CCF201-5
15.	Technical Skills - 1 : Text-graphic Conversion	Graphic and language skills	CCF201-5
16.	Aesthetic Skills	Attitude of aesthetic presentation	CCF201-6
17.	Behavioral Skills	Behavior, mannerism and etiquettes	CCF201-6
18.	Creativity Skills	Attitude of creativity and innovation	CCF201-6
19.	Self-motivated Activities	Self-motivation Skills	CCF201-6

### **C. INDUSTRIAL EXPOSURE:**

(Included in *Workbook on Generic Skills*)

SN	Mode of Exposure	Topic
1.	Self-motivated Activities	Industrial survey, information collection, Biographies of industrialists, etc.
2.	Recitation Exercises	Articles on industrial scenario and issues
3.	Domain Analysis	Analysis of field / industrial activities

### **ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION**

#### **f) Term work :**

##### **i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per criteria given in *Workbook on Generic Skills*.

Domain	Particulars	Marks out of 25
Cognitive	Understanding	02
	Application	02
Psychomotor	Presentation Skills	04
	Drafting skills	05
Affective	Discipline and punctuality	06
	Decency	06
<b>TOTAL</b>		<b>25</b>

##### **ii) Progressive Skill Test:**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given in *Workbook on Generic Skills*

Final marks of termwork shall be awarded as per *Assessment Pro-forma VI*

#### **g) Term-end Practical Examination :**

*Term-end Practical Examination* shall be conducted by internal examiner (course teacher) and external examiner (course teacher of different class from the Institute) as per the following criteria :

Skills >	Concentration Skills	Language Skills	Learning Skills	Technical Skills	Aesthetic, behavioral and creativity skills	Total	Marks converted out of ...
Marks >	20	20	20	20	20	100	50

## **INSTRUCTIONAL STRATEGIES:**

### **Instructional Methods:**

1. Lectures cum Demonstrations      2. Classroom practices

### **Teaching and Learning resources:**

1. Chalk board      2. LCD presentations    3. Audio presentations      4. Item Bank

## **REFERENCE MATERIAL :**

### **a) Books / Journals / IS Codes**

<b>Sr. No.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	K. Sudhesh	Development of Generic Skills	Nandu Printers & Pub,M'bai
2.	Kulkarni/Sharma	Independent Study Techniques	
3.	E.H.McGrath	Basic Managerial Skills for all	McGraw Hill Pub., New Delhi
4.	Sahukar&Bhalla	The book of Etiquette and Manners	Pustak Mahal, New Delhi
5.	Jeanne E.O.	Human Learning	Pearson Publishers, Mumbai
6.	Kenneth/Dubois	Learning to Learn	Pearson Publishers, Mumbai
7.	Fred Luthans	Organizational Behavior	McGraw-Hill Higher Edu.

### **b) Websites**

- i) *www.mindtools.com*
- ii) *www.samcerto.com*
- iii) *www.stress.org.uk*
- iv) *www.yogapoint.com*

\* \* \*



**COURSE ID:**

Course Name : COMMUNICATION SKILLS  
Course Code : CCF202  
Course Abbreviation : FCMS

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s)** : <nil>

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	02	04
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Details of Evaluation</b>	Average of two tests of 20 marks each to be converted out of 10 marks	One mid-term Skill Test(2 hrs)	Term End Theory Exam (1½ hours)	Term End Practical Exam (2 hours)	As per Proforma II.	
<b>Marks</b>	<b>10</b>	<b>--</b>	<b>40</b>	<b>25</b>	<b>--</b>	<b>75</b>

\* Practical Examination to be conducted by internal examiner (course teacher) and external examiner (course teacher of different class from the Institute) and marks to be entered as per Proforma II.

**RATIONALE:**

Communication being an integral part of every personal and professional human activity, communication skills plays a fundamental role in education as well as technology. As a unanimous feedback from the industry in general, technicians need to be specially strengthened in generic communication skills for their effectiveness in profession and career. Considering the age group and socio-economical background of the students of the Institute, this course has been designed with a skill-oriented content with some necessary theoretical foundation. For mastery and perfection in these skills, consistent practice and integrated application is necessary in all subjects of the Programme.

**COMPETENCY :**

Apply principles of communication to communicate in formal and informal scenario as follows:

**Cognitive :** Understanding and applying principles of communication in various situations

**Psychomotor :** i) Use of correct pronunciation, tone, accent & intonation ii) writing formal letters, drafts, reports etc. iii) Use of correct nonverbal code in formal & informal situations iv) Speaking in formal & informal situations

**Affective :** Attitude of i) perfection ii) iii) confidence iv) punctuality v) aesthetic presentation

## COURSE OUTCOMES :

- CCF202-1 Identify his/her communication barriers  
 CCF202-2 converse and convince by speaking, deliver prepared & extempore speech  
 CCF202-3 write letters, reports, resume in correct language  
 CCF202-4 Make effective use of body language & graphic communication  
 CCF202-5 Prepare and present simple media aided presentation  
 CCF202-6 Prepare and face mock interview

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs										PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency</b> Apply principles of communication to communicate in formal and informal scenario	-	-	-	-	1	-	-	2	3	1		
CCF202-1 Identify his/her communication barrier	-	-	-	-	1	-	-	1	2	-		
CCF202-2 converse and convince by speaking, deliver prepared & extempore speech	-	-	-	-	2	-	1	2	3	1		
CCF202-3 write letters, reports, resume in correct language	-	-	-	-	1	-	1	2	2	1		
CCF202-4 Make effective use of body language & graphic communication	-	-	-	-	1	-	2	2	2	1		
CCF202-5 Prepare and present simple media aided presentation	-	-	-	-	1	-	-	-	2	1		
CCF202-6 Prepare and face mock interview	-	-	-	-	-	-	1	-	2	-		

**CONTENT:**

**C. THEORY :**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	<b><i>CCF202-1 Identify his/her communication barriers</i></b>		
<b>1</b>	<b>Fundamentals of Communication</b>  1.1 Definition of communication by Newman and Peter Little. Importance communication 1.2 Model of communication: Sender-Message-Channel-Receiver-Feedback cycle. Encoding and decoding 1.3 Principles of effective communication 1.4 Types of communication 1.5 Barriers in communication	<b>08</b>	<b>12</b>
	<b><i>CCF202-2 Converse and convince by speaking, deliver prepared &amp; extempore speech</i></b>		
<b>2</b>	<b>Oral Communication</b>  2.1 Principles and characteristics of oral communication. 2.2 Tone, pronunciation and accents. Grammar. 2.3 Spoken English: Dialogue, conversation, prepared and extempore speech, discussion, debate, feedback	<b>06</b>	<b>06</b>
	<b><i>CCF202-3 Write letters, reports, resume in correct language</i></b>		
<b>3</b>	<b>Written Communication</b>  3.1 Principles and characteristics of written communication. 3.2 Writing reports, letters, resume and notes.	<b>06</b>	<b>06</b>
	<b><i>CCF202-4 Make effective use of body language &amp; graphic communication</i></b>		
<b>4</b>	<b>Non-verbal communication</b>  4.1 Principles and characteristics of non-verbal Communication. 4.2 Body Language: visual, tactile, auditory, cultural. Silence. 4.3 Graphic Communication: Visual illustration, technical graphic communication.	<b>04</b>	<b>06</b>
	<b><i>CCF202-5 Prepare and present simple media aided presentation</i></b>		
<b>5</b>	<b>Media Aided Presentation</b>	<b>04</b>	<b>06</b>

	5.1 Media aids for presentation: strengths and precautions 5.2 Planning, preparing and making a presentation 5.3 Use of presentation media: OHP, computer, MS PowerPoint, LCD, board, charts		
	<b>CCF202-6 Prepare and face mock interview</b>		
<b>6</b>	<b>Interview Techniques</b>  6.1 Preparing for an interview 6.2 Taking a mock interview and facing an interview	<b>04</b>	<b>04</b>
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course outcome	Total Marks
		Remember	Understand	Application		
1	Fundamentals of Communication	02	06	04	CCF202-1	12
2	Oral Communication	02	02	02	CCF202-2	06
3	Written Communication	02	02	02	CCF202-3	06
4	Non-verbal Communication	02	02	02	CCF202-4	06
5	Media aided presentation	02	02	02	CCF202-5	06
6	Interview Techniques	00	02	02	CCF202-6	04
	Total >>	10	16	14		40

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

## **D. TERM WORK**

### **Practical Exercises and related skills to be developed:**

The following practical exercises shall be conducted as Term Work as detailed in the *Workbook on Communication Skills* developed by the Institute in practical sessions of batches of about 22 students:

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Characteristics of Communication Process	Analysis of communication process	CCF202-1
2.	My Communication Barriers	Self analysis	CCF202-1
3.	Verbal Communication : Vocabulary	Improvement in vocabulary	CCF202-2 & 3
4.	Oral Communication : Prepared Speech	Preparing and delivery	CCF202-2
5.	Oral Communication : Extempore Speech	Creative thinking and speaking	CCF202-2
6.	Oral Communication : Conversation	Listening, thinking and speaking	CCF202-2
7.	Oral Communication : Group	Listening, thinking and convincing	CCF202-2

	Discussion		
8.	Oral Communication : Group Debate	Listening, thinking and convincing	CCF202-2
9.	Written Communication : Drafting Skills	Drafting	CCF202-3
10.	Written Communication : Writing formal and Informal Letters	Drafting	CCF202-3
11.	Written Communication : Writing Reports	Drafting with comprehension	CCF202-3
12.	Written Communication : Writing Scripts	Drafting	CCF202-3
13.	Non-verbal Communication : Graphic Communication	Graphic skills	CCF202-4
14.	Non-verbal Communication : Body Language	Body language	CCF202-4
15.	Using Presentation Aids	Using presentation aids	CCF202-5
16.	Interview Techniques	Facing interview	CCF202-6

### **E. INDUSTRIAL EXPOSURE:**

(Included in *Workbook on Communication Skills*)

SN	Mode of Exposure	Topic
1.	Oral and Written Communication Exercises	Industrial situations
2.	Recitation Exercises	Articles on industrial scenario and issues
3.	Interview Techniques Exercises	Industrial situations

## **ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION**

### **h) Assessment Criteria for Term work :**

#### **i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per criteria given in *Workbook on Communication Skills*.

Domain	Particulars	Marks out of 25
Cognitive	Understanding	02
	Application	02
Psychomotor	Presentation Skills	04
	Drafting skills	05
Affective	Discipline and punctuality	06
	Decency	06
<b>TOTAL</b>		<b>25</b>

#### **ii) Progressive Skill Test:**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given in *Workbook on Communication Skills*

Final marks of termwork shall be awarded as per *Assessment Pro-forma II*.

#### **i) Assessment Criteria for Term-end Practical Examination:**

*Term-end Practical Examination* shall be conducted by internal examiner (course teacher) and external examiner (course teacher of different class from the Institute) as per the following criteria

Item >	Oral	Speech	Body Languag e	Language Grammar	Letter Writing	Total	Marks out of
Marks >	20	20	20	20	20	100	25

### **INSTRUCTIONAL STRATEGIES:**

#### **Instructional Methods:**

1. Lectures cum Demonstrations
2. Classroom practices

#### **Teaching and Learning resources:**

1. Chalk board
2. LCD presentations
3. Audio presentations
4. Item Bank

### **REFERENCE MATERIAL :**

#### **a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1.	K. Sudhesh	Development of Generic Skills	Nandu Printers & Pub, M'bai
2.	B.V.Pathak	Communication Skills	NiraliPrakashan
3.	Burgoon Michael	Human Communication	SAGE Publications Inc.
4.	Geoffrey Leech and Jansvartvik	A communicative Grammar of English	Pearson Education ESL
5.	Elizabeth Hiemey	101 ways to better communication	Pustak Mahal
6.	Thomas Huckin and Leslie	Technical Writing and Professional Communication	McGraww Hill College Division

#### **b) Websites**

- i) [www.clrp.cornell.edu/workshops/pdf/communication\\_skills-web.pdf](http://www.clrp.cornell.edu/workshops/pdf/communication_skills-web.pdf)
- ii) [http://depssa.ignou.ac.in/wiki/images/c/ca/Communication\\_skills\\_in\\_English.pdf](http://depssa.ignou.ac.in/wiki/images/c/ca/Communication_skills_in_English.pdf) www
- iii) <http://www.cgg.gov.in/Handbook%20on%20Communication%20Skills.pdf>
- iv) <http://www.stf-media.com/31-0-Presentations.html>

\* \* \*

**COURSE ID:**

**Course Name : PROFESSIONAL PRACTICES**  
**Course Code : CCF203**  
**Course Abbreviation : FPRP**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : <nil >**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	01	03
Practical	02	

**Evaluation Scheme:**

Component	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory	TW	OR*	
Details and Duration	--	One mid-term Skill Test(2 hrs)	No Term End Theory Exam	As per proforma V	Oral Exam.	
Marks	--	--	--	25	50 I	75

\* Oral Examination to be conducted by internal examiner (course teacher) and external examiner (course teacher of different class from the Institute) and marks to be entered as per Proforma V.

**RATIONALE:**

The course *Professional Skills* is in continuation of the courses *Generic Skills* and *Communication Skills* studied in semester I and II respectively. In order to be a successful technician in industry, a diploma holder is required to acquire certain professional skills. These skills shall be studied in this course. An overview and awareness about the world of industry has been provided in Chapter 1. Professional skills like leadership skills, team building, stress and conflict management, time management have been dealt with. Aptitude tests have been introduced. A study of major technological projects in the respective programme discipline has been included in the syllabus. Term work assignments of the course provide the student on-field activities as well as self-learning activities providing professional exposure in order to help develop professional skills.

**COMPETENCY :**

Apply principles of organizational behavioral science for professional skill as follows:

**Cognitive :** Understanding and applying principles of professional practices in various situations

**Psychomotor :** i) Use of correct pronunciation, tone, accent & intonation ii) writing formal letters, drafts, reports etc. iii) Use of correct nonverbal code in formal & informal situations iv) Speaking in formal & informal situations

**Affective :** Attitude of i) perfection ii) iii) confidence iv) punctuality v) aesthetic presentation

## **COURSE OUTCOMES :**

**CCF203-1** Develop awareness about industrial scenario of world and India

**CCF203-2** Acquire professional skills like leadership, stress and conflict management, team building skills

**CCF203-3** Identify major technological project in program discipline.

## **COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
<b>Competency</b> Apply principles of organizational behavioral science for professional skill as follows:	-	-	-	-	2	-	1	1	2	1		
<b>CCF203 -1</b>	-	-	-	-	2	1	-	-	2	1		
<b>CCF203 -2</b>	-	-	-	-	2	-	2	2	3	1		
<b>CCF203 -3</b>	-	1	-	-	2	-	1	1	2	1		



**CONTENT:**

**A) THEORY :**

Sr. No.	Topics / Sub-topics	Lectures (Hours)
<b><i>CCF203-1 Develop awareness about industrial scenario of world and India</i></b>		
<b>1</b>	<b>Industrial Development of India</b> 1.4 Introduction to industrial revolution in the world 1.5 Brief history of industry in India 1.6 Broad categories of industries : Manufacturing industry, service industry 1.7 Present industrial scenario of India : Small scale, medium scale and major industries in the programme discipline 1.8 Major issues related to industrialization	<b>04</b>
<b><i>CCF203-2 Acquire professional skills like leadership, stress and conflict management, team building skills</i></b>		
<b>2</b>	<b>Profession and Professional Skills</b> 2.1 Difference in profession, occupation, business 2.2 Leadership : definition, styles and skills 2.3 Team Building : Types of teams. Characteristics of good team and effective teamwork 2.4 Conflict management : Definition and causes of conflict. Methods of resolution - negotiating, compromising, withdrawal, forcing, engagement 2.5 Self SWOT analysis as a professional technician 2.6 Aptitude test. 2.7 Emotion Management and Interpersonal Skills : Use of yogic processes like yogasanas, yognidra, breathing exercises and pranayam, omkar, meditation for effective handling of emotions and interpersonal relations	<b>06</b>
<b><i>CCF203-3 Identify major technological project in program discipline</i></b>		
<b>3</b>	<b>Industrial Personalities and Major Projects</b> 3.1 Pioneers of Industrial development of India : Brief biography of Sir M. Visvesarrrya and JRD Tata 3.2 Biography and contribution of two great industrial personalities from programme discipline 3.3 Study of 5 major technological projects in the programme discipline	<b>06</b>
	<b>Total</b>	<b>16</b>

## B) TERM WORK

### Practical Exercises and related skills to be developed:

The term work shall consist of a journal containing write ups by students on the following assignments conducted in practical sessions of batches of about 22 students :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	COURSE OUTCOMES
1.	Information Search through internet on Industrial Scenario of India	Information search and interpretation skills	CCF203-1
2.	Information Search through actual visit to MIDCs on classification of industries	Information search and interpretation skills	CCF203
3.	Biography and contribution of Sir M. Visverayya and J.R.D. Tata	Information search and presentation skills	CCF203-3
4.	Biography and contribution of two eminent industrialists from programme discipline	Information search and presentation skills	CCF203-3
5.	Individual SWOT analysis as a professional technician	Self-analysis skills	CCF203-2
6.	Leadership	Leadership skills	CCF203-2
7.	Stress and Conflict Management	Stress and conflict management skills	CCF203-2
8.	Aptitude test	Self-testing skills	CCF203-2
9.	Case study of a major technological project in the programme discipline	Case study skills	CCF203-1,2,3
10.	Breathing exercises, pranayam, omkar chanting and meditation	Attaining calmness of mind and balance of emotions	CCF203-2

## C. INDUSTRIAL EXPOSURE :

(Included in the contents of *Theory* and *Term work*)

SN	Mode of Exposure	Topic
1.	Theory inputs and practical survey	TW Exercise No. 1 and 2
2.	Study of biographies of industrialists	TW Exercise No. 3 and 4
3.	Case study of major industrial project	TW Exercise No. 9

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

### a) Assessment Criteria for Term work :

#### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks

#### ii) Progressive Skill Test:

One mid-term *Progressive Skill Test* of 25 marks shall be conducted.

Final marks of termwork shall be awarded as per *Assessment Pro-forma V*

**b) Term End Oral Examination :**

*Term-end Oral Examination* shall be conducted by internal examiner (course teacher) and external examiner (course teacher of different class from the Institute).

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Demonstrations
2. Classroom practices

**Teaching and Learning resources:**

1. Chalk board
2. LCD presentations
3. Audio presentations

**REFERENCE MATERIAL :**

**a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1.	EH McGrath, SJ	Basic Managerial Skills for all	McGraw Hill
2.	Prakash Iyer	The Secret of Leadership : Stories to Awaken, Inspire and Unleash the Leader Within	

**b) Websites**

- i) *en.wikipedia.org/wiki/Leadership*
- ii) [www.mindtools.com](http://www.mindtools.com)

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# **LEVEL –III**

# **BASIC TECHNOLOGY**

# **COURSES**



**Course Name : APPLIED MATHEMATICS**  
**Course Code : EIF 301**  
**Course Abbreviation : FAMT**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : CCF106**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	04
Tutorial	01	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
<b>Details of Evaluation</b>	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	NIL	NIL	
<b>Marks</b>	<b>20</b>	<b>----</b>	<b>80</b>	NIL	NIL	<b>100</b>

**RATIONALE:**

Mathematics is an important pre-requisite for the development and understanding of engineering and technological concepts. For an engineer and technologist, knowledge of Mathematics is an effective tool to pursue and to master the applications in the engineering and technological fields. Applied mathematics is designed for its applications in engineering and technology. It includes integration, differential equation,. The connection between applied mathematics and its applications in real life can be understood and appreciated. Integral calculus helps in finding the area, mean value R. M. S value etc . Differential equation is used in finding curve, rectilinear motion. The fundamentals of these topics are directly useful in understanding engineering applications in various fields.

**Competency:**

The course should be taught and implemented with the aim to develop the course outcomes (CO's) for the student to acquire the competency needed to apply the mathematical techniques for engineering subjects.

1. Cognitive: understanding and applying principles of mathematics to engineering problems
2. Psychomotor: To prepare charts displaying the area of irregular shapes using the concept of integration, To understand concept of complex numbers and hyperbolic functions
3. Attitude: discipline, consistency, hard work , to concentrate ,accuracy, punctuality, aesthetics

### Course Outcomes(CO's)

**EIF 301-1** Apply the concept of integration to find the area ,Mean value and Root Mean Square values

**EIF 301-2** Solve Differential equation of first order and first degree by various methods and use it to solve various geometrical problems and application to rate and motion of a particle

**EIF 301-3** To understand and solve examples of complex numbers and hyperbolic functions

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs										PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency:</b>	2	-	1	-	-	-	-	-	-	-	-	3
<b>EIF 301-1</b>	2	1	1	1	1	-	1	1	1	1	2	2
<b>EIF 301-2</b>	2	-	1	-	-	-	-	-	-	-	2	1
<b>EIF 301-3</b>	2	-	1	-	-	-	-	-	-	-	1	1



**CONTENT :**

**C) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>EIF 301-1 Apply the concept of integration to find the area ,Mean value and Root Mean Square values</i>			
<b>1</b>	Indefinite Integrals Definition, Standard formulae 1.1 Rules of Integration(without proof), Examples 1.2 Integration by substitution, 1.3 Integration by parts, 1.4 Integration by partial fractions	<b>12</b>	<b>20</b>
<i>EIF 301-1 Apply the concept of integration to find the area ,Mean value and Root Mean Square values</i>			
<b>2</b>	Definite Integrals 2.1 Definition, Examples 2.2 Properties of Definite Integration ( without proof), Examples based on properties	<b>06</b>	<b>10</b>
<i>EIF 301-1 Apply the concept of integration to find the area ,Mean value and Root Mean Square values</i>			
<b>3</b>	Application of Integration 3.1 Area under the curve and 3.2 Area between two curves 3.3 Mean value & R.M.S. value of a function	<b>06</b>	<b>10</b>
<b>Total</b>		<b>24</b>	<b>40</b>
<p><b>1.</b>Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.</p> <p><b>2.</b>In each topic, corresponding applications will be explained.</p>			

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>EIF 301-2 Solve Differential equation of first order and first degree by various methods</i>			
<b>4</b>	Differential equations 4.1 Definition of differential equation 4.2 Order & degree of Differential equations 4.3 Solutions of Differential equations of first order & first degree of following types 4.3.1 Variables separable 4.3.2 Homogenous Equation 4.3.3 Exact equations 4.3.4 Linear Equations	<b>08</b>	<b>16</b>
<i>EIF 301-2 Solve Differential equation of first order and first degree by various methods and use it to solve various geometrical problems and application to rate and motion of a particle</i>			
<b>5</b>	Applications of Differential Equations 5.1 Geometrical application-To find equation of curve 5.2 Application to rates-Displacement, velocity and acceleration of a moving particle	<b>04</b>	<b>04</b>
<i>EIF 301-3 To understand and solve examples of complex numbers and hyperbolic functions</i>			
<b>6</b>	Complex numbers 6.1 Definition, Algebra of complex numbers, simple examples 6.2 Argand diagram, Polar form; Exponential form; 6.3 De-Moivre's Theorem, Roots of a complex number 6.4 Euler's Theorem 6.5 Hyperbolic functions, Relation between trigonometric function and hyperbolic function 6.6 separation into real and imaginary parts	<b>12</b>	<b>20</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
<p>1.Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.</p> <p>2.In each topic corresponding applications will be explained</p>			

#### Specification table for setting question paper for semester end theory examination:

Topic No.	Name of topic	Distribution of marks (level wise)			Course Outcome	Total Marks
		Remember	Comprehension	Application		
1	Indefinite Integrals	4	6	10		20
2	Definite Integrals	2	2	6		10
3	Application of Integration	--	--	10		10

4	Differential equations	2	4	10		16
5	Application of diff.equations	--	--	04		04
6	Complex Numbers	4	4	08		20

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

#### **D) TUTORIALS**

Note: Tutorials are to be used to get enough practice [One batch for 20 Students ]

<b>Sr No.</b>	<b>Topic</b>	<b>Tutorial Content (10 problems in each tutorial)</b>
1	Indefinite Integrals	To evaluate Integration using standard formulae, To evaluate Integration using Substitution Method
2	Indefinite Integrals	To evaluate Integration of Various forms.
3	Indefinite Integrals	To evaluate Integration using by Parts rule and Partial fraction method
4	Definite Integrals	To evaluate Define Integration for various forms and using properties.
5	Application of Integration	Apply Integration concepts to find Area ,Mean value, RMS value
6	Differential equations	To determine Order and Degree of D.E.. Examples on V.S. form , Homogeneous form
7	Differential equations	Examples on Linear of D.E and Exact D.E.
8	Aplication of D.E.	Examples on Rates and geometrical applications
9	Complx numbers(1)	Examples of complex numbers
10	Complx numbers(2) Hyperbolic functions	Examples of hyperbolic functions

#### **INSTRUCTIONAL STRATEGIES:**

##### **Instructional Methods:**

1. Lectures and Demonstrations
2. Tutorials

##### **Teaching and Learning resources:**

1. Chalk board
2. Item Bank
- 3.Charts

#### **REFERENCE MATERIAL :**

**a) Books:**

Sr. No.	Author	Title	Publisher
1.	G.V. Kumbhojkar	Engineering Mathematics III	PhadakePrakashan, Kolhapur
2.	Patel, Rawal,	Applied Mathematics	NiraliPrakashan, Pune
3.	P.M.Patil and others	Applied Mathematics	Vision Publication, Pune
4.	Sameer Shah	Applied Mathematics	Tech-Max Publication, Pune
5.	P.N.Wartikar	Applied mathematics	Pune vidyarthiGriha Prakashan , pune
6	H.K.Dass	Higher engineering mathematics	S .Chand publication
7	B.S.Grewal	Higher engineering Mathematics	Khanna publication, New Delhi

**b) Website**

- i) [www.khanacademy.org](http://www.khanacademy.org)
- ii) [www.easycalculation.com](http://www.easycalculation.com)
- iii) [www.math-magic.com](http://www.math-magic.com)

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**COURSE ID:**

**Course Name : APPLIED ELECTRONICS**  
**Course Code : EIF 302**  
**Course Abbreviation : FATX**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : EIF 103 Basic Electronics**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	Pro-forma VI	*Practical (3 hours)	
Marks	20	----	80	25	50E	175

E-External Assessment

\* Assessment as per Pro-forma IV

**RATIONALE:**

Recent advances in electronics have revolutionized the traditional approach to electronic Circuitry. Numbers of modern devices are being continuously introduced and used as components in the Circuitry. The contents of this subject are in continuation of contents of Basic Electronics. It introduces more specialized devices and circuits, their characteristics and functions. It also discusses more examples of the basic processes of an electronic circuit.

**COMPETENCY:**

Apply and build simple electronics circuits in real time.

Cognitive: Understand applications of electronic circuits.

Psychomotor: .Build circuits as per application

Affective: Attitude of i) Logic ii) accuracy

## COURSE OUTCOMES:

EIF302-1. Select type of feedback amplifier as per requirement in industry.

EIF302-2. Identify various sinusoidal oscillators and generate sine wave of fixed frequency required in communication system.

EIF302-3. Analyze various power amplifiers and choose particular power amplifier as per requirement.

EIF302-4. Analyze transistor behavior as a switch and use its switching property in multivibrator applications..

EIF302-5. Use sweep generators in electronics circuits.

EIF302-6. Assemble different wave shaping circuits and change input waveform shape as per requirement

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ Note : Correlation levels :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Apply and build simple electronics circuits in real time	-	2	2	-	-	-	-	1	-	-	2	-
<b>EIF302-1</b>	1	2	2	-	-	-	-	1	-	-	1	1
<b>EIF302-2</b>	1	2	2	-	-	-	-	1	-	-	1	1
<b>EIF302-3</b>	-	2	2	-	-	-	-	1	-	-	3	1
<b>EIF302-4</b>	1	2	2	-	-	-	-	1	-	-	1	-
<b>EIF302-5</b>	-	2	2	-	-	-	-	1	-	-	3	1
<b>EIF302-6</b>	-	2	3	-	-	-	-	1	-	-	3	1

**CONTENTS :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF302-1. Select type of feedback amplifier as per requirement</i></b>			
<b>1</b>	<b>Feedback Amplifier</b> <b>Concept of feedback</b> 1.1 Negative and positive feedback 1.2 Advantages of negative feedback 1.3 Effect of negative feedback on amplifier characteristics 1.4 Types of negative feedback (Only block diagram, comparison) a) Current series feedback b) Voltage series feedback c) Voltage shunt feedback d) Current shunt feedback e) Darlington pair, Darlington amplifier (only introduction) f) Principle of Bootstrapping g) Numericals problems based on feedback formula.	07	12
<b><i>EIF302-2 Identify various sinusoidal oscillators</i></b>			
<b>2.</b>	<b>Sinusoidal Oscillators</b> 2.1 Positive feedback in oscillators 2.2 Barkhausen's Criteria 2.3 Circuit Diagram, Working, frequency formula of following oscillators:- 2.3.1 Tuned circuit Oscillators 2.3.2 Hartley Oscillator 2.3.3 Colpitts oscillator 2.3.4 RC phase-shift oscillator, 2.3.5 Wein Bridge oscillator 2.3.6 Crystal oscillator 2.4 Frequency stability consideration 2.5 Numericals based on oscillator frequency formulae	07	12
<b><i>EIF302-3. Apply particular power amplifier as per requirement</i></b>			
<b>3</b>	<b>Power and Tuned Amplifier</b> 3.1 Classification of Power Amplifier 3.2 Circuit Diagram, Working, Characteristics, Efficiency of following: 3.2.1 Class A Power Amplifier - Transformer coupled P A.	10	16

	3.2.2 Class B Power Amplifier and Crossover distortion 3.2.3 Class B Push pull amplifier 3.2.4 Complementary symmetry Class B Push Pull amplifier 3.2. Class C amplifier(only introduction) 3.3 Tuned Amplifiers 3.3.1 Series and parallel connections of tank circuit 3.3.2 Frequency response of tuned amplifier 3.3.3 Single tuned amplifier 3.3.4 Double tuned amplifier		
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF302-4 Analyze transistor behavior as a switch.</i></b>			
<b>4</b>	<b>BJT Switching Circuits</b> 4.1 Transistor as a switch, Transistor Switching Times 4.2 Transistorized Multivibrators and its types:- Circuit Diagram, Operation, timing equations & applications of following:- 4.2.1 Astable multivibrator 4.2.2 Monostable multivibrator 4.2.3 Bistable Multivibrator 4.2.4 Schmitt Trigger 4.3 Numericals problems based on timing equations	08	14
<b><i>EIF302-5. Use sweep generators in electronics circuits.</i></b>			
<b>5</b>	<b>Sweep Generators</b> 5.1 Voltage timebase generator 5.1.1 Exponential sweep circuit 5.1.2 Transistor switch sweep generator 5.1.3 UJT switch sweep generator 5.1.4 Miller sweep generator 5.1.5 Bootstrap sweep generator 5.2 Current sweep generator -Transistorized constant current sweep generator	08	14
<b><i>EIF302-6 Assemble and check output of different wave shaping circuits</i></b>			
<b>6</b>	<b>Wave shaping Circuits</b>		



<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
	Circuit diagram, waveforms and operation of following:- 6.1 Linear wave shaping circuits. 6.1.1 Differentiator - High pass R--C circuits -Response to triangular input & square wave 6.1.2 Integrator-- Low pass RC circuit – Response to square input & rectangular input 6.2 Nonlinear wave shaping 6.2.1 Clippers 6.2.1.1 Positive clipper 6.2.1.2 Negative clipper 6.2.1.3 Combinational clipper 6.2.2 Clampers 6.2.2.1 Positive clampers 6.2.2.2 Negative clampers 6.2.2.3 Voltage doublers and triplers.	08	12
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Sr. No.	Name of the Topic	Distribution Of Marks ( Cognitive Level- wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1.	Feedback Amplifiers	04	04	04	EIF302-1	12
2	Sinusoidal Oscillators	04	04	04	EIF 302-2	12
3	Power Amplifiers & tuned amplifiers	06	06	04	EIF 302-3	16
4	BJT switching circuits	04	04	06	EIF 302-4	14
5	Sweep Generators	04	04	06	EIF 302-5	14
6	Wave shaping Circuits	04	04	04	EIF 302-6	12
<b>TOTAL</b>		26	26	28		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**

**Practical Exercises and related skills to be developed :**

The following practical exercises shall be conducted as Term Work as detailed in the *Laboratory Manual for Applied Electronics* developed by the Institute in practical sessions of batches of about 22 students :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Clipping circuit waveform observation.	1) To observe Clipping waveform on CRO	EIF 302-6
2.	Astable multivibrator using transistor : time measurement and calculations.	1) To generate square wave form on CRO 2) To measure time period on CRO and verify it by using calculation formula	EIF 302-4
3.	Monostable multivibrator using transistor : time measurement and calculations	1) To measure time period on CRO and verify it by using calculation formula	EIF 302-4
4.	Bistable multivibrator using transistor	1) To check transistor switching action.	EIF 302-4
5.	Schmitt trigger using transistor	1) To check how the input waveform is converted in to the square wave	EIF 302-4
6.	Any type of LC oscillator using BJT: frequency calculation.	1) Calculate the oscillator frequency	EIF 302-2
7.	Any type of RC oscillator: frequency calculation.	1) Calculate the oscillator frequency	EIF 302-2
8.	Study of differentiator	a) To study RC circuit as an differentiator	EIF 302-6

		b) To check applications of differentiator as waveshaping circuit(response of the circuit for square and triangular input)	
9.	Study of voltage double	1) To study how the output voltage gets doubled the input voltage.	EIF 302-6
10.	Study of Clamping circuit :waveform observation	1)Observe Clamping waveform on CRO	EIF 302-6
11.	Study of integrator	a)To study RC circuit as an integrator b)To check applications of integrator as waveshaping circuit(response of the circuit for square input)	EIF 302-6
12.	Mini Project(Compulsory)	Build any circuit on general PCB in a group of 3 students	-

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### j) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given .

#### k) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Crite ria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

Assessment at semester end practical exam as per Pro-forma I.

### INSTRUCTIONAL STRATEGIES:

#### Instructional Methods:

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides                      4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

<b>Sr. No.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	Applied electronics	R.S.Sedha	S. Chand and Co.
2.	Electronics principles	A.P.Malvino	Tata McGraw-Hill
3	Applied electronics	G.K.Mithal	Khanna
4	Basic electronics	B.L.Theraja	S. Chand and Co.
5	Principles Of Electronics	V.K.Mehata	S. Chand and Co.

**b) Websites:**

- 1) <http://www.electronics-tutorials.ws/>  
2) [www.nptel.ac.in](http://www.nptel.ac.in)

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**COURSE ID:**

**Course Name** : Electronics Measurements and Instruments  
**Course Code** : EIF 303  
**Course Abbreviation** : FEMI

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s)** : NIL

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
<b>Details of Evaluation</b>	Average of two tests of 20 marks each	iii. 25 marks for each practical iv. One PST of 25 marks	Term End Theory Exam (03 hours)	As per proforma II	Internal Oral Exam	
<b>Marks</b>	<b>20</b>	<b>--</b>	<b>80</b>		<b>25I</b>	<b>125</b>

I-External Assessment

\* Assessment as per Pro-forma II

**RATIONALE :**

This subject intends to provide the students practical information & technical background. It also provides the students with concepts, principles and procedures of Analog and Digital electronic measuring instruments and the measurement techniques for the measurement of various electronic quantities. Because of the scope of the subject, students are well exposed to a good and wide area of the various electronic measuring instruments as the subject comprises of those basic equipment of which students should have knowledge.

This subject presumes that the students are familiar with basic utilization of measuring instruments. The era of this subject consists of the information about concepts, principles and Procedures of analog and digital electronic measuring instruments and measuring techniques.

**COMPETENCY:**

Illustrate practical information & technical background for different engineering applications.

Cognitive: Differentiate different types of measuring instruments.

Psychomotor: Calibrate different types of measuring instrument.

Affective: Attitude of i) accuracy ii) precision iii) punctuality iv) knowledge.

**COURSE OUTCOMES :**

EIF303-1 Define static and dynamic characteristics of measuring instruments.

EIF303-2 Determine R, L,C using different bridges .

EIF303-3 Ability to use and monitor digital meters.

EIF303-4 Ability to measure frequency, phase, voltage with oscilloscope.

EIF303-5 Describe signal generator and function generator.

EIF303-6 Collect the specification of Recorders.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
Competency: Illustrate practical information & technical back ground for different engineering applications	2	1	1	-	-	-	-	1	-	-	3	3
EIF303-1	2	-	1	-	-	-	-	1	-	-	2	1
EIF303-2	2	-	1	-	-	-	-	1	-	-	2	1
EIF303-3	2	-	1	-	-	-	-	1	-	-	3	2
EIF303-4	2	-	1	-	-	-	-	1	-	-	3	3
EIF303-5	-	2	1	-	-	-	-	1	-	-	3	3
EIF303-6	-	2	1	-	-	-	-	1	-	-	3	3

**CONTENT :**

**A) THEORY**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF303-1 Define static and dynamic characteristics of measuring instruments. Basics of Measurement:</i></b>			
<b>1</b>	<b>Basics of Measurement</b> 1.1 Classification of Instruments: Absolute , Secondary Instruments 1.2 Definitions of Static characteristics of Instruments: Accuracy, Precision, Sensitivity, Resolution, Static error, Reproducibility, Drift, Dead Zone 1.3 Definitions of dynamic characteristics of Instruments: Speed of response, Lag, fidelity, Dynamic error 1.4 Types of Errors- Gross, Systemic, Random 1.5 Units of measurement of fundamental quantity 1.6 Definition of Standards and their classification: International, Primary, Secondary. 1.7 Calibration: Definition, Need of calibration	06	08
<b><i>EIF303-2 Determine R, L,C using different bridges.</i></b>			
<b>2.</b>	<b>Ac/Dc Bridges &amp; Their Applications</b> 2.1 Bridge balance condition for DC bridge 2.2 Study of following Dc bridges : 2.2.1 Whetstone's bridge 2.2.2 Guarded whetstone's bridge. 2.2.3 Kelvin's bridge 2.3 Bridge balance condition for AC bridge 2.4 Study of following AC bridges. 2.4.1 Capacitance comparison bridge 2.4.2 Inductance comparison bridge 2.4.3 Maxwell's bridge. 2.4.4 Hay bridge 2.4.5 Schering's bridge 2.4.6 Wien's bridge. 2.5 Wagner ground connection	09	16
<b><i>EIF303-3 Ability to use and monitor digital meters..</i></b>			
<b>3</b>	<b>Digital Meters:</b> 3.1 Concepts of ADC & DAC only 3.2 Advantages and Disadvantages of Digital Instruments and comparison with analog instruments 3.3 Definition of Average & RMS value. 3.4 PMMC- Working Principle, Construction, Sources of torque. 3.5 Resolution, Sensitivity and Accuracy of digital display. 3.6 Digital Voltmeter-Ramp type DVM, Integrating type	09	16

	DVM, Successive approximation type DVM, Dual slope type DVM. (Block diagram, Operation and waveform if required). 3.7 Digital Multi-meter- Concept of 3. - 1/2 digit 3.8 Digital frequency meter 3.8.1 Basic block diagram of digital frequency meter 3.8.2 Basic block diagram for measurement of Frequency Showing Gate control Flip-Flop. 3.8.3 Time period measurement. 3.8.4 ratio measurement 3.9LCR, Q meter- Block diagram and operation only		
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF303-4 Ability to measure frequency, phase, voltage with oscilloscope.</i></b>			
<b>4</b>	<b>Oscilloscope</b> 4.1 Oscilloscope subsystems- 4.1.1 Display subsystems- CRT, Deflection of electron beam in CRT, Electrostatic and Electromagnetic deflection, sensitivity. 4.1.2 Vertical deflection subsystems- Input Coupling selector, Input attenuator, Pre-amplifier, Main vertical amplifier, delay line. 4.1.3 Horizontal deflection subsystems- Trigger circuit, Time base generator, Main Horizontal amplifier. 4.1.4 CRO Probes- General block diagram of CRO probe, passive voltage probe, and their compensation, Active Voltage probes, current probes. 4.1.5 Calibration circuits. 4.2 CRO-Block diagram of single beam dual trace and dual beam oscilloscope. 4.3 Block diagram of Digital storage oscilloscope. Uses of CRO- Frequency and phase measurement, Tracing of diode and transistor characteristics	12	20
<b><i>EIF303-5 Describe signal generator and function generator</i></b>			
<b>5</b>	<b>Signal Generator and Analyzer</b> 5.1 Concept of oscillator. 5.2 Signal generator-AF and RF type- Block diagram and Operation only. 5.3 Function generator and pulse generator- Block	08	12



<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
	diagram, Simple controls and operation only. 5.4 Specification. 5.5 Concept of time domain and frequency domain Instruments. 5.6 Spectrum & Logic analyzer- Block diagram and Operation only.		
<b>EIF303-6 Collect the specification of Recorders.</b>			
<b>6</b>	<b>Recorders</b> 6.1 Classification of recorders and necessity of Recorders. 6.2 Strip chart recorder 6.2.1 Block diagram of strip chart recorder and Function of each block. 6.2.2 Application 6.3 X - Y recorder 6.3.1 Block diagram of X - Y recorder and function of each block 6.3.2 Application 6.4 Circular chart recorder 6.5 Features of recorders.	04	08
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

<b>Topic No.</b>	<b>Name of topic</b>	<b>Distribution of marks (Cognitive level-wise)</b>			<b>Course Outcome</b>	<b>Total Marks</b>
		<b>Remember</b>	<b>Understand</b>	<b>Application</b>		
1	Basics of Measurement:	04	02	02	EIF303-1	08
2	AC/DC bridges & applications	04	04	08	EIF303-2	16
3	Digital Meters	04	04	08	EIF303-3	16
4	Oscilloscope	06	06	08	EIF303-4	20
5	Signal generators & analyzers	02	08	02	EIF303-5	12
6	Recorders	02	04	02	EIF303-6	08
<b>TOTAL</b>		<b>22</b>	<b>28</b>	<b>30</b>		<b>80</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**

**Practical Exercises and related skills to be developed:**

<b>Sr No.</b>	<b>Title of Practical Exercise</b>	<b>Skills / Competencies to be developed</b>	<b>Course Outcome</b>
1.	Using analog and digital Multimeter measurement of current, voltage, resistance.	1. Connect power supply 2. Check voltage, current, & resistance using multi-meter.	EIF303-1
2.	Using LCR--Q meter measurement of different values of inductor, Capacitor, resistor component's value	1. start LCR-Q meter in idle mode 2. measure resistor, inductor, & capacitor at different ranges	EIF303-3
3.	Use of CRO as component tester	1. Connect CRO & adjust intensity and focus 2. Test different components like diode, transistor etc. 3. Know front panel of CRO	EIF303-4
4.	Use of CRO for measurement of AC -- DC voltage & frequency	1. Connect CRO & adjust intensity and focus 2. Measure AC, DC voltage 3. Measure time period & frequency	EIF303-4
5.	Use of CRO for measurement of phase & frequency using Lissajous figures	1. Connect CRO & adjust intensity and focus 2. Measure phase difference & frequency ratio using Lissajous figure	EIF303-4
6.	Study of function generator	1. connect function generator 2. Know front panel of function generator 3. Check different output functions using CRO	EIF303-5
7.	Study of detectors for AC bridge like headphone	1. Connect circuit of AC bridge as per diagram 2. Detect the balancing condition	EIF303-2
8.	Study of whetstone's bridge for measurement of unknown resistance	1. Connect circuit of Wheatstone bridge as per diagram 2. Detect balancing condition 3. Find out unknown resistance	EIF303-2
9.	Measurement of unknown capacitance using bridge	1. Connect circuit of AC bridge as per diagram 2. Detect balancing condition 3. Find out unknown capacitance	EIF303-2
10.	Measurement of unknown inductance using bridge	1. Connect circuit of AC bridge as per diagram 2. Detect balancing condition 3. Find out unknown inductance	EIF303-2
11.	Study of frequency meter	1. Connect frequency meter 2. Know front panel of frequency meter 3. Measure different frequencies of signal	EIF303-3

#### **ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION**

**l) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test :**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma II*.

**m) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

Assessment at semester end practical exam as per Pro-forma II.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books**

Sr. No.	Author	Title	Publisher
1.	W.D. Cooper	Modern Electronic Instrumentation &	Pearson Education,

		Measurement Techniques	
2.	H.S.Kalsi	Electronic Instruments	Tata Mc Grow Hill
3	A.K. Sawhney	Electrical & Electronic Measurements & Instrumentations	Dhanpat Rai & Co

**b) Websites:**

**1) [www.tatamcgrawhill.com](http://www.tatamcgrawhill.com)**

\* \* \*

**COURSE ID:**

Course Name : **C PROGRAMMING**  
 Course Code : **EIF 304**  
 Course Abbreviation : **FCPR**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	02	04
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Practical	Term Work	
<b>Details of Evaluation</b>	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	One practical (3 Hours) As per Proforma -IV	As per Proforma-VI	
<b>Marks</b>	<b>NIL</b>	<b>--</b>	<b>NIL</b>	<b>50E</b>	<b>25</b>	<b>75</b>

E-External Assessment

**RATIONALE :**

This course is designed to develop programming attitude and attract the interest of the students in the C Language. Every student in Engineering domain must be able to work on a problem in a procedural way so it is useful to study structured and dynamic language like C.

**COMPETENCY:**

Develop programming skills for problems in engineering in procedural and modular way in C.

**Cognitive:** Understanding C programming language fundamentals, constructs and its advanced features.

**Psychomotor:** Write programs in C for wide range of applications.

**Affective:** Attitude of i) Logic ii) accuracy iii) precision v) punctuality

### **COURSE OUTCOMES:**

**EIF304-1** Illustrate structure of a C program and identify various constructs in it with function

**EIF 304-2** Use different C operators and standard library functions

**EIF 304-3** Use control statements to write basic programs to develop logic.

**EIF 304-4** Explore modular programming skills using user defined functions.

**EIF 304-5** Operate data handling using arrays

**EIF 304-6** Do input and output of strings and use string related functions

**EIF 304-7** Illustrate use of structures and pointers

### **COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Develop Programming skills for problems in engineering in procedural and modular way in C	3	1	1	-	-	-	-	-	-	2	1	1
<b>EIF 304-1</b>	3	1	-	-	-	-	-	-	-	2	-	-
<b>EIF 304-2</b>	3	1	-	-	-	-	-	-	-	2	-	-
<b>EIF 304-3</b>	3	1	1	-	-	-	-	-	-	2	1	1
<b>EIF 304-4</b>	3	1	1	-	-	-	-	1	-	2	1	1
<b>EIF 304-5</b>	3	1	1	-	-	-	-	1	-	2	1	1
<b>EIF 304-6</b>	3	1	2	-	-	-	-	1	-	2	1	1
<b>EIF 304-7</b>	3	1	2	-	-	-	-	1	-	2	1	1

**CONTENT:**

**A) THEORY**

Sr. No.	Topics / Sub-topics	Lectures (Hours)
<b><i>EIF304-1 Illustrate structure of a C program and identify various constructs in it with function</i></b>		
<b>1</b>	<b>C FUNDAMENTALS</b> 1.1 History of c 1.2 C character set 1.3 Identifiers & Keywords 1.4 Data types 1.5 Variables 1.6 Declarations 1.7 Constants 1.8 Expressions 1.9 C Instructions 1.10 The first C program 1.11 Compilation & Execution	<b>02</b>
<b><i>EIF304-2 Use different C operators and standard library functions</i></b>		
<b>2</b>	<b>OPERATORS&amp; DATA INPUT AND OUTPUT FUNCTIONS</b> 2.1 Operators 2.1.1 Arithmetic Operators 2.1.2 Assignment Operator 2.1.2 Unary operators 2.1.3 Relational & Logical Operators, 2.1.4 Conditional & Comma Operator 2.2 Simple Statement 2.3 Input and Output Library Functions 2.3.1 printf()                      2.3.2 scanf() 2.3.3 getchar()                  2.3.4 putchar() 2.3.5 gets()                      2.3.6 puts()	<b>04</b>
<b><i>EIF 304-3 Use control statements to write basic programs to develop logic.</i></b>		
<b>3</b>	<b>CONTROL STATEMENTS</b> 3.1 Decision making and branching 3.1.1 if Statement(if, if-else, if-else ladder, nested if-else) 3.1.2 Switch, break, continue, goto statement 3.2 Decision making and looping 3.2.1 While, do – while, for Statements 3.2.2 Nested loops	<b>06</b>
<b><i>EIF304-4 Explore modular programming skills using user defined functions.</i></b>		
<b>4</b>	<b>FUNCTIONS</b> 4.1 Defining a Function, Accessing a function, 4.2 Passing arguments to a Function(call by value), Specifying argument data types 4.3 Scope and lifetime of variables 4.4 Function prototypes 4.5 Recursion	<b>05</b>
<b><i>EIF304-5 Operate data handling using arrays</i></b>		
<b>5</b>	<b>ARRAYS</b>	<b>05</b>

Sr. No.	Topics / Sub-topics	Lectures (Hours)
	5.1 Defining an array, 5.2 One dimensional array ,Declaration and Initialization of Arrays, 5.3 Two Dimensional Arrays Declaration and Initialization of Arrays, 5.4 Passing arrays to a function	
<b>EIF304-6 Do input and output of strings and use string related functions</b>		
<b>6</b>	<b>CHARACTERS &amp; STRINGS</b> 6.1 The char data type, using character variables, using string 6.2 Declaring and initializing string variables, 6.3 Reading strings from terminal 6.4 Writing Strings to screen, putting strings together. 6.5 Comparison of two strings 6.6 String- handling Functions	<b>04</b>
<b>EIF304-7 Illustrate use of structures and pointers</b>		
<b>7</b>	<b>Structures and Pointers</b> 7.1 Simple structures (Defining & declaring structures, accessing structure members) 7.2 Complex structures (structures that contain arrays) 7.3 Arrays of structure, Initializing structure, 7.4 Understanding pointers, declaring pointer variable, initialization of pointer variable, accessing address of a variable	<b>06</b>

## B) TERM WORK

Term work shall consist of the following: **(Minimum Eight From List)**

### Practical Exercises and related skills to be developed :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
01	Study of Flowcharts and Algorithm	1. Studying and writing an Algorithm 2. Use of various Flowchart Symbols 3. Draw Flowchart on any Practical routine	EIF 304-1
02	Character set and Operators, Identifiers, Variables, Constants	1. Studying format of a simple C program 2. Studying various elements of a C program 3. Studying various types of operators 4. Use of various operators 5. Formation and declaration of identifiers, variables, constant, Keyword 6. Differentiation between identifier and Keyword.	EIF 304-1
03	Study of C. Expressions	1. Studying expressions 2. Conversion of mathematical expression to expression in C. 3. Identify valid and invalid C expressions. 4. Checking for Correct invalid Expressions	EIF 304-1



04	Input and output Functions	1. Study of Standard I/O functions- scanf(), printf() 2. Standard Header file 3. Syntax and use of scanf() and printf() function with Example	EIF 304-2
05	Study of Control Statements	1. Conditional and unconditional branching 2. Syntax and use of go to statement 3. Use of forward and backward jumping 4. Program based on goto statement	EIF 304-3
06	Study of control Structure	1. Necessity of control structure 2. Types of control structure Decision making statements, If statement, If-else statement 3. Syntax and flowchart of if statement 4. Program based on if statement	EIF 304-3
07	Study of control structure: switch statement	1. Use of switch statement 2. Syntax and flowchart of switch statement. 3. Significance of break statement in switch case 4. Use of default statement in switch case 5. Program using switch statement	EIF 304-3
08	Study of for Statement	1. Defining loop 2. Syntax and flowchart of for loop 3. Study of execution of for loop 4. Implementing nested for loop 5. Programming based on for loop	EIF 304-3
09	Study of while loop	1. Exit control and Entry control loop 2. Syntax and flowchart of while loop 3. Execution of while loop 4. Program based on while loop	EIF 304-3
10	Study of do while loop	1. Exit control and Entry control loop 2. Syntax and flowchart of do_ while loop 3. Execution of do_while loop 4. Nested do while loop, syntax, flowchart 5. Program based on do_while loop	EIF 304-3
11	Study of function	1. Types of functions 2. User defined Function declaration or prototype 3. Syntax to declare a function 4. Function call 5. Program using functions	EIF 304-4
12	Study of Recursive function	1. Definition of recursion 2. Use of recursion 3. Program using recursion function	EIF 304-4
13	Study of an array	1. Use of an array 2. Size of an array 3. Syntax to declare an array 4. Read and print the elements of an array 5. Access a particular element of an array 6. Any Two programs based on arrays	EIF 304-5
14	Study of two dimensional array	1. Application of two dimensional array 2. Size of 2-D array 3. Syntax to declare 2-D array 4. Read and print the elements of 2-D array 5. Access a particular element of 2-D array	EIF 304-5

		6. Use of two dimensional arrays 7. Program based on above 2-D array	
15	Study of strings using arrays	1. Declaration of string array 2. Reading and printing a string from and to terminal. 3. Syntax and use of : strcat(), strlen(), strcmp(), strcpy() 4. Program based on string functions	EIF 304-5
16	Study of Simple Structure	1. Studying structure 2. Size of structure 3. Declaration and use of variables of structure 4. Declaring and defining a structure 5. Accessing members of structure	EIF 304-6
17	Study of Complex Structure (Structure that contain arrays)	1. Declaration and use of arrays in structure 2. Size of structure 3. Accessing members of array of structure 4. Program based on arrays in structure	EIF 304-6
18	Study of Arrays of Structure	1. Studying arrays of structure 2. Size of structure 3. Declaration and use of arrays of structure 4. Accessing members of structure 5. Program based on structure array	EIF 304-6
19	Study of Pointer	1. Studying pointer 2. Basic difference between variable and pointer 3. Declaration of pointer 4. Initializing pointer variable 5. Program to access address of variable	EIF 304-7

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria.

##### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

#### Criteria for Continuous Assessment of Practical work and Progressive skill Test:

Domain	Particulars	Marks out of 50
Cognitive	Technical preparedness for practical	05
Psychomotor	Algorithm /Flowchart	05
	Program/Logic	05
Affective	Discipline and punctuality	05
	Decency and presentation	05
<b>TOTAL</b>		<b>25</b>

#### b) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria

Domain	Particulars	Marks out of 50
Cognitive	Technical Ability	05
	Logical Approach	05
Psychomotor	Presentation/ Algorithm and Flowchart	10
	Programming skill	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>50</b>

### **INSTRUCTIONAL STRATEGIES :**

#### **Instructional Methods:**

1. Lectures and discussions.
2. Laboratory experiences and laboratory interactive sessions.
3. Time bound assignments.

#### **Teaching and Learning resources:**

1. Books
2. Video tutorials
3. Power Point Presentation
4. Self- learning

### **REFERENCE MATERIAL:**

#### **a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1	E. Balgurusamy	Programming in ANSI C	Tata McGraw Hill Education
2	Yashwant Kanetkar	Let us C	BPB Publications
3	Byron Gottfried	Programming with C	Schaum's Outlines Series

#### **a) Websites**

- 1) <http://cplusplus.about.com/od/beginnerctutorial/a/bctut.htm>
- 2) <http://computer.howstuffworks.com/c.htm>
- 3) <http://www.indiastudycenter.com/studyguides/sc/object/default.asp>



**COURSE ID :****Course Name : ANALOG COMMUNICATION****Course Code : EIF 305****Course Abbreviation : FACM****TEACHING AND EVALUATION SCHEME****Pre-requisites: NIL****Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	3	5
Practical	2	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Practical	Term Work	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	One practical (3 Hours) As per Proforma -II		
Marks	20	--	80	50I	--	150

\*I-Internal Assessment

\* Assessment as per Pro-forma II.

**Rationale:**

It is the basic course which is helpful for understanding higher technologies in Radio Communication. Analog Communication is the subject that presents information about the basic philosophies, processes, circuits and other building blocks of communication system.

**COMPETENCY:**

Explain and Demonstrate, operate and troubleshoot different modulation and demodulation systems.

**Cognitive :** Understanding the fundamentals of different electronics communication systems and illustrate the function of each block.

**Psychomotor :** Demonstrate the operation of modulator and demodulator based on various modulating techniques .

**Affective :** Attitude of i) Logic ii) accuracy iii) soft skills v) punctuality

### COURSE OUTCOMES:

**EIF305-1** To apply the basic knowledge of electronics communication systems to understand the need of modulation - demodulation.

**EIF305-2** Identify/Discover the circuit details of AM modulator and SSB modulator to calculate mathematical parameters of modulated signal.

**EIF305-3** Identify different angle modulation techniques to explore their basic mathematical properties.

**EIF305-4** Observe experimentally various characteristics of radio receivers based on which suggest radio receiver for a typical application.

**EIF305-5** Summarize antenna properties and discuss different types of antenna used for various applications.

**EIF305-6** Discuss effect of atmospheric layers on electromagnetic wave propagation.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Explain, test different modulation and demodulation systems and demonstrate transmission reception process.	2	1	2	1	-	-	-	-	-	-	1	1
<b>EIF305-1</b>	1	-	-	-	-	-	-	-	-	-	1	1
<b>EIF305-2</b>	2	-	2	-	-	-	-	-	-	-	1	1
<b>EIF305-3</b>	2	-	1	-	-	-	-	-	-	-	1	1
<b>EIF305-4</b>	1	2	2	-	-	-	-	-	-	1	1	1
<b>EIF305-5</b>	1	2	1	1	-	-	-	-	-	1	1	1
<b>EIF305-6</b>	-	1	-	-	-	-	-	-	-	-	1	1

**CONTENT:**

**Section I**

Ch No	Name of the Topic	Hours	Theory Marks
<b><i>EIF305-1 Classify electronics communication systems and Define modulation - demodulation.</i></b>			
<b>1</b>	<b>Introduction to electronic communication system</b> 1.1 Elements of Basic electronic communication system. 1.2 Classification of electronic communication systems into Wired and Wireless, Uni-cast and Broadcast, simplex, half duplex and full duplex 1.3 Noise Fundamentals, Types, Noise figure, Noise Temperature, numerical based on noise figure and noise temperature. 1.4 Electromagnetic Frequency spectrum 1.5 Bandwidth and Information Capacity 1.6 Modulation and Demodulation	06	10
<b><i>EIF305-2 Identify/Discover the circuit details of AM modulator and SSB modulator and calculate mathematical parameters of modulated signal.</i></b>			
<b>2</b>	<b>Amplitude Modulation And SSB Techniques.</b> 2.1 Amplitude modulation theory. 2.2 Sidebands, Frequency domain representation and bandwidth of AM wave 2.3 Time domain representation of AM wave and trapezoidal pattern 2.4 Power relation in AM wave. 2.5 Amplitude modulator circuits. 2.6 AM Transmitters – Low level and High level 2.7 Single side band technique (SSB) 2.7.1 Advantages and disadvantages of SSB 2.7.2 Suppression of carrier. 2.7.3 Suppression unwanted side band. 2.8 Concept of vestigial sideband & waveforms 2.9 Numerical problems based on AM & SSB theory.	10	16
<b><i>EIF305-3 Identify different angle modulation techniques and explore their mathematical properties.</i></b>			
<b>3</b>	<b>Angle Modulation and FM Transmitters</b> 3.1 Frequency modulation and Phase modulation theory. 3.2 Mathematical representation of FM and PM 3.3 FM and PM waveforms. 3.4 Difference between FM and PM 3.5 Modulation index, Deviation ratio, Bandwidth, Power considerations 3.6 Generation of FM – Direct and Indirect methods 3.7 FM transmitters - Direct and Indirect 3.8 Comparison between AM and Angle modulation 3.9 Numerical problems based on FM and PM theory.	08	14
	<b>TOTAL</b>	24	40

### Section II

Ch no.	Name of the Topic	Hours	Theory Marks
<b><i>EIF305-4 List various characteristics of radio receivers and describe schematic of each block.</i></b>			
<b>4</b>	<b>Radio Receivers</b> 4.1 AM Receivers 4.1.1 TRF receivers, Super heterodyne receiver. 4.1.3 Receiver performance characteristics 4.1.4 RF section 4.1.5 Frequency mixer and down converters 4.1.6 Image frequency and its rejection 4.1.7 IF amplifiers 4.1.8 AM detector 4.1.9 AGC circuits. 4.2 FM receivers: 4.2.1 FM discriminators – slope detector, ratio detector, PLL detector 4.2.2 Pre-emphasis and De-emphasis	10	18
<b><i>EIF305-5 Summarize antenna properties and discuss different types of antenna.</i></b>			
<b>5</b>	<b>Antennas</b> 5.1 Radiation Mechanism. 5.2 Radiation pattern 5.3 Antenna gain, resistance, polarization, beam width, bandwidth 5.4 Resonant and non-resonant antennas. 5.5 Half wave dipole 5.6 Loop antenna. 5.7 Helical antenna. 5.8 Yagi-Uda antenna. 5.9 Parabolic reflector antenna.	06	10
<b><i>EIF305-6 Discuss effect of atmospheric layers on electromagnetic wave propagation.</i></b>			
<b>6</b>	<b>Electromagnetic Wave Propagation</b> 6.1 Electromagnetic waves and polarization 6.2 Reflection, refraction, diffraction of waves 6.3 Ground (surface) waves propagation. 6.4 Space wave propagation. 6.5 Sky wave propagation 6.5.1 Virtual height. 6.5.2 Critical frequency and critical angle 6.5.3 Skip distance. 6.5.4 Maximum usable frequency. 6.6 Fading	08	12
	<b>TOTAL</b>	24	40



**Specification table for setting question paper for semester end theory examination**

Ch No	Name Of Topic	Distribution Of Marks (Cognitive Level Wise)			Course outcome	Total Marks
		Remember	Understand	Application		
1.	Introduction to electronic communication system	04	06	-	EIF305-1	10
2.	Amplitude modulation and SSB technique.	04	08	04	EIF305-2	16
3.	Angle modulation and FM transmitters	04	06	04	EIF305-3	14
4.	Radio receivers	04	10	04	EIF305-4	18
5.	Antennas	04	06	-	EIF305-5	10
6.	Electromagnetic Wave Propagation	06	06	-	EIF305-6	12
		26	42	12		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B)TERM WORK:** Term work shall consist of the following :

**Laboratory experiments and related skills to be developed:**

Sr. no	Title of Experiment	Skills developed	Course outcome
1	Generation of AM , waveform observation & measurements	Trace the circuit Identify carrier, modulating signal and modulated signal. Observe above waveforms on CRO. Calculate modulating index.	EIF305-2, 3
2	Study trapezoidal pattern	Observe various trapezoidal patterns on CRO. Calculate modulating index using this pattern.	EIF305-2, 3
3	Demodulation of AM using Diode detector	Trace the circuit. Identify modulated and demodulated signal. Observe above waveforms on CRO.	EIF305-2, 3
4	Generation of FM, waveform observation.	Trace the circuit Identify carrier ,modulating signal and modulated signal. Observe above waveforms on CRO. Calculate modulating index.	EIF305-2 , 3
5	Demodulation of FM signal	Observe FM and FM demodulated waveforms on CRO. Observe similarities in modulating signal and demodulated signal.	EIF305-2, 3
6	Visit to Transmitter	Understand AM/FM transmitter and prepare a project report	EIF305-1 - 6
7	Study of super heterodyne radio receiver	Identify different blocks & expected waveforms of super heterodyne radio receiver. Identify different controls and their functions . Observe the waveforms and measure the voltages at different check points.	EIF305-1- 5
8	Fault finding in Super heterodyne receiver	Identify different blocks & expected waveforms of superheterodyne radio receiver. Identify different controls and their functions . Locate faults using voltage and waveform technique at	EIF305-1-5

		different check points and correct them.	
9	To plot selectivity curve for superhetrodyne radio receiver	Give input AM wave from signal generator to the circuit and observe output on CRO . Plot the selectivity & fidelity curve by varying carrier frequency.	EIF305-3,4
10	Study of FM Receiver	Identify different blocks & expected waveforms of slope or ratio detector. Identify different controls and their functions available. Observe the waveforms and measure the voltages at different check points.	EIF305-3,4
11	Study and design Of Yagi-Uda Antenna	Draw diagram of yagi_uda antenna. Identify and describe parts of yagi_uda antenna.	EIF305-5
12	Study and design of Parabolic reflector (Dish Antenna)	Draw diagram of Dish antenna. Identify and describe parts of Dish antenna for particular $\lambda$ .	EIF305-5

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria.

##### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

#### Criteria for Continuous Assessment of Practical work and Progressive Skill Test:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	04
3	Correct figures / diagrams	04
4	Observation tables	04
5	Result table / calculations / graphs	04
6	Safety / use of proper tools	04
	Total	25

#### b) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Presentation	20
2	Conceptual Understanding	20
3	Overall Performance	10
	Total	50

\* Assessment as per Pro-forma II

**INSTRUCTIONAL STRATEGIES:**

- 1) Lectures and discussions.
- 2) Laboratory experiences and laboratory interactive sessions.
- 3) Time bound assignments.

**Teaching and Learning resources, including references:**

- 1) Chalk-board.
- 2) Overhead Projector
- 3) Demonstrative kits.
- 4) Demonstrative charts.
- 5) Books:

**REFERENCE MATERIAL :**

**a) Books / Codes:**

Sr. No.	Author	Title	Publisher
1.	Wayne Tomasi	Electronic Communication Systems	Pearson Publication
2.	Louis Frenzel	Communication Electronics	TMH Publication
3	Kennedy	Electronic Communication Systems	TMH Publication
4	Roddy Coolen	Electronic Communication	PHI Publication

**b) Website :**

1. [www.nptel.ac.in](http://www.nptel.ac.in)
2. [www.antenna-theory.com](http://www.antenna-theory.com)

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**COURSE ID :**

**Course Name : Digital Techniques & Applications**  
**Course Code : EIF 306**  
**Course Abbreviation : FDTA**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	- As per proforma VI	*Practical (3 hours)	
Marks	20	--	80	--	50I	150

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE :** The world around us is fast becoming digital. Digital electronics is widely used in computers, Digital communication & digital instrumentation. A grasp of certain fundamental principles and concepts of digital electronics are essential pre-requisites for electronics engineering.

This course aims to provide these crucial inputs to the students so that they are elevated to a proper platform whereon the study of the systems using digital circuits & micro-processor along with their applications become easy and creative to them.

**COMPETENCY**

Build digital combinational and sequential circuits for different engineering applications.

**Cognitive :** Acquire knowledge of Digital technics and its different applications.

**Psychomotor :** Try to design different logic circuits.

**Affective :** Attitude of i) Logic ii) accuracy iii) control

### COURSE OUTCOMES :

**EIF 306 -1** Classify different number systems and codes to use it properly in future.

**EIF 306-2** Apply the logic and solve the Equations using Boolean laws to provide solution .

**EIF 306-3** Analyze different Logic families & working of logic gates to select in solution of problems .

**EIF 306-4** Explore combination circuits and ICs for logic design in industry.

**EIF 306-5** Explore sequential circuits and ICs for logic design in industry.

**EIF 306-6** Classify and characterize the memory & Develop knowledge of different data converters to use them in different circuits in industry.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency: Build</b> digital combinational and sequential circuits for different engineering applications	2	1	3	-	-	-	-	1	-	1	3	2
<b>EIF 306-1</b>	2	-	-	-	-	-	-	-	-	1	-	-
<b>EIF 306-2</b>	2	-	1	-	-	-	-	1	-	1	-	-
<b>EIF 306--3</b>	1	2	-	-	-	-	-	-	-	1	-	-
<b>EIF 306-4</b>	2	2	3	-	-	-	-	1	-	1	3	3
<b>EIF 306-5</b>	2	3	3	-	-	-	-	1	-	1	3	3
<b>EIF 306-6</b>	2	2	-	-	-	-	-	-	-	1	1	3

**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<b><i>EIF 306-1. Classify different number systems and codes</i></b>			
<b>1</b>	<b>Number system &amp; codes</b> 1.1 Decimal, binary, octal and hexadecimal number systems. 1.2 Decimal to binary and binary to decimal conversion. 1.3 Decimal to octal and octal to decimal conversion. 1.4 Decimal to hex and hex to decimal conversion. 1.5 Binary to hex and hex to binary conversion 1.6 Binary addition 1.7 Binary subtraction using 1's & 2's compliment 1.8 BCD addition & subtraction using 9's and 10's compliment. 1.9 Excess 3 code. 1.10 Gray code and ASCII code	10	12
<b><i>EIF 306-2 Apply the logic and solve the Equations using Boolean laws.</i></b>			
<b>2.</b>	<b>Boolean Algebras.</b> 2.1 Boolean Algebra. Fundamentals of Boolean laws 2.2 Basic gates and derived gates(symbols and truth tables) 2.3 Duality Theorem , De Morgan's Theorems.(upto 3 variables) 2.4 Sum of product and product of sum reduction. 2.5 Maxterm and Minterm, 2.6 Standard conversion between SOP and POS form 2.7 K – map reduction techniques (upto 4 variables)	10	12
<b><i>EIF 306-3 Analyze different Logic families and discover knowledge of working of logic gates.</i></b>			
<b>3</b>	<b>Digital Logic Families</b> a. Study of IC 7400, 7402, 7404, 7408, 7432, 7486 (pinout diagram and features), equivalent circuits of gate. 3.2 Characteristics of logic families (fan in, fan out, propagation delay, power dissipation, noise margin ) 3.3 TTL family: a) Circuit diagram & working of TTL NAND gate 3.4 CMOS family: a) Circuit diagram & working of CMOS inverter b) Circuit diagram & working of CMOS NAND & NOR gates 3.5 ECL family: a) Circuit diagram and working of 3 input	12	16

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<b>EIF 306-1. Classify different number systems and codes</b>			
	NOR/OR gate. 3.6 Comparison of logic families. 3.7 Interfacing TTL TTL to CMOS CMOS to TTL		
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<b>EIF 306-4 Explore combination circuits and ICs for logic design.</b>			
<b>4</b>	<b>Combinational Logic Circuits</b> 4.1 Full and half adder 4.2 Full adder using half adders 4.3 Parallel binary adder 4.4 Study of 4 bits binary adder IC 7483 4.5 One digit BCD adder using IC 7483 4.6 Half and full subtractor 4.7 Study of ALU IC 74181 4.8 Encoder , Multiplexer, their uses in combinational Logic design. 4.9 Study of code decoder , drivers for displays Viz. IC7447 4.10 Study of code converters BCD to binary( IC 74184), Binary to BCD (IC74185),priority encoder(IC 74147). 4.11 Demultiplexers , decoders and their use in combinational logic design. 4.12 Study of IC's 74150 and 74154	14	16
<b>EIF 306-5 Explore sequential circuits and ICs for logic design.</b>			
<b>5</b>	<b>Sequential Logic Circuits</b> 5.1 Circuit diagram, truth table and working of S-R flip-flop, J-K, master-slave J-K, D and T flip-flop. 5.2 Race around condition in J--K flip-flop. 5.3 Levels triggered and edge triggered flip-flop. 5.4 Specifications of edge triggered flip--flop. 5.5 SISO,SIPO, PISO & PIPO modes of operation of Shift register. 5.6 Applications of shift registers. 5.7 Bi-directional shift registers. 5.8 Ripple up counter	12	14



Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	5.9 Ripple down counter 5.10 Ripple UP/DOWN counter. 5.11 MOD 12 ripple counter 5.12 Design of 4 bit synchronous counter using flip-flops. 5.13 Study of IC's 7474, 7490, 74193, 74160, 7476, (logic Diagram and truth table only expected.)		
<b>EIF 306-6. Classify and characterize the memory &amp; Develop knowledge of different data converters.</b>			
<b>6</b>	Data converters and Memories 6.1 DAC Types – Weighted resistor method and R-2R method 6.2 ADC Types, block diagram and working of Single slope ADC, Study of IC 0801 Dual slope ADC, SAR ADC 6.3 IC PCF 8591 : 8 Bit ADC-DAC 6.4 Memory organization and operation 6.5 Classification of memories 6.5 Characteristics of memories 6.6 RAM- Static, Dynamic and Volatile , Nonvolatile 6.7 ROM – PROM, EPROM, EEPROM 6.8 Study of IC's 2716 and 6116. 6.9 Flash memory	6	10
	<b>Total</b>	<b>32</b>	<b>40</b>

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Applica-tion		
1	Number systems and codes	4	6	2	EIF 306-1	12
2	Boolean algebra	-	6	6	EIF 306-2	12
3	Digital logic families	4	4	8	EIF 306-3	16
4	Combinational Logic Circuits	4	6	6	EIF 306-4	16
5	Sequential Logic Circuits	4	6	4	EIF 306-5	14
6	Data converters and Memories	6	-	4	EIF 306-6	10
TOTAL		22	28	30	total	80

**Specification table for setting question paper for semester end theory examination:**

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

## B) TERM WORK

### Practical Exercises and related skills to be developed : (Minimum Eight From List)

The following practical exercises shall be conducted :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Study of Logic gates and De Morgan's theorem	1. Test ICs for truth table. 2. Connect as per circuit and find truth table.	EIF 306-3
2.	Study of truth table of half adder & full adder using half adder	1. Connect circuit as per diagram 2. Check truth table using multimeter	EIF 306-4
3.	Study of truth table of half subtractor & full subtractor using half subtractor	1. Connect circuit as per diagram 2. Check truth table using multimeter	EIF 306-4
4.	Study of adder and subtractor IC using 7483 and 7486	1. Connect power supply 2. Check truth tables using multimeter	EIF 306-4
5.	Study of working of RS/D/T/JK flip-flop devices	1. Connect power supply 2. Connect clock circuit 3. Check truth table using LEDs	EIF 306-5
6.	Study of ripple counter using 7476	1. Connect power supply 2. Connect clock circuit 3. Check truth table using LEDs	EIF 306-5
7.	Study of Decade counter IC 7490	1. Connect power supply 2. Connect clock circuit 3. Check truth table using LEDs	EIF 306-5
8.	Study of synchronous counter	1. Connect power supply 2. Connect clock circuit 3. Check truth table using LEDs	EIF 306-5
9.	Design of full adder using multiplexer.	1. Study and connect mux IC as per circuit. 2. Check output.	EIF 306-5
10.	Study of Multiplexer and demultiplexer	1. Connect power supply 2. Check output using circuit.	EIF 306-5
11.	Study of Encoder and decoder	1. Connect power supply 2. Check output using circuit.	EIF 306-5

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

### n) Assessment Criteria for Term work :

#### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

#### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma VI*.

### o) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Crite ria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

Assessment at semester end practical exam as per Pro- forma II.

## INSTRUCTIONAL STRATEGIES :

### Instructional Methods :

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

### Teaching and Learning resources:

1. Chalk board      2. Video clips      3. Slides      4. Item Bank      5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Malvino and Leach	Digital Principles and Applications:	Tata McGraw-Hill
2.	Malvino	Digital Computer Electronics	Tata McGraw-Hill
3	R. P. Jain	Modern Computer Fundamentals	Tata McGraw-Hill
4	Bartee	Computer Fundamentals	Tata McGraw-Hill
5	Floyd	Digital Fundamentals	Pearson Education

**b) Websites:**

- 1) [www.asic-world.com/digital/tutorial.htm](http://www.asic-world.com/digital/tutorial.htm)
- 2) [www.nptel.ac.in](http://www.nptel.ac.in)

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**COURSE ID :**

**Course Name** : Linear Integrated Circuits  
**Course Code** : EIF307  
**Course Abbreviation** : FLIC

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s)** : EIF103

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per proforma VI	*Practical (3 hours)	
Marks	20	--	80	25	50E	175

E-External Assessment \* Assessment as per Pro-forma IV

& Term Work Assessment as per Pro-forma VI.

**RATIONALE:**

The physical world is inherently analog, indicating that there is always need for analog circuitry. Today the growth of any industry is depending upon electronics to a great extent. Integrated circuit is one of the main component of electronics. This subject acquaints students with general analog principles and design methodologies using practical devices & application.

It focus on process of learning about signal conditioning, signal generation, instrumentation, timing & control using various IC circuitry.

**COMPETENCY**

Design and build circuitry based on IC741 and IC555 for different engineering applications.

**Cognitive** : Understanding working principle of IC 741 & IC555 and its applications.

**Psychomotor** : Constructing and testing circuits based on IC 741 & IC555 for wide range of applications.

**Affective** : Attitude of i) Logic ii) accuracy iii) precision v) punctuality

### COURSE OUTCOMES :

**EIF307-1.** Acquire the basic concepts of operational amplifier and its parameters.

**EIF307-2.** Analyze and design various configuration of op-amp.

**EIF307-3** Analyze and examine various linear and non-linear applications of op-amp.

**EIF307-4** Inspect and interpret circuits of oscillators and multivibrators using op-amp .

**EIF307-5** Analyze operation of active filters and design various types of filters

**EIF307-6** Analyze and interpret operation of timer IC and PLL and its applications.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Design and build circuitry based on IC741 and IC555 for different engineering applications	2	3	2	-	-	-	-	1	-	-	2	2
<b>EIF307-1</b>	2	-	-	-	-	-	-	-	-	-	-	-
<b>EIF307-2</b>	2	-	1	-	-	-	-	1	-	-	2	2
<b>EIF307-3</b>	1	2	2	-	-	-	-	1	-	-	-	-
<b>EIF307-4</b>	-	2	2	-	-	-	-	1	-	-	-	-
<b>EIF307-5</b>	-	3	1	-	-	-	-	1	-	-	2	2
<b>EIF307-6</b>	-	3	2	-	-	-	-	1	-	-	2	2

**CONTENT :**

**THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF307-1. Acquire the basic concepts of operational amplifier and its parameters.</i></b>			
<b>1</b>	<b>Basics Of Operational Amplifier</b> 1.1 Differential amplifier-basics 1.2 Circuit Diagram and circuit description only: DIBO,DIUO,SIBO,SIUO 1.3 Block diagram of OP-Amp: 1.3.1 Input Stage 1.3.2 Intermediate Stage 1.3.3 Level Shifting Stage: Need and circuit diagram 1.3.4 Output Stage: Circuit Diagram 1.4 Op-Amp IC-741 pin diagram and function. 1.5 Equivalent Circuit, Circuit Symbols And Terminals 1.6 Ideal OP-AMP and transfer curve Electrical parameters of 741 1.7 Input offset voltage, Input offset current, Input bias current, Differential input resistance, Input capacitance, Offset voltage adjustment range, Input voltage range, CMRR, SVRR, Large signal voltage gain, Output voltage swing, Output resistance, Output short circuit current, supply current, Power consumption, Slew rate	10	12
<b><i>EIF307-2. Analyze and design various configuration of op-amp.</i></b>			
<b>2.</b>	<b>Op-Amp Configuration and Feedback Amplifiers</b> 2.1 Open Loop and closed loop configuration of op-amp comparison 2.2 Virtual ground, virtual short concept. <b>Circuit Diagram, operation, Equations and derivation for output for following:-</b> 2.3 Open loop configuration – Inverting , Non-inverting 2.4 Close loop configuration – Inverting, non-inverting, 2.5 Voltage follower, Inverter (Sign changer) 2.6 Inverting and non-inverting configuration of Adders (summing amplifier, scaling Amplifier, averaging amplifier) 2.7 Subtractor 2.8 Basic and Practical Integrator 2.9 Basic and Practical Differentiator Numerical Examples on based on above circuits	12	14
<b><i>EIF307-3 Analyze and examine various linear and non-linear applications of op-amp.</i></b>			

<b>3</b>	<b>Op-Amp. Applications</b> <b>( Circuit Diagram , Operation, Equation and applications)</b> 3.1 Precision Rectifier: Half wave and Full Wave 3.2 Voltage to current(V to I) converter with floating load and grounded load 3.3 Current to voltage (I to V) converter 3.4 Three op amp Instrumentation amplifier Circuit diagram, operation, advantages & application 3.5 Voltage comparator: 3.5.1 Inverting & non inverting comparator(transfer characteristics ) 3.5.2 Zero crossing detector-Inverting & Non-inverting 3.5.3 Window comparators(Detector) 3.5.4 Schmitt Trigger- Inverting & Non-inverting 3.5.5 Comparison between voltage comparator and Schmitt trigger	10	14
	<b>Total</b>	<b>32</b>	<b>40</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF307-4 Inspect and interpret circuits of oscillators and multivibrators using op-amp</i></b>			
<b>4</b>	<b>Waveform Generator</b> <b>( Circuit Diagram , Operation &amp; Waveform )</b> 4.1 Op-amp as an astable multivibrator 4.2 Op-amp as monostable multivibrator 4.3 Op-amp as bistable multivibrator 4.4 Triangular waveform generator 4.5 Wien Bridge oscillator using op-amp 4.6 Phase shift oscillator using op-amp 4.7 Quadrature oscillator 4.8 Study of waveform generator IC's IC 566 block diagram, pin diagram, simple circuit.	8	12
<b><i>EIF307-5 Analyze operation of active filters and design various types of filters</i></b>			
<b>5</b>	<b>Active filters</b> 5.1 Introduction to filters ,Classification of filters, 5.2 Concept of passive and active filters 5.3 Merits and demerits of active filters over passive filters 5.4 Definition:-cut off frequency, Pass band, Stop band, center frequency, roll off rate, BW, Q-factor	12	14



Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	5.5 Realistic and ideal response curve of LP, HP, BP, BP, notch filters. 5.6 Order of filter and Need of higher order filter <b>Circuit Diagram, frequency response, operation equation for gain and cut-off frequency(no-derivation) for following filters:-</b> 5.7 First order Butterworth Low pass and high pass filters using op-amp 5.8 Second order Butterworth Low pass and high pass filters using op-amp 5.9 Band pass filter ( wide band pass , narrow band pass filter) 5.10 Band reject filter(wide band reject, narrow band reject filter) Numerical examples on design of op-amp filters 1 <sup>st</sup> order and 2 <sup>nd</sup> order filters(LPF & HPF)		
<b><i>EIF307-6 Analyze and interpret operation of timer IC and PLL and its applications.</i></b>			
6	<b>Timers and PLL</b> <b>6.1 Timer IC's</b> 6.1.1 555 pin out ,block diagram and specification 6.1.2 555 as monostable multivibrator 6.1.3 555 as astable multivibrator 6.1.4 555 as bistable multivibrator 6.1.5 Industrial application IC 555 as Water level controller <b>6.2 PLL</b> 6.2.1 PLL – Block diagram, Operating principle 6.2.2 PLL Transfer Characteristics 6.2.3 Definitions - Center frequency, Lock range, Capture range 6.2.4 IC 565 Block diagram, Pin diagram. And specifications 6.2.5 Applications of PLL Frequency multiplication, Frequency synthesizer .	12	14
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Basics Of Operational Amplifier	02	04	06	EIF 307-1	12
2	Op-Amp Configuration & f/b amplifiers	04	04	06	EIF 307-2	14
3	Op-Amp Applications	04	04	06	EIF 307-3	14
4	Waveform Generators	04	04	04	EIF 307-4	12
5	Active filters	04	06	04	EIF 307-5	14
6	Timers and PLL	04	06	04	EIF 307-6	14
	TOTAL	22	28	30		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**C) TERM WORK**

**Practical Exercises and related skills to be developed :  
(Minimum Eight From List)**

The following practical exercises shall be conducted on trainer kit/bread-board :-

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
12.	OP Amplifier IC 741	i) Identify Pin out of IC 741. ii) Analyze features of IC 741. iii) Define electrical parameters of IC 741.	EIF 307-1
13.	Inverting Amplifier using op-amp	i) Illustrate operation of Open loop and closed loop Inverting Amplifier ii) Measure and verify the output voltage for various gain for Close loop Inverting Amplifier	EIF 307-2
14.	Non Inv. Amplifier using op-amp	i) Illustrate operation of Open loop and closed loop Inverting Amplifier ii) Measure and verify the output voltage for various gain for Close loop Inverting Amplifier	EIF 307-2
15.	Adder, Subtractor using op-amp	i) Analyze and demonstrate operation of adder and subtractor ii) Measure and verify the output voltage for various combination of inputs for inverting and non-inverting adder. iii) Measure and verify the output voltage for various combinations of inputs for subtractor.	EIF 307-2
16.	Active Integrator	i) Analyze and demonstrate operation of integrator ii) Verify and plot the output voltage for square wave, sine wave as a input	EIF 307-3
17.	Active Differentiator	i) Analyze and demonstrate operation of	EIF 307-

		differentiator. ii) Verify and plot the output voltage for square wave, sine wave as a input	3
18.	Astable multivibrator using IC555	i) Analyze and demonstrate operation of astable multivibrator. ii) Verify and plot the output voltage for various RC combination iii) Measure the Ton and Toff i.e duty cycle	EIF 307-6
19.	Monostable multivibrator using IC555	i) Analyze and demonstrate operation of monostable multivibrator. ii) Verify and plot the output voltage for various RC combinations. iii) Measure the Ton and Toff i.e duty cycle	EIF 307-6
20.	Low pass filter using op-amp(1 <sup>st</sup> order)	i) Analyze and demonstrate operation of low pass filter ii) Plot frequency Response of filters on semilog paper. iii) Measure and verify cut-off frequency of LPF.	EIF 307-5
21.	High pass filter using op-amp(1 <sup>st</sup> order)	i) Analyze and demonstrate operation of high pass filter ii) Plot frequency Response of filters on semilog paper. iii) Measure and verify cut-off frequency of HPF.	EIF 307-5
22.	Phase lock loop using IC565	Analyze operation of phase lock loop.	EIF 307-6
23.	Mini Project(Compulsory)	Build any circuit on general PCB in a group of 3 students	EIF 307-1-6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

**p) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test :**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma VI*.

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05

3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

**q) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Crite ria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

Assessment at semester end practical exam as per Pro-forma IV.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions
2. Regular Home Assignments.
3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board
2. Video clips
3. Slides
4. Item Bank
5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Ramakant Gaikwad	Operational Amplifier	Prentice Hall, 2000
2.	K.R.Botkar	Integrated Circuits	Khanna
3	Graeme & Tobey	Operational Amplifier	McgrawHill
4	Clayton	Operational Amplifier	Newnes-Butterworth
5	Driscoll	Basic Op-Amp. Circuits	Prentice Hall, 2000

**b) Websites:**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)
- 2) [www.onlinevideolecture.com](http://www.onlinevideolecture.com)

\* \* \*

**COURSE ID:**

Course Name :Circuits and networks  
Course Code :EIF308  
Course Abbreviation :FCKN

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Oral Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	-----	Oral	
Marks	20	--	80	-----	25I	125

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE:**

Basic circuit theory is the foundation of electronics engineering and technology. The development of all engineering topics requires good understanding of fundamental principles in circuit theory, further studying of which develops scientific methodology in the circuit analysis and technical aptitudes in the students. A good foundation in circuit theory is essential for self-development in future to cope up with the innovations and advancement in technology through self-study.

**COMPETENCY:**

Analyze various circuits and networks for different engineering applications.

**Cognitive:**Understanding circuit behavior.

**Psychomotor:**Determining circuit responses using various network theorems.

**Affective:**Attitude of i) Logic ii) accuracy iii) precision

### COURSE OUTCOMES :

- EIF308-1** Apply different rules and laws for circuit analysis.  
**EIF 308 -2** Differentiate series and parallel circuits.  
**EIF 308 -3** Predict circuit responses using network theorems.  
**EIF 308 -4** Design RC circuits  
**EIF 308-5** Analyze output of resonant circuits.  
**EIF 308-6** Select appropriate filters as per requirement

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Analyze various circuits and networks for different engineering applications	3	1	2	-	-	-	-	1	-	-	2	-
<b>EIF 308-1</b>	3	-	-	-	-	-	-	-	-	-	-	-
<b>EIF 308-2</b>	2	-	-	-	-	-	-	-	-	-	-	-
<b>EIF 308-3</b>	3	1	2	-	-	-	-	1	-	-	-	-
<b>EIF 308-4</b>	2	1	2	-	-	-	-	1	-	-	3	-
<b>EIF 308-5</b>	2	1	2	-	-	-	-	1	-	-	2	-
<b>EIF 308-6</b>	-	3	2	-	-	-	-	1	-	-	-	-

**CONTENT :**

**D) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF 308-1 Apply different rules and laws for circuit analysis</i></b>			
<b>1</b>	<b>Basic Theory</b>  1.1 Electric charge and current 1.2 AC and DC circuits 1.3 Short and open circuit 1.4 Resistance and ohm's law 1.5 Energy and power in resistor circuits 1.6 Kirchhoff's voltage law 1.7 Kirchhoff's current law 1.8 Voltage divider rule 1.9 Current divider rule <b>( Numericals on above topic)</b>	12	14
<b><i>EIF 308-2 Differentiate series and parallel circuits.</i></b>			
<b>2.</b>	<b>Circuit Simplification Techniques</b>  2.1 Series and parallel circuits 2.2 Mesh analysis 2.3 Nodal analysis 2.4 Concept of ground 2.5 Voltage and current source 2.6 Source transformation <b>( Numericals on above topics)</b>	08	12
<b><i>EIF 308-3 Predict circuit responses using network theorems.</i></b>			
<b>3</b>	Network Theorems 3.1 Superposition theorem 3.2 Thevenin's theorem 3.3 Norton's theorem 3.4 Maximum power transfer theorem <b>( Numericals on above topics)</b>	12	14
	<b>Total</b>	<b>32</b>	<b>40</b>

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>EIF 308-4 Design RC circuits</b>			
<b>4</b>	<b>R-L AND R-C CIRCUITS</b> 4.1 Concept of inductor and capacitor 4.2 Concept of impedance and admittance 4.3 Phase relationship between current and voltage in an inductor and capacitor 4.4 Power in inductor and capacitor 4.5 Concept of time constant 4.6 Charging and discharging equations and curves for inductor and capacitor <b>(Numericals on above topic )</b>	12	14
<b>EIF 308-5 Analyse output of resonant circuits</b>			
<b>5</b>	<b>RLC CIRCUITS AND RESONANCE</b> 5.1 Series resonance 5.2 Parallel resonance 5.3 Bandwidth and quality factor 5.4 Applications of resonance circuits like filters, IF amplifiers (only introduction) <b>(Numericals on above topic )</b>	08	10
<b>EIF 308-6 Select appropriate filters as per requirement</b>			
<b>6</b>	<b>PASSIVE FILTERS</b> Necessary diagram and response for following filter types: 6.1 R-L And R-C low pass filter 6.2 R-L And R-C high pass filter 6.3 Series resonant band pass filter 6.4 Parallel resonant band pass filter 6.5 Series resonant band stop filter 6.6 Parallel resonant band stop filter 6.7 Concept of decibel 6.8 Roll off rate	12	16
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Basic theory	4	4	6	EIF 308-1	14
2	Circuit simplification techniques	4	4	4	EIF 308-2	12
3	Network theorems	4	4	6	EIF 308-3	14
4	R-L and R-C circuits	4	4	6	EIF 308-4	14
5	RLC circuits and resonance	3	3	4	EIF 308-5	10
6	Passive filters	5	5	6	EIF 308-6	16
TOTAL	Total	24	24	32	--	80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**E) TERM WORK**

**Practical Exercises and related skills to be developed: (Minimum Eight From List)**

The following practical exercises shall be conducted as Term Work as detailed in the *Laboratory Manual for Applied Mechanics* developed by the Institute in practical sessions of batches of about 22 students :

Sr. No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1	Verify series and parallel combination formulae of resistors	<ul style="list-style-type: none"> <li>Tracing and Connection of circuit</li> <li>Calculation of parameters</li> <li>Formulation of result</li> </ul>	EIF 308-2
2	Verification of Ohm's law	1. Tracing and Connection of circuit 2. Calculation of parameters 3. Formulation of result 4. Plotting of graph	EIF 308-2
3	Verification of KVL	1. Tracing and Connection of circuit 2. Calculation of parameters 3. Formulation of result	EIF 308-2
4	Verification of KCL	1. Tracing and Connection of circuit 2. Calculation of parameters 3. Formulation of result	EIF 308-2
5	Verify Superposition Theorem	1. Tracing and Connection of circuit 2. Calculation of parameters 3. Formulation of result	EIF 308-3

6	Verify Thevenin's theorem	1.Tracing and Connection of circuit 2.Calculation of parameters 3.Formulation of result	EIF 308-3
7	Verify Maximum Power Transfer theorem	1.Tracing and Connection of circuit 2.Calculation of parameters 3.Formulation of result	EIF 308-3
8	Verify Norton's theorem	1.Tracing and Connection of circuit 2.Calculation of parameters 3.Formulation of result	EIF 308-3
9	Frequency response of Low Pass Filter	1.Tracing and Connection of circuit 2.Calculation of parameters 3.Formulation of result 4.Plotting of graph on semilog paper	EIF 308-6
10	Frequency response of High Pass Filter	1.Tracing and Connection of circuit 2.Calculation of parameters 3.Formulation of result 4.Plotting of graph on semilog paper	EIF 308-6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### r) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given

##### s) Criteria for assessment at semester end oral exam :

Every student has to give oral exam at semester end which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for oral	10
2	Correct figures / diagrams	05
3	Correct Answers to questions	10
	Total	25

Assessment at semester end practical exam as per Pro-forma II.

### INSTRUCTIONAL STRATEGIES:

#### Instructional Methods:

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

#### Teaching and Learning resources:

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

### REFERENCE MATERIAL:

#### a) Books / Codes:

Sr. No.	Author	Title	Publisher
1	Sudhakar and Sham Mohan	Circuits and networks	Tata McGraw Hill
2	Ravish R Singh	Electric Networks	Tata McGraw Hill
3	Theodore Bogart	Electric circuits	Macmillan /McGraw Hill

#### b) Websites:

- 1). [www.tatamcgrawhill.com](http://www.tatamcgrawhill.com)
- 2). [www.mhne.com/ravish/ens](http://www.mhne.com/ravish/ens)
- 3). [www.electrical4u.com/rlc-circuit](http://www.electrical4u.com/rlc-circuit)

\* \* \*



**COURSE ID :**

Course Name : 8051 MICROCONTROLLER  
Course Code : EIF 309  
Course Abbreviation : FMCS

**TEACHING AND EVALUATION SCHEME:**

Pre-requisite Course(s) : Nil

Teaching Scheme :

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Practical	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-I	
Marks	20	--	80	50E	150

E-External Examination

**RATIONALE:**

An electronic engineer working in industry has to deal invariably with microcontrollers. In this regard, the course 8051 Microcontroller is designed in such a way that the student undergoing through it will understand not only theoretical background but also design and practical concepts.

The technology of microprocessor has led to a single chip Microcontroller technology MCS-51 family. Architecture, details of 8051 Microcontroller and its programming is covered in this subject. Use of assembler and stimulator for programming of Microcontroller will make the students equipped for the development of embedded systems.

**COMPETENCY:**

Build 8051 microcontroller based systems for different engineering applications.

**Cognitive :** Understanding 8051 microcontroller architecture, working and instruction set.

**Psychomotor :** Write assembly language programs for wide range of applications.

**Affective :** Attitude of i) Logic ii) accuracy iii) precision v) punctuality

### COURSE OUTCOMES:

**EIF 309-1** Differentiate among variety of microcontrollers based on their features.

**EIF 309-2** Identify/Discover the architectural details of 8051 microcontroller and illustrate its functioning.

**EIF 309-3** Use instructions from the instruction set of 8051 to write basic assembly language programs to develop logic.

**EIF 309-4** Explore programming skills for I/O ports, Timers/Counters and interrupts of 8051.

**EIF 309-5** Design interfacing of I/O devices with 8051 and write programs for it.

**EIF 309-6** Develop 8051 microcontroller based systems for various applications.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Build 8051 microcontroller based systems for engineering applications	-	2	1	1	-	-	-	1	-	1	2	1
<b>EIF 309-1</b>	-	1	-	-	-	-	-	-	-	1	-	-
<b>EIF 309-2</b>	-	1	-	-	-	-	-	-	-	1	-	-
<b>EIF 309-3</b>	-	3	1	-	-	-	-	1	-	1	1	-
<b>EIF 309-4</b>	-	3	3	1	-	-	-	1	-	1	3	2
<b>EIF 309-5</b>	-	3	3	1	-	-	-	1	-	1	3	2
<b>EIF 309-6</b>	-	3	3	1	-	-	-	1	-	1	3	2

**CONTENT:**

**THEORY : SECTION I**

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<b><i>EIF 309-1 Differentiate among variety of microcontrollers based on their features.</i></b>			
01	<b>Introduction and Overview of 8051 family</b> 1.1 Introduction to single board microcomputer. 1.1.1 Block Diagram of Microcomputer. 1.1.2 Elements of Microcomputer. (Buses, Microprocessor, memory, I/O devices). 1.1.3 Different types of buses: address, Data, and control bus 1.2 Introduction to Microcontroller 1.2.1 General block diagram of microprocessor and microcontroller 1.2.2 Comparison of Microprocessors and Microcontrollers. 1.2.3 Types of architectures - Harvard and Von-neuman. 1.3 Selection factors of microcontroller (Architecture type, speed, Word size, instruction set, memory, and I/O capability) 1.4 8051 family members and its comparison– 8052, 8031, 8751, AT89C51, DS89C4x0 1.5 Introduction to Microcontroller programming simulation software like – KEIL compiler	06	08
<b><i>EIF 309-2 Identify/Discover the architectural details of 8051 microcontroller and illustrate its functioning.</i></b>			
02	<b>8051 Architecture</b> 2.1 8051 Architecture 2.1.1 Features 2.1.2 Architectural block diagram, 2.1.3 Pin description of 8051 2.1.4 Accumulator and B register, PSW 2.1.5 Memory Organization: Internal RAM and ROM Only architectural features of following 2.1.6 I/O ports 2.1.7 Timers/Counters-TCON,TMOD 2.1.8 Serial port-SCON,PCON,SBUF 2.1.9 Interrupts-IE,IP 2.2 Special Features of 8051 2.2.1 Boolean Processor 2.2.2 Power saving options- idle and power down mode.	12	16
<b><i>EIF 309-3 Use instructions from the instruction set of 8051 to write basic assembly</i></b>			

<i>language programs to develop logic.</i>			
03	<b>Instruction set and programming:</b> 3.1 Instruction format and addressing modes 3.2 Data transfer instructions 3.3 Logical and rotate instructions 3.4 Arithmetic instructions 3.5 Jump and call instructions 3.6 simple programs	14	16
	<b>Total:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## SECTION II

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<b><i>EIF 309-4 Explore programming skills for I/O ports, Timers/Counters and interrupts of 8051.</i></b>			
04	<b>Programming of 8051:</b> 4.1 Timer/Counter programming 4.1.1 Timer / Counter logic and modes 4.2.2 Simple programs on timer to generate time delay using polling and interrupt method. 4.2 Parallel Port-I/O port Structure and its Programming 4.3 Serial port of 8051 4.3.1 Modes of serial communication 4.3.2 Simple programs for serial communication 4.4 8051 Interrupts 4.4.1 Interrupts and polling. 4.4.2 Simple programs based on interrupts and polling method	12	16
<b><i>EIF 309-5 Design interfacing of Memory &amp; I/O devices with 8051 and write programs for it.</i></b>			
05	<b>Memory and I/O Interfacing</b> 5.1 Memory Interfacing- 5.1.1 Interfacing External RAM and ROM 5.2.2 Address Map table 5.2.3 Linear and absolute decoding techniques 5.2.4 Simple example showing interfacing of ROM and RAM(upto 8X8K) 5.2 I/O Interfacing: 5.2.1 Interfacing of LED, keys, Relays, Seven segment display		14



	5.2.2 Port expansion using 8-bit bidirectional buffer like 74LS245		
<b>EIF 309-6 Develop 8051 microcontroller based systems for various applications.</b>			
06	<b>Applications</b> Interfacing diagram with programming of following with 8051 6.1 Key-board interfacing (4X4 Matrix keyboard), concept of key bouncing and de-bounce logic. 6.2 LCD display interfacing 6.3 8 bit ADC and DAC interfacing (0808/0809) 6.4 Stepper Motor interfacing	10	10
	<b>Total:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic no.	Name of the Topic	Distribution Of Marks (Level Wise)			Course Outcome	Total Marks
		Remember	Understand	Applications		
1	Introduction & Overview of 8051 family	6	2	0	EIF 309-1	08
2	8051 Architecture	4	6	6	EIF 309-2	16
3	Instruction set and programming	4	4	8	EIF 309-3	16
4	Programming of 8051	4	2	10	EIF 309-4	16
5	Memory and I/O Interfacing	4	4	6	EIF 309-5	14
6	Applications	2	2	8	EIF 309-6	10
				TOTAL		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

- A) **TERM WORK** Term work shall consist of the following :  
(Minimum Eight From List)

**Practical Exercises and related skills to be developed :**

Sr No	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1	Introduction to KEIL Compiler software	Use KEIL compiler software for assembly language programming 8051 microcontroller	EIF 309-1

<b>Sr No</b>	<b>Title of Practical Exercise</b>	<b>Skills / Competencies to be developed</b>	<b>Course Outcome</b>
2	Addition and subtraction of 8 & 16 bit numbers, 8-bit addition of BCD numbers	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
3	Multiplication and division of 8 bit numbers	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
4	Block transfer and Block Exchange	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
5	Even & odd number	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
6	Largest and smallest number	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
7	Ascending & Descending order	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-3
8	Square wave generation using timer delay	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-4
9	Serial Transmission	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Writing, Compiling, debugging, and execution of program</li> </ol>	EIF 309-4
10	Stepper motor interfacing	<ol style="list-style-type: none"> <li>1. Acquire Logical thinking ability</li> <li>2. Identify the suitable instructions for the given problem</li> <li>3. Interfacing given stepper motor with 8051 MC</li> <li>4. Writing program for rotating motor in clockwise/anticlockwise</li> </ol>	EIF 309-4 , 5

Sr No	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
		5. Compiling, debugging, and execution of program	
11	Display given message (LCD interfacing)	1. Acquire Logical thinking ability 2. Identify the suitable instructions for the given problem 3. Interfacing LCD with 8051 MC 4. Writing, Compiling, debugging, and execution of program	EIF 309-4, 6
12	ADC/DAC Interfacing	1. Acquire Logical thinking ability 2. Identify the suitable instructions for the given problem 3. Interfacing given ADC/DAC with 8051 MC 4. Writing program for conversion, 5. Compiling, debugging, and execution of program	EIF 309-5 6
13	LED, Relay, Keyboard Interfacing	1. Acquire Logical thinking ability 2. Identify the suitable instructions for the given problem 3. Interfacing LED ,Relay ,Keyboard with 8051 MC 4. Writing, Compiling, debugging, and execution of program	EIF 309-5,6
14	Mini Project(Compulsory)	8051 based measurement system having ADC/DAC, LCD,etc (Any application)	EIF309-4,5,6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria.

##### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below.

#### Criteria for Progressive Assessment of Practical and Skill Test

Domain	Particulars	Marks out of 50
Cognitive	Technical preparedness for practical	05
Psychomotor	Algorithm /Flowchart	05
	Program/Logic	05
Affective	Discipline and punctuality	05
	Decency and presentation	05
<b>TOTAL</b>		<b>25</b>

**b) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria

Domain	Particulars	Marks out of 50
Cognitive	Technical Ability	05
	Logical Approach	05
Psychomotor	Presentation/ Algorithm and Flowchart	10
	Programming skill	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>50</b>

Assessment at semester end practical exam as per Pro-forma I

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum discussions
2. Regular home assignments
3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board
2. Video clips
3. PPT Slides
4. Question Bank
5. Charts

**REFERENCE MATERIAL:**

**a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1	Keneth J. Ayala	The 8051 Microcontroller	PenRam International
2	M. A. Mazidi	The 8051 Microcontroller and Embedded systems	Pearson Edition, Prentice Hall
3	Ajay Deshmukh	Microcontrollers (Theory & Applications)	Tata Mcgraw Hill

**b) Websites:**

1. [www.nxp.com](http://www.nxp.com)
2. [www.datasheet.com](http://www.datasheet.com)
3. [www.nptel.ac.in](http://www.nptel.ac.in)

\* \* \*

**COURSE ID :**

**Course Name : DIGITAL COMMUNICATION**  
**Course Code : ETF310**  
**Course Abbreviation : FDCM**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	3	05
Practical	2	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Oral	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-I	
Marks	20	--	80	25I	125

I-Internal Examination \* Assessment as per Pro-forma II.

**RATIONALE :**

Digital communication systems are becoming increasingly attractive because of ever-growing demand for data communication. Digital transmission offers data processing option and flexibility not available with analog transmission. This is technology group subject, which will enable student to comprehend facts, concepts & working principle of digital communication system. This subject familiarizes the student with information theory, measurement of information rate & capacity. This subject helps the student to understand the concept of various pulse modulations, Digital modulation techniques, coding methods and error control, multiplexing & multiple access techniques and S.S. modulation. The knowledge acquired by students will help them to apply it in various modern communication systems.

**COMPETENCY**

Recognize and collect detail knowledge about digital communication system.

**Cognitive** : Acquire basic knowledge of digital communication concepts.

**Psychomotor** : Discover different modulation techniques practically.

**Affective** : Proficiency in knowledge of digitization and communication of signal.

## COURSE OUTCOMES :

- ETF310-1** Acquire knowledge of various concepts in digital communication system.  
**ETF310-2** Distinguish between different pulse modulation techniques.  
**ETF310-3** Differentiate and discover knowledge of digital modulations.  
**ETF310-4** Control the errors by understanding various coding methods.  
**ETF310-5** Analyze Multiplexing and multiple access types.  
**ETF310-6** Classify and discover spread spectrum modulation concept.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Recognize and collect detail knowledge about digital communication system.	2	2	1	-	-	-	-	-	-	-	-	2
<b>ETF310-1</b>	2	-	-	-	-	-	-	-	-	-	-	-
<b>ETF310-2</b>	1	2	2	-	-	-	-	1	-	-	-	-
<b>ETF310-3</b>	1	2	3	-	-	-	-	1	-	-	-	-
<b>ETF310-4</b>	2	1	-	-	-	-	-	-	-	-	-	2
<b>ETF310-5</b>	-	3	-	-	-	-	-	-	-	-	-	2
<b>ETF310-6</b>	2	2	-	-	-	-	-	-	-	-	-	2

**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>ETF310-1 Acquire knowledge of various concepts in digital communication system.</i></b>			
<b>1</b>	<b>Introduction of Digital Communication</b> 1.1 Basic digital communication system( block diagram and function of each block) 1.2 Channel capacity-definition ,equation 1.3 Shannon-Hartley theorem 1.4 channel noise and its effect 1.5 Entropy (definition and equation) 1.6 Advantages and disadvantages of digital communication ,Comparison between analog and digital communication	4	8
<b><i>ETF310-2 Distinguish between different pulse modulation technics.</i></b>			
<b>2.</b>	<b>Pulse Communication</b> 2.1 Introduction, comparison with Continuous Wave Modulation, advantages 2.2 Sampling: sampling theorem, Nyquist rate, and aliasing, natural & flat top. 2.3 PAM, PWM, PPM (definition, generation, block diagram, waveform analysis, and their comparison. ) 2.4 Pulse code modulation- block diagram of PCM transmitter & receiver, 2.5 Quantization, quantization error 2.6 Companding, 2.7 Inter symbol interference 2.8 Delta modulation- block diagram of DM, slope overload, granular noise. 2.9 ADM, DPCM, block diagram and its working.	10	16
<b><i>ETF310-3 Discover knowledge of digital modulations.</i></b>			
<b>3</b>	<b>Digital Modulation Techniques</b> Definition & waveforms, their transmitter and receiver block diagram and working for following types: 3.1 ASK:BASK,QAM 3.2FSK:BFSK ,MFSK 3.3 PSK:BPSK,QPSK,MPSK 3.4 Bandwidth, spectrum for each modulation technique and their comparison.	10	16
	<b>Total</b>	<b>24</b>	<b>40</b>

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>ETF310-4 control the errors by understanding various coding methods.</i></b>			
<b>4</b>	<b>Coding methods and Error control</b> 4.1 Baud rate, Bit rate. 4.2 Line coding - unipolar, bipolar – NRZ, RZ, Manchesterd 4.3 Comparison between different coding 4.4 Source coding, shannon fano coding , Huffman coding. 4.5 Channel coding: Error, Causes of error and its effects, error detection& correction using parity, Hamming code & CRC code simple numerical.	10	16
<b><i>ETF310-5 Analyze Multiplexing and multiple access types.</i></b>			
<b>5</b>	<b>Multiplexing and Multiple Access</b> 5.1 Need of Multiplexing, 5.2 TDM, FDM definition block diagram and their comparison. 5.3 Introduction to WDM. 5.5 OFDM (only concepts), introduction to multiple access (TDMA, FDMA) 5.6 Advantages of TDMA over FDMA.	8	14
<b><i>ETF310-6 Classify and discover spread spectrum modulation concept.</i></b>			
<b>6</b>	<b>Spread spectrum modulation</b> 6.1 Introduction, PN Sequence. 6.2 Model of spread spectrum modulation system. 6.3 Direct sequence spread spectrum signal. 6.4 Frequency hop spread spectrum, slow frequency hopping, and fast frequency hopping. 6.5 Application S. S. modulations.	6	10
	<b>Total</b>	<b>24</b>	<b>40</b>



**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Introduction of Digital Communication	4	-	4	ETF310-1	8
2	Pulse Communication	4	6	6	ETF310-2	16
3	Digital Modulation Techniques	4	6	6	ETF310-3	16
4	Coding methods and Error control	4	6	6	ETF310-4	16
5	Multiplexing and Multiple Access	2	4	8	ETF310-5	14
6	Spread spectrum modulation	4	6	-	ETF310-6	10
TOTAL		22	28	30	total	80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

## **B) TERM WORK**

**Practical Exercises and related skills to be developed :  
(Minimum Eight From List)**

The following practical exercises shall be conducted :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Pulse Amplitude modulation	1) Understand pulse amplitude modulation 2) Understand sampling 3) Analyze the modulated waveforms	ETF310-2
2.	Pulse width modulation	1) Understand pulse width modulation 2) Make connections on kit	ETF310-2

		3) Analyze the modulated waveforms	
3.	Pulse Position modulation	1) Understand pulse Position modulation 2) Make connections on kit 3) Analyze the modulated waveforms	ETF310-2
4.	Pulse code modulation and demodulation.	1) Understand pulse code modulation 2) Make connections on kit 3) Analyze the modulated and demodulated waveforms	ETF310-2
5.	Delta modulation.	1) Understand Delta modulation 2) Make connections on kit 3) Analyze the modulated waveforms	ETF310-3
6.	Adaptive delta Modulation.	1) Understand Adaptive Delta modulation 2) Make connections on kit 3) Analyze the modulated waveforms	ETF310-3
7.	ASK modulation & demodulation.	1) Understand ASK modulation 2) Make connections on kit 3) Analyze the modulated and demodulated waveforms	ETF310-3
8.	FSK modulation & demodulation.	1) Understand FSK modulation 2) Make connections on kit 3) Analyze the modulated and demodulated waveforms	ETF310-3
9.	PSK modulation & demodulation.	1) Understand PSK modulation 2) Make connections on kit 3) Analyze the modulated and demodulated waveforms	ETF310-3
10.	QPSK modulation & demodulation.	1) Understand QPSK modulation 2) Make connections on kit 3) Analyze the modulated and demodulated waveforms	ETF310-3

#### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

##### t) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test :**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma VI*.

**u) Criteria for assessment at semester end oral exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Correct figures / diagrams	10
2	calculations / graphs	10
3	presentation	5
	Total	25

Assessment at semester end practical exam as per Pro-forma II.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board      2. Video clips      3. Slides      4. Item Bank      5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr.No.	Author	Title	Publications
1	Sanjay Sharma	Digital Communication	S.K.Kataria and sons
2	Wayne Tomasi	Electronic communication system	Pearson
3	R.P.Singh,S.D.Sapre	Communication Systems	Tata Mcgraw hill

**b) Websites:**

1. [www.pearsoned.co.in/waynetomasi](http://www.pearsoned.co.in/waynetomasi)  
2. [www.skktariaandsons.com](http://www.skktariaandsons.com)

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**COURSE ID :**

**Course Name : NON- CONVENTIONAL ENERGY SOURCES**  
**Course Code : EIF311**  
**Course Abbreviation : FNCE**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : <nil >**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	04
Practical	01	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	--	One paper (3 hour)	Practical (3 hours)	--	
<b>Marks</b>	20	--	80	--	--	100

**RATIONALE:**

Energy is an important aspect in all sectors of country's economy. The energy crisis is mainly caused due to increased population and enhanced standard of living and life style of people. The conventional sources of energy are insufficient to meet these demands. Hence, alternative energy sources are utilized for power production. The use of alternate energy sources is increasing day by day. Diploma engineers are expected to develop, operate and maintain these systems. It is therefore essential to know basics of energy conversion, conservation, and energy audit and waste heat recovery techniques.

**COMPETENCY:** Develop and operate different non-conventional energy sources

**Cognitive:** Understand importance and scope of various non-conventional energy..

**Psychomotor:** Use of various non-conventional energy conversion equipments .

**Affective:** Develop awareness of energy conservation.

**COURSE OUTCOMES:**

**EIF311-1** Develop awareness for effective utilization of non-conventional energy sources.

**EIF311-2** Describe different components of solar energy and wind energy devices and their functions.

**EIF311-3** Recognize the scope and working of biomass plant.

**EIF311-4** Identify various non-conventional energy sources for energy conservation.

**EIF311-5** Apply energy conservation technique and principles of energy management in industrial sectors.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX:**

[Note: Corelation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“: No correlation]

Competency and COs	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
Competency.	1	-	-	-	1	2	-	-	-	1	-	-
EIF311-1	1	-	-	-	1	2	-	-	-	1	-	-
EIF311-2	1	-	-	-	1	2	-	-	-	1	-	-
EIF311-3	1	-	-	-	1	2	-	-	-	1	-	-
EIF311-4	1	-	-	-	1	2	-	-	-	1	-	-
EIF311-5	1	-	-	-	1	2	-	-	-	1	-	-

**CONTENT:**

**A) THEORY : SECTION-I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	<i><b>EIF311-1 Develop awareness for effective utilization of non-conventional energy sources</b></i>		
<b>1</b>	<b>SOLAR ENERGY</b> 1.1 Major Sources of Energy: Renewable and Non-renewable. 1.2 Need and Prospectus of Alternate Energy Sources 1.3 Solar radiation Geometry: Declination, hour Angle, Altitude angle, incident angle, Zenith angle, solar Azimuth angle 1.4 Instruments for measuring solar radiation 1.5 Construction and working of typical flat plate collector, solar concentrating collector and their applications, Advantages and Limitations 1.6 Solar Photo voltaic Electric Conversion 1.7 Solar pond, solar electric power generation, Solar cooking and furnace.	<b>10</b>	<b>20</b>
	<i><b>EIF311-2 Describe different components of solar energy and wind energy devices and their functions</b></i>		
<b>2</b>	<b>WIND ENERGY</b> 2.1 Principle of Wind energy conversion, Nature of the Wind energy. 2.2 Power in wind, Power coefficient, Maximum power, Wind Energy Conversion 2.3 Selection of site for wind mill. 2.4 Classification of WEC systems. 2.5 Advantages and limitations of WECS 2.6 Basic components of WECS. 2.7 Wind energy collectors- Horizontal and Vertical axis machines, 2.8 Safety systems and Environmental aspects.	<b>08</b>	<b>12</b>
	<i><b>EIF311-3 Recognize the scope and working of biomass plant</b></i>		
<b>3</b>	<b>BIO-MASS ENERGY</b> 3.1 Biomass conversion technologies - 1) combustion 2) Thermo chemical 3) Biochemical. Wet processes, Dry processes. 3.2 Biogas generation – anaerobic digestion, 3.3 Types of Bio-gas plants, KVIC Digester,	<b>06</b>	<b>08</b>

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
	Deenbhandu, Pragati Biogas plant. 3.4 Materials used for biogas generation. Selection of site for biogas plant. Applications.		
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			
<b>SECTION – II</b>			
<b><i>EIF311-4 Identify various non-conventional energy sources for energy conservation</i></b>			
<b>4</b>	<b>ENERGY FROM THE OCEANS</b>  4.1 Ocean Thermal Electric Conversion-Methods of Power Generation,Open and closed cycle OTEC system 4.2 Tidal power –Basic Principle, Components of Tidal Power Plants, 4.3 Operation Methods-single basin and double basin 4.4 Advantages and limitations for tidal power Generation 4.5 Sites Requirements.	<b>06</b>	<b>08</b>
<b><i>EIF311-5 Apply energy conservation technique and principles of energy management in industrial sectors</i></b>			



Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
5	<b>OTHER ALTERNATE SOURCES OF ENERGY</b>  5.1 Geothermal Energy –Sources, Principle, Geothermal energy power plant, Advantages ,Limitation and application of Geothermal Energy. 5.2 Small Hydroelectric Plant(Mini and Micro hydel): Nature, Classification of SHP station, Components of SHP, Advantages and Limitation of SHP 5.3 Fuel Cell- Principle of Operation, Classification, Advantages, Limitation and Application of Fuel cell 5.4 Magneto-Hydro Dynamic(MHD) Power Generation- Principles, MHD system, Advantages, Future Prospects	09	16
6	<b>ENERGY CONSERVATION AND MANAGEMENT</b>  6.1 Energy scenario in various sectors and Indian Economy. 6.2 Need and importance of energy conservation and management. 6.3 Concept of payback period, Return on Investment (ROI), Life cycle cost, Sankey diagrams, Specific Energy consumption 6.4 Principle and methods of energy conservation 6.5 Energy Audit and its types. 6.6 Co-generation and its Application 6.7 Waste Heat Utilization 6.8 Concept and Technique of energy management	09	16
	<b>Total</b>	<b>48</b>	<b>80</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks			Total marks
		Knowledge	Comprehension	Application	
1	Solar Energy	10	04	06	20
2	Wind Energy	04	04	04	12
3	Bio-Mass Energy	04	02	02	08
4	Energy From the Oceans	02	04	02	08
5	Other Alternate Sources of Energy	08	04	04	16
6	Energy Conservation and Management	04	04	08	16
Total >>		32	22	26	80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**

**Practical Exercises and related skills to be developed :**

The following practical exercises shall be conducted as Term Work in practical sessions of batches of about 22 students:

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed
1.	Study and Demonstration on solar flat plate collector used for water heating.	Use and working of Solar flat plate collector.
2.	Study and Demonstration working of photo voltaic cell.	Use and working of Photo voltaic cell.
3.	Demonstration on working of solar cooker.	Using the solar cooker.
4.	Visit to Mini and Micro hydel power plant.	Awareness to hydel power plant
5.	Visit to solar heating system/ wind power plant.	Awareness of solar heating system/ wind power plant.
6.	To study construction and working of horizontal axis wind mill or to visit a nearest wind farm.	Use and working of wind mill.
7.	To study construction and working of a biomass/ biogas plant or visit a biomass/ biogas plant of municipal waste or elsewhere.	Use and working of Biogas plant.
8.	Case study on Energy Audit.	Suggest the methods of energy saving

**C. INDUSTRIAL EXPOSURE:**

SN	Mode of Exposure	Topic
1.	Industrial Visits	Wind Energy and Biomass Energy
2.	Industrial Visits	Solar heating system

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Demonstrations
2. Classroom practices

**Teaching and Learning resources:**

1. Chalk board
2. LCD presentations

3. Audio presentations
4. Item Bank

**REFERENCE MATERIAL:**

**a) Books**

Sr. No.	Author	Title	Publisher
1.	Dr B.H.Khan	N energy Resources	Tata McGraw Hill
2.	G. D. Rai	Non conventional energy sources	Khanna publication
3.	S. P. Sukhatme	Solar energy	Tata McGraw Hill
4.	H. P. Garg	Solar energy	Tata McGraw Hill
5.	Arora	Power plant engineering	Dhanpat Rai & Co.
6.	Arora and Domkundwar	Power plant engineering	Dhanpat Rai & Co.
7	P.H. Henderson	India- The energy sector	Oxford University Press
8	D. A. Ray	Industrial energy conservation	Pergaman Press
9	W. C. Turner	Energy management handbook	Wiley Press

**b) Websites**

- i) [www.mahaurja.com](http://www.mahaurja.com)
- ii) [www.indiasolar.com](http://www.indiasolar.com)
- iii) [www.beeindia.in](http://www.beeindia.in)

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**COURSE ID :**

Course Name : HIGHER MATHEMATICS  
Course Code : EIF 312  
Course Abbreviation : FHMT

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : Applied Mathematics(EIF 301)**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	04
Tutorial	01	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	Assignments given by teacher	Term End Theory Exam (03 hours)	--	--	
<b>Marks</b>	<b>20</b>	<b>---</b>	<b>80</b>	<b>--</b>	<b>--</b>	<b>100</b>

**RATIONALE:**

Mathematics is an important pre-requisite for the development and understanding of engineering and technological concepts. For an engineer and technologist, knowledge of Mathematics is an effective tool to pursue and to master the applications in the engineering and technological fields. The connection between Higher Mathematics and its applications in real life can be understood and appreciated. Finite Differences helps in finding population, temperature of a city etc . Laplace Transform is used to solve ordinary differential equations. Fourier Series finds its applications in wide number of topics in electronics field.

**COMPETENCY**

The course should be taught and implemented with the aim to apply mathematical technics to engineering field.

- 1.Cognitive:** understanding ,remembering and applying principles of mathematics to engineering problems
- 2. Psychomotor:** To prepare difference table ,to compute interpolation ,extrapolation and missing values in engineering data
- 3. Attitude:** discipline, consistency, hard work , to concentrate ,accuracy, punctuality, aesthetics

### Course Outcomes(CO's)

The student will be able to:

**EIF312.1** Apply methods of finite differences to Engineering and technical field..

**EIF312.2** Apply rules and methods of partial differentiation to solve Engineering and technical Problems

**EIF312.3** To equip student with tools of Laplace transform and Linear Differential equations with constant coefficients to enable him to use in Engineering and technology.

**EIF 312.4** To equip a student with the knowledge and tool of expressing any function in Fourier expansion series

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO1 Plan and Design	PSO2 Supervision and Providing Solution
The course should be taught and implemented with the aim to apply mathematical techniques to engineering field	2	1	1	-	-	-	-	-	-	1	2	2
<b>EIF 312-1</b>	2	-	1	-	-	-	-	-	-	-	2	1
<b>EIF312-2</b>	2	-	1	-	-	-	-	-	-	-	1	1
<b>EIF312-3</b>	2	1	1	-	-	-	-	-	-	1	3	2
<b>EIF 312-4</b>	2	1	1	-	-	-	-	-	-	1	3	2

## CONTENT:

### A. THEORY :

#### Section I

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>EIF 312-1 Apply methods of finite differences to Engineering and technical field..</b>			
<b>1</b>	<b>1 FINITE DIFFERENCE</b> Finite differences, forward difference $\Delta$ , Backward differences $\nabla$ ,Operator $E$ and Difference tables. 1.1 Inverse of $E$ , $\Delta$ , $\nabla$ , 1.2 Factorial notations of polynomials 1.3 To find missing terms by using difference table 1.4 Newton's forward & backward differences interpolation formulae (Examples ) 1.5 Lagrange's interpolation formula for unequal intervals.(Examples )	<b>12</b>	<b>20</b>
<b>EIF 312-2 Apply rules and methods of partial differentiation to solve Engineering and technical Problems</b>			
<b>2</b>	<b>2 PARTIAL DIFFERENTIATION</b> 2.1 Partial Derivatives of first order (Definition, Examples ) 2.2 Partial Derivatives of higher Order (Definition, Examples ) 2.3 Homogeneous functions , Euler's theorem on homogeneous functions (Examples ) 2.4 Jacobians ( Definition, Examples )	<b>12</b>	<b>20</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
1.Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only. 2.In each topic corresponding applications will be explained			

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>EIF 312-3 To equip student with tools of Laplace transform and Linear Differential equations with constant coefficients to enable him to use in Engineering and</b>			
<b>3</b>	<b>3 LAPLACE TRANSFORM</b> 3.1 Definition ,Linearity property 3.2 Laplace Transforms of Standard functions 3.3 First shifting property 3.4 Examples on Multiplication by $t^n$ 3.5 Examples on Division by $t$ 3.6 Inverse Laplace Transform, Definition 3.7 Standard formulae (Examples ) 3.8 Inverse L.T.by using First shifting property 3.9 Inverse L.T. by using Partial fraction method, 3.10 Inverse L.T. by using Convolution theorem 3.11 Applications of Laplace transforms 3.12 to solve differential equation using Laplace Transform	<b>12</b>	<b>20</b>
<b>EIF 312-4 To equip a student with the knowledge and tool of expressing any function in Fourier expansion series</b>			
<b>4</b>	<b>4 .FOURIER SERIES</b> 4.1 Definition by using Dirichlet's conditions 4.2 Expansion of functions in Fourier series in the intervals $(0,2\pi)$ , $(-\pi,+\pi)$ , $(0,2l)$ , $(-l,l)$ 4.3 Even & Odd function in $(-\pi,+\pi)$ & $(-l,l)$	<b>12</b>	<b>20</b>
	<b>Total</b>	<b>24</b>	<b>40</b>

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (level wise)			Total Marks
		Knowledge	Comprehensio	Application	
1	Finite Differences	4	6	10	20
2	Partial Differentiation	4	6	10	20
3	Laplace Transform	4	6	10	20
4	L.D.E. With constant coefficients	4	6	10	20

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

### B. PRACTICALS.

Note: Practicals are to be used to get enough practice [One batch for 20 Students]



Sr No.	Topic	Tutorial Content (10 problems in each tutorial)
1	Finite Differences	To evaluate examples on operators as $E$ , $\Delta$ , $\nabla$ and Factorial notation
2	Finite Differences	To evaluate Newton's forward & backward differences interpolation formulae
3	Finite Differences	To evaluate Lagrange's interpolation formulae
4	Partial Differentials.	To evaluate Partial Derivatives of higher Order Homogeneous functions ,
5	Partial Differentials.	To evaluate examples on Euler's theo. On homogeneous functions, Jacobian's
6	Laplace transform	Examples on first shifting property , Multiplication by $tn$ Property, Division by $t$ property
7	Laplace transform	Examples on inverse L.T. by Partial fraction method
8	Laplace transform	Examples on Applications of L.T.
9	Fourier Series	Examples on F.S. in interval $(0, 2\pi)$ , $(-\pi, \pi)$
10	Fourier Series	Examples to $(0, 2l)$ , $(-l, l)$

### INSTRUCTIONAL STRATEGIES:

#### Instructional Methods:

1. Lectures cum Demonstrations
2. Tutorials

#### Teaching and Learning resources:

1. Chalk board
2. Item Bank
- 3 Formulae Charts
- 4 Power point presentation

### REFERENCE MATERIAL:

#### a) Books:

Sr. No.	Author	Title	Publisher
1.	G.V. Kumbhojkar	Engineering Mathematics III	Phadake Prakashan, Kolhapur
2.	P.N. Wartikar	Applied mathematics	Pune vidyarthi Griha Prakashan , pune
3	H.K. Dass	Higher engineering mathematics	S .Chand publication
4	B.S.Grewal	Higher engineering Mathematics	Khanna publication, New Delhi

#### b) Websites

- i) [www.khanacademy.org](http://www.khanacademy.org)
- ii) [www.easycalculation.com](http://www.easycalculation.com)
- iii) [www.math-magic.com](http://www.math-magic.com)

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# **LEVEL -IV APPLIED TECHNOLOGY COURSES**



**COURSE ID :**

**COURSE NAME : POWER ELECTRONICS-1**  
**Course Code : EIF 401**  
**Course Abbreviation : FPE1**

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme:**

	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	-----	One practical (2 hours)	
Marks	20	--	80	-----	50 E	150

\* Assessment as per pro-forma I

E – external Examination

**RATIONALE:**

Day by day the change in Electronics Industry is dynamic. The role of diploma engineers changed over the years. Engineers should have knowledge of power electronics. Electronics control circuits have major role in industries for which study of power devices are essential. Concept of electronics devices and circuits along with the application are necessary. Power electronics 1 is the foundation subject to study industrial drives and advanced industrial electronics. This subject deals with the understanding of electronic semiconductor switches from thyristor family.

**COMPETENCY:**

Acquire knowledge about various electronic semiconductor switches from thyristor family & using them designing some power control circuits.

**Cognitive :** Understanding operating principle ,characteristics of semiconductor electronic switches from thyristor family'

**Psychomotor :** Develop skills to build, and troubleshoot power electronics circuits..

**Affective :** Attitude of i) Logic ii) accuracy iii) precision v) punctuality vi) Fault finding.

### COURSE OUTCOMES:

EIF401-1 Understanding of electronic semiconductor switches from thyristor family

EIF401-2 Describe different turn on methods of thyristor.

EIF401-3 Express commutation methods of thyristor.

EIF401-4 Analyze Series and parallel connection of SCR

EIF401-5 Acquire knowledge of uncontrolled rectifier circuit .

EIF401-6 Identify Controlled rectifier circuit.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Acquire knowledge about various electronic semiconductor switches from thyristor family & using them designing some power control circuits.	-	3	3	--	--	--	--	1	--	--	3	3
EIF401-1	2	-	2	--	--	--	--	1	--	--	1	1
EIF401-2	1	2	2	--	--	--	--	1	--	--	2	2
EIF401-3	1	2	2	--	--	--	--	2	--	--	3	2
EIF401-4	1	2	-	--	--	--	--	-	--	--	3	3
EIF401-5	1	2	-	--	--	--	--	-	--	--	3	3
EIF401-6	--	3	2	--	--	--	--	2	--	--	3	3

**CONTENT:**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF401-1 understanding of electronic semiconductor switches from thyristor family</i></b>			
<b>1</b>	<b>Power electronic semiconductor switches</b> 1.1 Need for high power semiconductor switches. 1.2 power transistor(Symbol, Construction, Characteristics) 1.2 SCR 1.2.1 Symbol, Construction (doping levels of layers) 1.2.2 Transistorized equivalent circuit. Operating principle 1.2.3 Characteristics, Specifications- Anode current - holding & latching current, on state voltage, VBO, turn on, turn off time etc. 1.3 TRIAC 1.3.1 Layer diagram , operating principle 1.3.2 four modes of operation 1.3.3 Triac characteristics. 1.4 Ideal switch characteristics 1.5 Triggering devices– layer diagram , Characteristics , operating principle , specifications of 1.5.1 UJT 1.5.2 PUT 1.5.3 DIAC.	14	18
<b><i>EIF401-2 Describe different turn on methods of thyristor.</i></b>			
<b>2</b>	<b>Triggering methods of thyristors</b> 2.1 Mechanisms with which SCR turns on: 2.1.1 Voltage triggering 2.1.2 Gate triggering 2.1.3 dv /dt triggering – (specification – dv/dt rating) 2.1.4 Light triggering 2.1.5 Temp triggering. 2.2 Advantages of gate triggering. 2.3 Pulse triggering (using active components) 2.3.1 Requirements of pulse to trigger SCR successfully, Pulse width, Pulse amplitude & gate current. 2.4 UJT relaxation oscillator:	08	12

	2.4.1 Circuit , operating principle 2.4.2 expression for time period , Maximum & minimum value of timing resistor 2.4.3 Waveforms. 2.5 PUT relaxation oscillator: 2.5.1 Circuit , operating principle 2.5.2 expression for time period , waveforms 2.6 Advantage of PUT over UJT. 2.7 Triac triggering using Diac.		
<b><i>EIF401-3 Express commutation methods of thyristor.</i></b>			
<b>3</b>	<b>Commutation circuits.</b> 3.1 Turn off mechanism of SCR 3.2 Specifications related to turn off: Turn off time 3.3 Types of commutation methods. 3.3.1. Resonant turn off - Class A ,Class B 3.3.2. Parallel Capacitance turn off using auxiliary SCR - Class C, Class D 3.3.3 Coupled pulse turn off - Class E 3.3.4 Natural turn off - Class F	10	10
	<b>Total:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



**Section II**

Sr. No.	Topics / Subtopics	Lectures (Hours)	Theory Evaluation (Marks)
<b><i>EIF401-4 Analyze Series and parallel connection of SCR</i></b>			
<b>4</b>	<b>Series and parallel connection of SCR</b> 4.1 Series connection 4.1.1 Need of series connection 4.1.2 Reason for unequal distribution of voltage 4.1.3 Voltage equalization circuits- dynamic & static  4.2 Parallel connection 4.2.1 Need of parallel connection 4.2.2 Reasons of unequal distribution of current 4.2.3 Current equalization networks	10	12
<b><i>EIF401-5 Acquire knowledge of uncontrolled rectifier circuit.</i></b>			
<b>5</b>	AC to DC converter - Uncontrolled rectifier 5.1 Uncontrolled Rectifier 5.1.1 Uncontrolled rectifier: Meanings, Review of single phase uncontrolled rectifier. 5.1.2 Three Phase uncontrolled rectifier 5.1.3 Advantages of 3 $\phi$ uncontrolled rectifier 5.1.4 Three Phase transformer delta – star connection (review) 5.1.5 Three Phase half wave uncontrolled rectifier: Circuit , vector diagram 5.1.6 operating principle , input & output voltage waveforms 5.1.7 expression for average output voltage & rms output voltage ( no derivation) 5.2 Three phase bridge rectifier Circuit, vector diagram, 5.2.1 operating principle, input & output voltage waveforms 5.2.2 expression for average output voltage & rms output voltage, 5.3 Performance parameters of both rectifier 5.3.1. Efficiency 5.3.2. Form factor 5.3.3. Ripple factor 5.3.4. PIV 5.3.5 TUF	08	12
<b><i>EIF401-6 Identify Controlled rectifier circuit.</i></b>			
	<b>AC to DC converter -Controlled rectifier</b> 6.1 Controlled Rectifiers: Meaning, AC phase control principle.		

<b>6</b>	6.1.1 Single phase half wave controlled rectifier with resistive load circuit, Operating principle , waveforms 6.1.2 Single phase half wave controlled rectifier with RL load: Concept of inductive load with example, Circuit , operating principle , waveforms 6.1.3 Concept of load & source (review) 6.1.4 Concept of two quadrant operation, power feedback (Regeneration), power factor.  6.1.5 Single phase half wave controlled rectifier with RL load & free wheeling diode: circuit, Operating principle 6.2.1 Single phase full wave bridge controlled rectifier with resistive load circuit, Operating principle , waveforms 6.2.2 Single phase full wave bridge controlled rectifier with RL load: Concept of inductive load with example, Circuit , operating principle , waveforms 6.2.3 Concept of load & source (review) 6.2.4 Concept of two quadrant operation, power feedback (Regeneration), power factor. 6.2.5 Single phase full wave bridge controlled rectifier with RL load & free wheeling diode: circuit, Operating principle	14	16
	<b>Total:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Section / Topic no.	Name of topic	Distribution of marks			Course outcome	Total marks
		Knowledge	Comprehension	Application		
I/1	Semiconductor switches	5	5	8	EIF401-1	18
I/2	Triggering methods of thyristor	4	3	5	EIF401-2	12
I/3	Commutation circuits	3	3	4	EIF401--3	10
II/4	Series and parallel connection of SCR	4	3	5	EIF401--4	12
II/6	AC to DC converter-uncontrolled rectifier	4	3	5	EIF401--5	12
II/7	AC to DC converter-controlled rectifier	5	5	6	EIF401--6	16

	Total	25	22	33		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B)TERM WORK** Term work shall consist of the following :  
**(Minimum Eight From List)**

**Laboratory experiments and related skills to be developed:**

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	To study the characteristics of SCR	1. To understand and study the characteristic and diff. points in it. 2. To plot graph of V-I characteristics	EIF401-1
2.	To study the characteristics of Diac	1. To understand and study the characteristic and diff. points in it. 2. To plot graph of V-I characteristics	EIF401-1
3.	To study the characteristics of Triac	1. To understand and study the characteristic and diff. points in it. 2. To plot graph of V-I characteristics	EIF401-1
4.	Study the phase control using Diac and Triac.	1. Know how the Diac is used to trigger Triac. 2. To plot graph of V-I characteristics	EIF401-2
5.	Flasher using SCR.	1. To study the flasher circuit.	EIF401-2, EIF401-3
6.	To study the characteristics of PUT.	1.To understand and study the characteristic and diff. points in it.	EIF401-1
7.	To study the SCR commutation circuits(class A,B,C)	1.To study resonant turn off, using auxiliary SCR	EIF401-3
8.	To study the SCR commutation circuits(class D,E,F)	1.To study auxiliary SCR, coupled pulse turn off, natural turn off	EIF401-3
9.	To study Half controlled rectifier	1.Draw Circuit Diagram And Waveform.	EIF401-6
10.	To study full controlled rectifier	1. Draw Circuit Diagram And Waveform	EIF401-6

#### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

**a) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments :**

Every practical assignment shall be assessed for 25 marks as per given criteria .

**ii) Progressive Skill Test :**

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

**Criteria for Internal Assessment of Practical work and Progressive skill Test :**

Sr. No.	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Presentation	05
3	Conceptual understanding	05
5	Proper procedure /safety measures	05
6	Oral based on term work	05
	Total	25

**b) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	Total	50

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3.Slides      4. Question Bank 5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Deodatta Shingare	Industrial and Power Electronics	Tata McGraw Hill
2.	P.C. Sen	Power Electronics	Tata McGraw Hill
3	M.D.Singh&K.B.Khanchandani	Power Electronics	Tata McGraw Hill

**b) Websites:**

- 1) [www.circuitstoday.com](http://www.circuitstoday.com)
- 2) [www.daenotes.com](http://www.daenotes.com)
- 3) [www.electronicinstrumentsmanufacturer.com](http://www.electronicinstrumentsmanufacturer.com)
- 4) [www.talkingelectronics.com](http://www.talkingelectronics.com)
- 5) [www.bbs.sciencenet.net](http://www.bbs.sciencenet.net)

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**COURSE ID :**

**Course Name : EMBEDDED SYSTEMS**  
**Course Code : EIF402**  
**Course Abbreviation : FEMB**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	-----	Practical (3 hours)	
Marks	20	--	80	-----	50E	150

E-External Examination

\* Assessment as per Pro-forma I

**RATIONALE:**

Embedded system is a new trend in the field of automation. The subject has been introduced in order to enhance the knowledge of microcontroller programming. This subject will help a student to design small embedded systems and write the code for the same.

**COMPETENCY:**

Design & implement 8051 microcontroller based systems for real time applications.

**Cognitive:** Understand embedded C programming and peripheral interfacing.

**Psychomotor:** Write embedded C programs for wide range of applications.

**Affective:** Attitude of i) Logical thinking ability ii) System hardware design skills.

### COURSE OUTCOMES:

**EIF 402-1** Understand the fundamentals of Embedded C programming for 8051 microcontroller.

**EIF 402-2** Develop the critical timing related and real time event counter applications.

**EIF 402-3** Develop the applications based on 8051 interrupts using embedded C.

**EIF 402-4** Interface and program for various I/O devices(sensors, displays, RTC etc) with 8051 microcontroller.

**EIF 402-5** Interface and program for various peripheral I/O devices with 8051 microcontroller.

**EIF 402-6** Design and implement 8051 microcontroller based systems for real time applications.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Design & implement 8051 microcontroller based systems for real time applications.	-	2	3	2	-	-	-	1	-	-	3	3
<b>EIF 402-1</b>	1	1	2	-	-	-	-	1	-	-	2	2
<b>EIF 402-2</b>	-	3	2	-	-	-	-	1	-	-	2	2
<b>EIF 402-3</b>	-	3	2	1	-	-	-	1	-	-	2	2
<b>EIF 402-4</b>	1	2	2	1	-	-	-	1	-	1	3	3
<b>EIF 402-5</b>	1	2	2	1	-	-	-	1	-	1	3	3
<b>EIF 402-6</b>	-	3	-	-	-	-	-	-	-	1	3	3



**CONTENT:**

**A) THEORY :**

**SECTION I**

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<b><i>EIF402-1 Understand the fundamentals of Embedded C programming for 8051 microcontroller.</i></b>			
01	<b>8051 Programming in C:</b> 1.1 Overview of 8051 architecture 1.2 Data types and time delay in 8051 using C 1.3 I/O programming in 8051 using C 1.4 Logic operations in 8051 using C 1.5 Data conversion programs in 8051 using C 1.6 Accessing code ROM space in 8051 using C 1.7 Data serialization in 8051 using	08	10
<b><i>EIF402-2 Develop the critical timing related and real time event counter applications.</i></b>			
02	<b>8051 Timer, Serial port programming in C:</b> 2.1 Programming 8051 timers(mode 0,1,2) in C 2.1.1 Generation of time delay using timer 2.1.2 Square wave generation at port pin of various duty cycle 2.1.3 timer as a counter 2.2 Basics of serial communication 2.3 Connection of 8051 to RS232 2.4 Serial port programming in C.	12	14
<b><i>EIF402-3 Develop the applications based on 8051 interrupts using embedded C.</i></b>			
03	<b>8051 interrupts programming in C:</b> 3.1 8051 interrupts 3.2 Programming of timer interrupts 3.3 Programming of Serial communication interrupts 3.4 Programming of External hardware interrupts in C.	12	16
	<b>Total</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## SECTION II

Chapter	Topic Subtopics	Teaching Hours	Theory Evaluation Marks
<b><i>EIF402-4 Interface and program for various I/O devices(sensors, displays, RTC etc) with 8051 microcontroller.</i></b>			
04	<b>LCD, keyboard, ADC, DAC and Interfacing to 8051:</b> 4.1 LCD interfacing, keyboard interfacing and their Programming in C 4.2 Parallel(ADC0804,ADC0808/09) and serial(MAX1112) ADC interfacing and their programming in C, DAC(DAC0808) interfacing and programming in C 4.3 Temperature sensor(LM35) interfacing and signal conditioning and program to read and display temperature reading.	12	16
<b><i>EIF402-5 Interface and program for various peripheral I/O devices with 8051 microcontroller.</i></b>			
05	<b>Motor control and Real time clock(DS12887) interfacing and programming:</b> 5.1 Stepper motor interfacing 5.2 DC motor interfacing and speed control of DC motor using PWM, its programming in C 5.3 DS12887 RTC interfacing and its programming in C, alarm, SQW and IRQ features of DS12887 chip.	12	16
<b><i>EIF402-6 Design and implement 8051 microcontroller based systems for real time applications.</i></b>			
06	<b>8051 based System Design:</b> 6.1 Designing 8051 microcontroller based system such as data acquisition system 6.2 Design of sensor interfacing 6.3 Selection of ADC 6.4 Selection of output drivers 6.5 Writing efficient programs using C	08	08
	Total	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic no.	Name of the Topic	Distribution Of Marks (Level Wise)			Course Outcome	Total Marks
		Remember	Understand	Applications		
1	8051 Programming in C	8	2	0	EIF 402-1	10
2	8051 Timer, Serial port programming in C	8	6	0	EIF 402-2	14
3	8051 interrupts programming in C	8	4	4	EIF 402-3	16
4	LCD, keyboard, ADC, DAC and Interfacing to 8051	8	4	4	EIF 402-4	16
5	Motor control and Real time clock(DS12887) interfacing and programming	8	4	4	EIF 402-5	16
6	8051 based System Design	0	4	4	EIF 402-6	08
				TOTAL		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK** Term work shall consist of the following :  
(Minimum Eight From List)

**Practical Exercises and related skills to be developed :**

Sr No	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1	Development and execution of the program in C for sending data on port lines.	<ul style="list-style-type: none"> <li>Writing programs using Embedded C of 8051.</li> <li>Programming using 8051 kit/ any standard compiler.</li> </ul>	EIF 402-2,3
2	Development and execution of the program in C for arithmetic operation and time delay.	<ol style="list-style-type: none"> <li>Writing programs using Embedded C of 8051.</li> <li>Programming using 8051 kit/ any standard compiler.</li> <li>Calculation</li> </ol>	EIF 402-2,3
3	Development and execution of the program in C for input and output operation.	<ol style="list-style-type: none"> <li>Writing programs using Embedded C of 8051.</li> <li>Programming using 8051 kit/ any standard compiler.</li> <li>Calculation</li> </ol>	EIF 402-3
4	Development and execution of the program in C for interface LEDs to particular	<ol style="list-style-type: none"> <li>Writing programs using Embedded C of 8051.</li> <li>Programming using 8051 kit/ any</li> </ol>	EIF 502-2,3

Sr No	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
	port.	standard compiler. 3. Calculation	
5	Development and execution of the program in C to generate a square wave on port.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	IEF 502-2,3
6	Development and execution of the program for logical operators and data conversion.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler.	EIF 402-1,2
7	Development and execution of the program PWM waveform generation.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,3,4
8	Development and execution of the program to display a message on LCD (16x2).	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,3,4,
9	To write 8051 C program to send data on serial port continuously.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,3
10	Interface Stepper Motor to Microcontroller 8051 and development and execution of the program to run stepper motor using C.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,5
11	Interface ADC to Microcontroller 8051 and development and execution of the program to display digital equivalent of analog input using C	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,4
12	Interface DAC to Microcontroller 8051 and development and execution of the program to generate specified voltage using C.	1. Writing programs using Embedded C of 8051. 2. Programming using 8051 kit/ any standard compiler. 3. Calculation	EIF 402-2,4

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

### c) Assessment Criteria for Term work :

#### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria.

#### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below.

#### Criteria for Progressive Assessment of Practical and Skill Test

Domain	Particulars	Marks out of 50
Cognitive	Technical preparedness for practical	05
Psychomotor	Algorithm /Flowchart	05
	Program/Logic	05
Affective	Discipline and punctuality	05
	Decency and presentation	05
<b>TOTAL</b>		<b>25</b>

### d) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria

Domain	Particulars	Marks out of 50
Cognitive	Technical Ability	05
	Logical Approach	05
Psychomotor	Approach to problem	10
	Programming skill	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>50</b>

Assessment at semester end practical exam as per Pro-forma I

## INSTRUCTIONAL STRATEGIES:

### Instructional Methods:

4. Lectures cum discussions
5. Regular home assignments
6. Laboratory work

### Teaching and Learning resources:

6. Chalk board
7. Video clips
8. PPT Slides
9. Question Bank
10. Charts
6. Reference Books, Internet

**REFERENCE MATERIAL :**

**c) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1	Keneth J. Ayala	The 8051 Microcontroller	PenRam International
2	M.A.Mazidi	The 8051 Microcontroller and Embedded systems	Pearson Edition, Prentice Hall
3	Ajay Deshmukh	Microcontrollers(Theory & Applications)	Tata Mcgraw Hill

**d) Websites:**

1. [www.google.com](http://www.google.com)
2. [www.8051.com](http://www.8051.com)
3. [www.alldatasheet.com](http://www.alldatasheet.com)

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**COURSE ID :****Course Name : DATA COMMUNICATION AND NETWORKING****Course Code : ETF403****Course Abbreviation : FDCN****TEACHING AND EVALUATION SCHEME :****Pre-requisite Course(s) : < nil >****Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical iv. One PST of 25 marks	Term End Theory Exam (03 hours)	-----	Oral	
Marks	20	--	80	-----	25E	125

E-External Assessment

\* Assessment as per Pro-forma I

**RATIONALE:**

After understanding basic communication system it is worth to discuss Data Communication & Networking. Telecommunication & Data communication is the fastest growing technology & undoubtedly has strong growth in future hence students should know data transfer from one system to another through different communication networks like WAN, MAN & different switching techniques.

**COMPETENCY:**

Implement different protocols used for data communication using various switching techniques and network topologies.

**Cognitive :** Understanding network topologies, and protocols used in various switching approaches and wireless technologies.

**Psychomotor :** Implement star and tree topologies using computers and simple protocols in C for wide range of applications.

**Affective :** Attitude of i) Logic ii) accuracy iii) speed of data communication v) punctuality

### COURSE OUTCOMES:

**ETF403-1** Define various parameters of data communications and differentiate them.

**ETF403-2** Identify/Discover the architectural details of different topologies and models of network and illustrate its functioning based on connecting devices.

**ETF403-3** Identify different switching techniques and explore their applications.

**ETF403-4** List various errors in data communication and employ error detection and correction techniques.

**ETF403-5** Summarize application layer protocols and describe its formats.

**ETF403-6** Classify wireless LAN protocols and describe network security solutions.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ **Note : Correlation levels** : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Implement different protocols used for data communication using various switching techniques and network topologies.	1	2	2	2	-	-	-	1	-	1	1	1
<b>ETF403-1</b>	1	-	-	-	-	-	-	-	-	-	-	-
<b>ETF403-2</b>	1	1	2	1	-	-	-	1	-	1	-	1
<b>ETF403-3</b>	-	2	-	-	-	-	-	-	-	-	-	-
<b>ETF403-4</b>	1	2	2	1	-	-	-	1	-	1	-	-
<b>ETF403-5</b>	-	3	1	1	-	-	-	1	-	1	-	1
<b>ETF403-6</b>	-	2	1	1	-	-	-	1	-	1	-	1



**CONTENT:**

**A) THEORY :                      Section-I**

Sr. no.	Topics Subtopics	Lectures (Hours)	Theory evaluation Marks
<b><i>ETF403-1 Define various parameters of data communications and differentiate them.</i></b>			
1	<b>Concept of Data Communication</b> 1.1 Data Communication – components, data representation 1.2 Data flow - Simplex, half-duplex & full-duplex 1.3 Protocols; Standards; Standards Organizations; 1.4 Periodic analog signal : wavelength, time and frequency domain, composite signals, bandwidth 1.5 Digital signals – bit rate, bit length, digital signal as composite analog signal 1.6 Digital signal transmission – base band transmission, broadband transmission 1.7 Transmission impairment – attenuation, distortion, noise 1.8 Data rate limits – noiseless channel ( Nyquist bit rate), Noisy channel ( Shannon capacity) 1.9 Performance of network – Bandwidth, Throughput, Latency (Delay), Transmission time, Bandwidth delay product, Jitter	08	12
<b><i>ETF403-2 Identify/Discover the architectural details of different topologies and models of network and illustrate its functioning based on connecting devices.</i></b>			
2	<b>Introduction To Networking</b> 2.1 Network definition & criteria ( performance, reliability, security ) 2.2 Type of connection - Point to point, multipoint 2.3 Network topology: Mesh, ring, bus, star, hybrid 2.4 Types Of Networks :LAN,WAN and MAN, Internet and internet service providers 2.5 Network models – OSI, TCP/IP(Organization and Functions Of each layer) 2.6 Levels ( Types ) of addressing – Physical, Logical (IP), Port, and Specific addresses 2.7 Transmission media and types – performance, advantages and applications, 2.8 Modems –block schematic & standards 2.9 Network Devices: Network Connectors, Hubs, Switches, Routers, Bridges	12	14
<b><i>ETF403-3 Identify different switching techniques and explore their applications.</i></b>			
3	<b>Switching Techniques</b> 3.1 Connection oriented services (Virtual circuits) and Connectionless services (Datagrams) 3.2 Switching Basics - Circuit Switching; Packet Switching - Datagram approach, Virtual circuit approach; Message Switching 3.3 Frame Relay - Introduction; The need for Frame Relay;	12	14

	3.4 How Frame Relay works; Frame Relay frame format 3.5 Asynchronous Transfer Mode (ATM) – Introduction, Overview of ATM		
	<b>TOTAL</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

### Section II

Sr. no.	Topics Subtopics	Teaching (Hours)	Theory evaluation Marks
<b>: ETF403-4 List various errors in data communication and employ error detection and correction techniques.</b>			
4	<b>Error correction &amp; detection :</b> 4.1 Types of errors. 4.2 Redundancy 4.3 Detection VS Correction 4.4 Error Detection method – CRC encoder and decoder 4.5 Data Link Control & Protocol – Framing, Flow & Error Control, 4.6 Noiseless and noisy channels 4.7 Simple, Stop-N- Wait Protocol 4.8 Stop-N-Wait ARQ Protocol 4.9 Go Back N ARQ, Selective Repeat ARQ, Piggybacking	10	14
<b>ETF403-5 Summarize application layer protocols and describe its formats.</b>			
5	<b>Applications Layer</b> 5.1 Domain Name System (DNS) 5.2 Email -Simple Mail Transfer Protocol (SMTP); 5.3 Multipurpose Internet Mail Extension (MIME); 5.4 POP 5.5 IMAP 5.6 Hyper Text Transfer Protocol(HTTP); 5.7 TELNET 5.8 File Transfer Protocol (FTP) 5.9 World wide web(www) 5.10 Uniform Resource Locator (URL) 5.11 IP version 4 5.12 Simple Network Management Protocol(SNMP)	12	14
<b>ETF403-6 Classify wireless LAN protocols and describe network security solutions.</b>			
6	<b>Wireless LAN and Network security</b> 1.1 IEEE Standards, Standard Ethernet, Changes in the Standard, Fast Ethernet, Gigabit Ethernet 6.2 Wireless LAN systems 6.2.1 IEEE 802.11n - Architecture- BSS, ESS; 6.2.2 Physical layer : FHSS, DSSS, OFDM; 6.2.3 MAC layer – DCF, PCF 6.3 Cryptography: Need, Encryption model	10	12

	6.4 Firewalls: Packet and proxy firewalls		
	<b>TOTAL</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification Table For Question Paper of Theory Examination:**

Section / Topic no.	Name of the Topic	Distribution Of Marks (Cognitive Level Wise)			Course outcome	Total Marks
		Remember	Understand	Applications		
I/1	Concept of Data Communication	4	6	2	ETF403-1	12
I/2	Introduction to networking	6	4	4	ETF403-2	14
I/3	Switching Techniques	6	4	4	ETF403-3	14
II/4	Error correction & detection	6	4	4	ETF403-4	14
II/5	Applications Layer	4	6	4	ETF403-5	14
II/6	Wireless LAN and network security	2	6	4	ETF403-6	12
	<b>TOTAL</b>	28	30	22		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK** Term work shall consist of the following :

**Laboratory experiments and related skills to be developed :**

Sr. No.	Title of Experiment	Skills / competencies to be developed	Course outcome
1.	Types of networks	Understand and compare various types of networks and topologies with diagram.	ETF403-2
2.	Physical Layer network components	Describe Physical Layer network components such as Network Connectors, Hubs, Switches, Routers, Bridges (Types of Bridges) and identify network components in given network.	ETF403-2
3.	Types of transmission media	Describe different types of transmission media and list their properties.	ETF403-2
4.	Troubleshooting of network	Know the procedure of network troubleshooting and identify the faults in the network.	ETF403-1 TO 3

5.	PING command	Interpret various responses of PING command.	ETF403-1 TO 3
6.	Peer to Peer network Implementation	Understand installation of NIC Driver and Assign IP address, name to node and Share resources like Drives, Folder.	ETF403-1 TO 3
7.	Adhoc mode wireless network implementation (For Windows 7 operating system)	Implement Adhoc Wireless network	ETF403-1 TO 4
8.	Infrastructure mode wireless network implementation	Implement Secured Infrastructure mode Wireless network	ETF403-1 TO 6
9.	Flow control using Wireshark	Use wireshark open source network troubleshooting software for flow control.	ETF403-1 TO 4
10.	Error control using Wireshark	Use wireshark open source network troubleshooting software for error control.	ETF403-1 TO 4

#### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

##### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria.

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / working skills	05
Affective	Discipline and punctuality	03
	Decency and presentation	02
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given in *Laboratory Manual for Data communication and networking*

Final marks of term work shall be awarded as per *Assessment Pro-forma I*.

Sr.No.	Particulars	Marks
1	Attendance	05
2	Correct figures	05
3	Proper Observation & Result Table	05
4	Sample Calculation with relevant Formulae	05
5	Procedure/ Workmanship/ Safety	05
	Total	25

**b) Criteria for assessment at semester end Oral exam:**

Every student has shall be assessed as per following criteria.

Sr.No.	Particulars	Marks
1	Attendance	02
2	Question and Answer	20
3	Decency and presentation	03
	Total	25

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods:**

1. Lectures cum Discussions    2. Regular Home Assignments.    3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3.Slides    4. Question Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books**

Sr. No	Author	Title	Publication
01	Achyut S. Godbole	Data Communication & Networking	Tata McGraw-Hill Edition
02	B.A. Forouzan	Data Communication & Networking	Tata McGraw-Hill Edition(4th Edition)
03	Michal Miller	Data & Network Communication	Thomson Delmar Learning

**c) Websites:**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)  
2) [www.tutorialspoint.com/data\\_communication\\_computer\\_network/](http://www.tutorialspoint.com/data_communication_computer_network/)  
3) [frevidelectures.com](http://frevidelectures.com) › Networking › IIT Kharagpur  
4) [www.concise-courses.com/security/wireshark-basics/](http://www.concise-courses.com/security/wireshark-basics/)

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**COURSE ID:**

Course Name :Principals of control system  
Course Code :EIF 404  
Course Abbreviation :FPCS

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	v. 25 marks for each practical vi. One PST of 25 marks	Term End Theory Exam (03 hours)	-----	Oral	
Marks	20	--	80	-----	25I	125

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE:**

Control is the process of causing a system variable to take some desired value, known as reference value. A control system consists of several elements or components connected and operated in such a way as to achieve a desired control in a specific domain of operation of the system. The development of technology has seen a wide range use of automatic control system to improve the quantity and quality of manufactured products in many domestic, industrial and defense applications.

To increase the effectiveness, efficiency and quality of products, now a days it is very much essential to complete the required work or task automatically in every field. Control systems are also used in space technology and defense applications such as nuclear power weapons, guided missiles etc. Robots are also created by using automatic control systems. As the control system is the basis of various automatic control systems, therefore the students of electronic engineering must have the knowledge of control system.

**COMPETENCY:**

Apply control systems in real time.

**Cognitive:** Understand applications of control systems.

**Psychomotor:** Draw block diagrams of control systems as per application

**Affective:** Attitude of i) Logic ii) accuracy

### COURSE OUTCOMES:

- EIF 404-1**..Identify various types of control systems  
**EIF 404-2** Predict transient and steady state responses of system.  
**EIF 404-3**..Draw frequency response of system using bode plot  
**EIF 404-4**Determine stability conditions of control system.  
**EIF 404-5**..Identify use of servo motors as per requirement.  
**EIF 404-6**: Select appropriate Control system processes as necessary.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ Note : Correlation levels :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency: Apply control systems in real time</b>	-	3	3	1	-	-	-	1	-	-	-	-
<b>EIF 404-1</b>	2	1	-	-	-	-	-	-	-	-	-	-
<b>EIF 404-2</b>	1	2	1	-	-	-	-	1	-	-	1	1
<b>EIF 404-3</b>	1	2	2	1	-	-	-	1	-	-	1	1
<b>EIF 404-4</b>	1	2	2	1	-	-	-	1	-	-	1	2
<b>EIF 404-5</b>	1	2	1	-	-	-	-	1	-	1	1	2
<b>EIF 404-6</b>	1	2	2	2	-	-	-	1	-	1	1	1



**CONTENTS:**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>EIF 404-1. Identify various types of control systems</b>			
<b>1</b>	<p><b>COURSE OUTCOME EIF 404-1. Identify various types of control systems</b></p> <p><b>Overview of Control system</b></p> <p>1.1 System- definition &amp; practical examples, Control system – definition and examples</p> <p>1.2 Classification of control system</p> <p>1.3 Open loop &amp; closed loop systems – definition, block diagram, practical example &amp; Comparison</p> <p>1.4 Laplace transform – Significance in control system</p> <p>1.5 Transfer function – definition, derivation of transfer function for close loop control system.</p> <p>1.6 Order of a system – definition, 0<sup>th</sup>, 1<sup>st</sup>, 2<sup>nd</sup> order system standard equation, practical examples.</p> <p>1.7 Linear time varying and time in varying systems – definition and example</p> <p>1.8 Developing differential equations of R-C and RLC electric circuits-simple numerical for finding transfer function of electrical network</p> <p>1.9 Block diagram representation of a system- need, reduction rules, problems (only SISO).</p>	10	14
<b>EIF 404-2 Predict transient and steady state responses of system.</b>			
<b>2.</b>	<p><b>Time Domain Analysis</b></p> <p>2.1 Time domain and frequency domain analysis- definition</p> <p>2.2 Transient and steady state response, steady state error-definition and equation only.</p> <p>2.3 Standard test inputs - step, ramp, parabolic &amp; impulse. Need of them, significance, and corresponding Laplace representation</p> <p>2.4 Poles, zeros &amp; characteristics equation – definition</p> <p>2.5 Types of feedback control system: type 0 system, type 1 system and type 2 system-only definition</p> <p>2.6 Analysis of first order control system for unit step input.</p> <p>2.7 Analysis of second order control system for unit step input.</p>	12	14

	2.8 time response specifications (no derivations) ; problems on time response specification		
<b>EIF 404-3. Draw frequency response of system using bode plot</b>			
<b>3</b>	<b>Frequency domain Analysis</b> 3.1 Introduction, advantages & disadvantages of frequency response analysis 3.2 Frequency response specifications 3.3 Correlation between time and frequency domain specifications, 3.4 Bode plot-introduction, 3.4.1. General procedure for constructing Bode plot 3.4.2 Plotting gain margin & phase margin 3.4.3 Simple numericals (max. up to 2-poles)	10	12
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>	<b>Theory Evaluation (Marks)</b>
<b>EIF 404-4 Determine stability conditions of control system.</b>			
<b>4</b>	<b>Stability</b> 4.1 S-plane – Introduction 4.2 Definition of stability 4.3 Necessary Conditions for stability 4.4 Types of stability:- stable, unstable, critically stable & conditionally stable system; relative stability; 4.5 Root locations in S-plane for stable unstable & critically stable systems 4.6 Routh's stability criterion-different cases & conditions & numericals 4.7 Root Locus technique-Introduction and steps to draw root locus.	10	14
<b>EIF 404-5. Identify use of servo motors as per requirement</b>			
<b>5</b>	<b>Servo Systems</b> 5.1 Servo system –definition, block diagram, 5.2 AC & DC servo systems- comparison, practical example, schematic diagram , concept and principle 5.3 Servo components: 5.3.1 Potentiometer as error detector 5.3.2 Synchro as error detector 5.3.3 Rotary encoder 5.3.4 Stepper motor- variable reluctance type, comparison of stepper motor with DC servo motor 5.4 DC servo motor- characteristic, difference from a	12	14

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
	normal DC motor, comparison between armature controlled and field controlled DC servo motors (with TF) 5.5 AC servo motor-difference from a normal 2 phase induction motor, characteristic of AC Servo motor (no TF)		
<b>EIF 404-6</b> Select appropriate Control system processes as necessary.			
<b>6</b>	<b>Control actions &amp; process controllers</b> i) Process control system – block diagram, elements ii) Role of controllers in process industry; iii) Control actions: discontinuous & continuous modes; iv) on off controllers: neutral zone v) proportional controllers (offset, proportional band) integral & derivative controllers; vi) composite controllers; PI, PD, PID controllers vii) Control actions of electronic controllers with circuits & equations (with op amp)	10	12
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Overview of Control system	4	4	6	EIF 404-1	14
2	Time domain Analysis of a system	4	4	6	EIF 404-2	14
3	Frequency domain Analysis	4	4	4	EIF 404-3	12
4	Stability	4	4	6	EIF 404-4	14
5	Servo Systems	4	4	6	EIF 404-5	14
6	Control actions & process controllers	4	4	4	EIF 404-6	12
TOTAL		24	24	32		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**  
(Minimum Eight From List)

**Practical Exercises and related skills to be developed:**

The following practical exercises shall be conducted as Term Work as detailed in the *Laboratory Manual for Applied Mechanics* developed by the Institute in practical sessions of batches of about 22 students :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	DC position control system	1. Analyze the DC Position Control system. 2. Measure input position and output position. 3. Plot the graph of input position versus output position.	EIF 404-5
2.	AC position control system	1. Analyze the AC Position Control system. 2. Measure input position and output position. 3. Plot the graph of input position versus output position.	EIF 404-5
3.	Characteristics of potentiometer as error detector	1. Analyze, understand and construct circuit for potentiometer as error detector. 2. Measure input position and output position. 3. Plot the graph of input position versus output position.	EIF 404-5
4.	Characteristics of synchro as error detector	1. Analyze and understand operation of synchro as error detector. 2. Measure input position and output position. 3. Plot the graph of input position versus output position.	EIF 404-5
5.	Step response of first order R-C circuit	1. Construct first order RC Circuit. 2. Observe the output for step input and measure various parameters. 3. Plot the step response on graph.	EIF 404-2

6.	Step response of R-L-C second order circuit	1. Construct second order RLC Circuit. 2. Observe the output for step input and measure various parameters such as delay time, rise time, etc. 3. Plot the step response on graph.	EIF 404-2
7.	Temperature controller with on-off controller	1. Analyze temperature controller with on-off controller. 2. Observe output for various set points.	EIF 404-6
8.	Temperature controller with PI controller	1. Analyze temperature controller with PI controller. 2. Observe output for various set points.	EIF 404-6
9.	Temperature controller with PID controller.	1. Analyze temperature controller with PID controller 2. Observe output for various set points.	EIF 404-6
10.	Study of bode plot	1. Understand procedure to draw bode plot. 2. Using Matlab, sketch bode plot. 3. Verify theoretical and practical graph and parameters such as GM, PM.	EIF 404-3
11.	Study of root locus	1. Understand procedure to draw root locus. 2. Using Matlab, sketch root locus.	EIF 404-4

#### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

v) **Assessment Criteria for Term work :**

i) **Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test :**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given

**w) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Correct figures / diagrams	10
2	Result table / calculations / graphs	10
3	Presentation	05
	Total	25

Assessment at semester end practical exam as per Pro-forma II

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	M. Gopal	Digital Control System	Tata McGraw-Hill
2.	J.J.Nagrath& M. Gopal	Control system Engg.	Tata McGraw-Hill
3.	M.Gopal	Control System	Tata McGraw-Hill
4.	K. Ogata	Modern control Engg.	Tata McGraw-Hill
5.	Kumar	Control systems	Tata McGraw-Hill

**b) Websites:**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)  
2) [www.electronics-tutorials.ws](http://www.electronics-tutorials.ws)  
3) <http://electrical4u.com/controlsystem>

\* \* \*

**COURSE ID :**

**Course Name : SIMULATION SOFTWARE**  
**Course Code : EIF405**  
**Course Abbreviation : FSIM**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**  
**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	-	04
Practical	04	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	Termwork assessment as per proforma-VI	Practical (3 hours)	
Marks	--	--	--	25	50I	75

\*Practical Assessment as per pro-forma V

I – Internal Examination

\*\*Termwork assessment as per proforma-VI

**RATIONALE :**

Today in industry there is extensive use of Computer Aided Design(CAD) packages in engineering applications. For example MATLAB, ORCAD are very powerful mathematical computation and electronic circuit design, simulation, and analysis tools. If an engineering problem can be solved using software tool, it is usually more efficient to use the software tool than to write a program in a computer language to solve the problem. MATLAB is a high level programming language / software tool developed by The Mathworks Inc. USA. MATLAB consists hundreds of commands to do mathematics. You can use graph equations to, solve equations, perform statistical tests. By using SIMULINK tool you can do simulations and modeling.

ORCAD consist three tools for specific purposes. OrCAD Capture provides the simple schematic commands you need to enter, modify and verify the PCB design. Thus you can employ this tool e.g. in designing new analog circuit, in revising a schematic diagram for an existing PCB, or designing a digital block diagram with an HDL module. OrCAD Pspice A/D is advanced simulation solutions for analog and mixed-signal environments. OrCAD Layout is advanced, Rules-Driven PCB design tool for simple to complex printed circuit boards. This subject introduces students to the software tools MATLAB & OrCAD.

### COMPETENCY:

Design and simulate various electronic circuits using software tool.

**Cognitive :** Understand applications of MATLAB and OrCAD tool

**Psychomotor :** Analyze and simulate electronic circuits

**Affective :** Attitude of i) Design ii) Test iii) Simulate iv) Debug v) Analyze

### COURSE OUTCOMES:

**EIF405-1** Understand sub windows in MATLAB

**EIF405-2** Illustrate basic mathematics and library functions

**EIF405-3** Draw and study communication blockset in simulink

**EIF405-4** Introduce OrCAD tools

**EIF405-5** Design, Simulate and analyze schematics of electronic circuits

**EIF405-6** Outline PCB layout

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO)

#### MATRIX :

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
Competency: Design and simulate various electronic circuits using software tool	-	2	2	2	-	-	-	1	-	2	3	3
EIF 405-1	2	-	2	2	-	-	-	1	-	-	2	2
EIF 405-2	2	-	2	2	-	-	-	1	-	-	2	3
EIF 405-3	1	2	3	3	-	-	-	2	-	2	3	3
EIF 405-4	2	-	1	2	-	-	-	1	-	-	2	2
EIF 405-5	-	2	3	2	-	-	-	2	-	2	3	2
EIF 405-6	-	2	3	2	-	-	-	2	-	2	2	3



**CONTENT :**

Chapter	Name of the Topic
<b><i>EIF405-1 Understand sub windows in MATLAB</i></b>	
01	<b>Introduction to MATLAB &amp; SIMULINK</b> 1.1. Introduction to MATLAB Environment 1.2. Introduction to SIMULINK Environment 1.3. Simulation parameters, development of Model 1.4. M-File Environment 1.5. Basic Analysis on command line Interface.
<b><i>EIF405-2 Illustrate base mathematics and library functions</i></b>	
02	<b>MATLAB Programming Basics</b> 2.1 Data Types 2.2 Base Mathematics 2.3 Plotting functions 2.4 Script File 2.5 Function File 2.6 Different 2D, 3D plotting techniques 2.7 Algorithm development using MATLAB
<b><i>EIF405-3 Draw and study communication blockset in simulink</i></b>	
03	<b>Industrial Applications of MATLAB</b> 3.1 Introduction to Communication BlockSet 3.1.1 Analog Communication 3.1.1.1 Analysis of Frequency Modulation 3.1.1.2 Analysis of Amplitude Modulation 3.1.1.3 Analysis of Phase Modulation 3.1.2 Digital Communication 3.2 Transfer function representation- CT,DT
<b><i>EIF405-4 Introduce OrCAD tools</i></b>	
04	<b>Introduction TO OrCAD</b> 4.1 Introduction to OrCAD tools - OrCAD Capture, OrCAD PSpice, OrCAD, Layout 4.2 Features of OrCAD tools
<b><i>EIF405-5 Design schematics of electronic circuits</i></b>	
05	<b>OrCAD Capture</b> 5.1 Use of Capture in Schematic Development. 5.2 Modification, Editing of different parts. 5.3 Study of properties. 5.4 Schematic development.
<b><i>EIF405-6 Simulate and analyze electronic circuits</i></b>	
06	<b>OrCAD Pspice</b> 6.1 Different Simulation techniques

	6.2 Transient Analysis - OpAmp Circuitry 6.6.1 Bias Point Analysis - Resistive Circuitry 6.6.2 A/C Sweep - Frequency Response 6.6.3 D/C Sweep - Transistor Analysis
<b>EIF 509-1405-7 Outline PCB layout</b>	
07	<b>OrCAD Layout</b> 7.1 Clean Room Standards. 7.2 Basics of PCB layers. 7.3 Component Routing -Auto Routing & Manual Routing. 7.4 Post Processing Techniques.

**B)TERM WORK** Term work shall consist of the following :

**(Minimum Eight From List)**

**Laboratory experiments and related skills to be developed :**

Sr. No	Laboratory experience	Skills developed	Course outcomes
1.	Introduction to MATLAB IDE	1) Students will able to know different tools of MATLAB 2) Students will able to know M-File, Simulink, Toolbox IDEs	EIF405-1
2.	Using MATLAB Command line window	1) Students will able to know executing different basic MATLAB commands -I/O, Arithmetic, algebraic etc 2) Students will able to know managing variables, solving, equations, understanding Vectors and Matrices 3) Students will able to access Online Help from Command line 4) Students will able to know - Graphics commands	EIF405-1, EIF405-2
3.	Study of Base Mathematics in MATLAB	1) Verify simple mathematical operations of all elements in row/column vector. using MATLAB a. Sum b. Mean c. Length d. Max e. Min f. Prod g. Sign h. Round i. Sort j. Fix	EIF405-2

<b>4.</b>	Matrix Operations	1) Use of commands to a. Find the determinant, inverse & transpose of the given 2X2 matrix b. Evaluate the mathematical operation using matrix	EIF405-2
<b>5.</b>	Study of Plotting functions	1) Students will be able to know to plot in MATLAB	EIF405-3
<b>6.</b>	Study of file handling & string manipulation (any two each)	1) Students will be able to use file handling and string manipulation	EIF405-3
<b>7.</b>	Looping & Branching instructions (any two each)	Students will be able to use looping and branching instruction	EIF405-1, EIF405-2, EIF405-3
<b>8.</b>	Introduction to Communication BlockSet	1) Students will be able to know what is Blockset 2) Students will be able to know communication blockset various blocks 3) Students will be able to use various blocks	EIF405-3
<b>9.</b>	Analysis of Amplitude Modulation	1) Students will be able to know to do analysis in MATLAB	EIF405-3
<b>10.</b>	Analysis of Frequency Modulation	1) Students will be able to know to do analysis in MATLAB	EIF405-3
<b>11.</b>	Introduction to OrCAD tools - OrCAD Capture, OrCAD PSpice, OrCAD Layout	1) Students will be able to know various tools of OrCAD	EIF405-4
<b>12.</b>	Designing circuit using OrCAD Capture-Rectifiers, Filters, Amplifiers -Oscillators, Multivibrators	1) Students will be able to know to design simple electronic circuits using OrCAD tool Capture	EIF405-4, EIF405-5
<b>13.</b>	Analysis of above electronic circuits using OrCAD Pspice	1) Students will be able to know to Analyze simple electronic circuits designed using OrCAD tool Capture	EIF405-6
<b>14.</b>	PCB Layout designing of of above electronic circuits using OrCAD Layout	1) Students will be able to know to design PCB layout of simple electronic circuits designed using OrCAD tool Layout	EIF405-7

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

**a) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per given criteria .

**ii) Progressive Skill Test:**

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

**Criteria for Continuous Assessment of Practical work and Progressive Skill Test:**

Sr. No	Criteria	Marks allotted
1.	Attendance at regular practical	5
2.	Preparedness for practical	10
3.	Correct figures / diagrams	10
4.	Observation tables	10
5.	Result table / calculations / graphs	10
6.	Safety / use of proper tools	5
	Total	50

**b) Criteria for assessment at semester end practical exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct diagram	10
3	Logical approach	10
4	Program/coding	10
5	Result	10
	Total	50

**REFERENCE MATERIAL:**

**a) Books / Codes**

<b>Sr. No.</b>	<b>Author</b>	<b>Title</b>	<b>Publisher</b>
1.	Delores M. Etter, David C. Kuncicky, Doug Hull	Introduction to MatlabR 6	Second Edition PEARSON Education Low Price Edition
2.	Rudra Pratap	Getting Started With Matlab7	Oxford University Press, Incorporated, 2006
3	Brian R. Hunt , Ronald L. Lipsman, Jonathan M. Rosenberg	A guide to MATLAB For Beginners and Experienced Users	Cambridge University Press
4	M. H. Rashid	Introduction to P-spice using OrCAD for circuits and Electronics	Pearson Education

**b) Websites**

- 1) <http://www.mathworks.in/>
- 2) [www.scilab.org/](http://www.scilab.org/)
- 3) [www.cadence.com/products/orcad](http://www.cadence.com/products/orcad)

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**COURSE ID :**

**Course Name : ELECTRONIC CIRCUIT DESIGN**  
**Course Code : EIF 406**  
**Course Abbreviation : FECD**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of Two tests (1hour each)	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)		One Oral	
Marks	20	--	80	--	25I	125

\*\* Assessment as per Pro-forma II.

I– Internal Examination

**RATIONALE :**

Though Diploma holders are not design engineers, they must have some basic knowledge of circuit design. Electronic circuits are widely used in industries, communication, etc. This subject will help a student to maintain these various circuits and also for development of his career as a design Engineer.

**COMPETENCY:**

Design and build electronic circuit for different engineering applications.

**Cognitive :**Understanding specification of electronics component.

**Psychomotor :**design electronic circuits for different applications.

**Affective :**Attitude of i) Design ii) Test iii) Diagnoseiv)Analyze v) Formulate

**COURSE OUTCOMES:**

**EIF 406-1** Identify specification of different electronics component.

**EIF 406-2** Design power supply as per requirement

**EIF 406-3** Design small signal amplifier.

**EIF 406-4** Differentiate and design power amplifier

**EIF 406-5** Classify and formulate oscillators

**EIF 406-6** Differentiate and design multivibrator

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Design electronic circuit for different engineering applications	-	1	3	-	1	-	-	2	-	-	3	3
<b>EIF 406-1</b>	1	-	1	-	-	-	-	-	-	-	1	1
<b>EIF 406-2</b>	-	2	2	-	1	-	-	2	-	-	3	3
<b>EIF 406-3</b>	-	2	2	-	1	-	-	2	-	-	3	2
<b>EIF 406-4</b>	-	2	2	-	1	-	-	2	-	-	2	2
<b>EIF 406-5</b>	-	2	2	-	1	-	-	2	-	-	3	2
<b>EIF 406-6</b>	-	2	2	-	1	-	-	2	-	-	3	2



**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	TOPIC/ SUB TOPIC	Teaching (Hours)	Theory evaluation Marks
<b>EIF 406-1 Identify specification of different electronics component</b>			
01	<b>Electronics Components and Specifications</b> Specifications of following: 1.1 Transformer Power, Audio frequency, Intermediate frequency, pulse transformer. 1.2 Semiconductor diode 1.3 Zener diode 1.4 Bipolar Junction Transistor 1.5 Field Effect Transistor 1.6 Unipolar Junction Transistor (NUMERICAL BASED ON ABOVE TOPIC)	04	08
<b>EIF 406-2 Design power supply as per requirement</b>			
02	<b>Design of Power Supply</b> 2.1 Design of Center-tapped rectifier with C , L- section , pi filter. 2.2 Design of Bridge rectifier with C , L- section , pi filter. 2.3 Design of Zener shunt regulator. 2.4 Design of Transistor series regulator(emitter follower) 2.5 Design of 723 low and high voltage regulator 2.6 Design of Three terminal regulator-78xx and 79xx. (NUMERICAL BASED ON ABOVE TOPIC)	10	16
<b>EIF 406-3Design small signal amplifier.</b>			
3	<b>Design of small signal amplifier.</b> 3.1 Design of small signal voltage amplifier (CE) 3.2Design of Emitter follower, Darlington Emitter follower. 3.3Types of coupling in multistage amplifiers-Direct Coupling and RC coupling 3.4 Design of two stage R-C coupled amplifier 3.5 Design of Common-source amplifier 3.6Design of source follower. (NUMERICAL BASED ON ABOVE TOPIC)	10	16
	<b>TOTAL:</b>	<b>24</b>	<b>40</b>

## Section II

<i>EIF 406-4 Differentiate and design power amplifier</i>			
04	<b>Power Amplifier Design</b> <b>4.1</b> Classification of power amplifiers, Efficiency considerations, comparison <b>4.2</b> Design of Class A power amplifier with Resistive load <b>4.3</b> Design of transformer coupled class A Power amplifier <b>4.4</b> Design of Class B Push Pull power Amplifier <b>4.5</b> Design of Class AB Push Pull power Amplifier. <b>4.6</b> Design of complementary Symmetry Power amplifier	07	12
<i>EIF 406-5 Classify and formulate oscillators</i>			
5	<b>Design of Oscillators</b> <b>5.1</b> Barkhausen's Criteria <b>5.2</b> Design of R-C phase shift oscillator using BJT and Op-amp <b>5.3</b> Design of Wien bridge oscillator using BJT and Op-amp <b>5.4</b> Design of Colpitt's oscillators using BJT and Op-amp <b>5.5</b> Design of Hartley oscillator using BJT and Op-amp	10	16
<i>EIF 406-6 Differentiate and design multivibrator</i>			
06	<b>Design of Multivibrators</b> <b>6.1</b> Design of Astable multivibrator using IC 555 and Op-amp <b>6.2</b> Design of Monostable multivibrator Using IC 555 and Op-amp. <b>6.3</b> Design of Bistable multivibrator using IC 555.  (NUMERICAL BASED ON ABOVE TOPIC)	07	12
	<b>TOTAL:</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of Topic	Distribution of Marks (Level wise )			Course Outcomes	Total Marks
		Knowledge	Comprehension	Application		
1.	Electronics Components and Specifications	04	04	00	EIF 406-1	08
2.	Design of power Supply	04	04	08	EIF 406-2	16
3.	Design of small signal amplifier	04	04	08	EIF 406-3	16
4.	Power Amplifier design	02	02	08	EIF 406-4	12
5.	Design of Oscillators	04	04	08	EIF 406- 5	16
6.	Design of Multivibrators	02	02	08	EIF 406-6	12
				Total		80

B) **TERM WORK** Term work shall consist of the following :  
(Minimum Eight From List)

**Laboratory experiments and related skills to be developed :**

Sr.no	Laboratory experiments	Skills Developed	Course Outcomes
1	Study of colour coding of resistor, capacitor, inductor and specification of diode ,BJT , FET, OPAMP	Study specification of diode ,BJT , FET, OPAMP. DETERMINE values of resistor, capacitor, Inductor using colour coding.	EIF 406-1
2	Full wave rectifier design with filter with given specifications.	Design bridge rectifier with capacitor filter and test it on breadboard	EIF 406-2
3	Design of zener regulator with given specifications.	Design Zener regulator with specific requirements and test it on breadboard	EIF 406-2
4	Design of transistorised series regulator with given specifications.	Design circuit with given specification and test it . Do fault finding if required	EIF 406-2
5	Design of 723 regulator.	Design Low and High voltage regulator using IC 723 and test it	EIF 406-2
6	Design of small signal voltage amplifier.	Design BJT/FET amplifier for specific voltage gain and test it	EIF 406-3
7	Design of power amplifier.	Design class B complementary symmetry power amplifier and test it	EIF 406-4
8	Design of LC/RC oscillator	Design any one oscillator circuit for specified frequency and test it	EIF 406-5
9	Design of multivibrator.	Design astable / monostable MV using 741 / 555/ BJT for specific frequency and test it	EIF 406-6
10	Miniproject on different electronics design.	Design, test and find different types of fault in different electronics circuits.	EIF 406-1,2,3,4,5,6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

**a) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments :**

Every practical assignment shall be assessed for 25 marks as per given criteria .

**iii) Progressive Skill Test :**

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

**Criteria for Continuous Assessment of Practical work and Progressive skill Test :**

Sr.No.	Particulars	Marks
1	Attendance	05
2	Correct figures	05
3	Proper Observation & Result Table	05
4	Sample Calculation with relevant Formulae	05
5	Procedure/ Workmanship/ Safety	05
	Total	25

**b) Criteria for assessment at semester end oral exam :**

Every student shall be assessed as per following criteria

Sr. no	Criteria	Marks allotted
1	Presentation	10
2	Conceptual Understanding	10
3	Confidence and Overall Performance	05
	Total	25

### INSTRUCTIONAL STRATEGIES :

**Instructional Methods :**

work      1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory

**Teaching and Learning resources:**

1. Chalk board      2. Video clips      3. Slides      4. Item Bank      5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	N.C.Goyal&R.K.Khetan	A monograph on Electronic Design Principles	Khanna
2.	Millman&Halkias	Electronic Circuits & Devices	McGraw-Hill
3	B. S. Sonde	Power Supplies	Tata McGraw-Hill
4	Mottershad.	Electronic Devices & Circuits	PHI
5	Mantri Jain	Electronic circuit design	Techmax
6	Talbar and Sontakke	Electronic circuit design	Sadhu Sudha Publications,

**b) Websites**

- i. [www.electroschematics.com](http://www.electroschematics.com)
- ii. [www.discovercircuits.com](http://www.discovercircuits.com)
- iii. [www.allaboutcircuits.com](http://www.allaboutcircuits.com)
- iv. [www.learningcircuits.co.uk](http://www.learningcircuits.co.uk)

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**COURSE ID :**

**Course Name : SIGNALS AND SYSTEMS**  
**Course Code : ETF 407**  
**Course Abbreviation : FSNS**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : <>**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
<b>DET Fails of Evaluation</b>	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	--	<b>Oral</b>	
<b>Marks</b>	<b>20</b>	<b>--</b>	<b>80</b>	<b>--</b>	<b>25 I</b>	<b>125</b>

E-External Assessment

\* Assessment as per Pro-forma II

**COMPETENCY:**

Signals and systems for different engineering applications.

**Cognitive :** Understanding classification of different signals and systems.

**Psychomotor :** Analyze different properties of Signals and Systems and perform different operations on signals and systems.

**Affective :** Attitude of i) Logic ii) accuracy iii) precision v) punctuality

### COURSE OUTCOMES:

- ETF 407-1 Understand classification of all continuous and discrete signals.  
 ETF 407-2 Analyze different operations on signals.  
 ETF 407-3 State Sampling theorem and illustrate Reconstruction of signals and aliasing.  
 ETF 407-4 Identify different types of the systems.  
 ETF 407-5 Describe Linear time invariant systems using mathematical models and discuss their properties.  
 ETF 407-6 Define Z- Transform and specify properties of Z- Transform.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX:

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs										PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning		
<b>Competency:</b> Signals and systems for different engineering applications.	1	2	1	1	-	-	-	-	-	-	1	1
ETF 407-1	2	-	1	1	-	-	-	-	-	-	1	1
ETF 407-2	2	-	1	1	-	-	-	-	-	-	1	1
ETF 407-3	2	1	1	1	-	-	-	-	-	-	1	1
ETF 407-4	2	-	1	1	-	-	-	-	-	-	1	1
ETF 407-5	2	1	1	1	-	-	-	-	-	1	1	1
ETF 407-6	1	2	1	1	-	-	-	-	-	-	1	1



**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>ETF 407-1. Understand classification of all continuous and discrete signals.</i>			
<b>1</b>	<b>Introduction to signals and systems.</b> 1.1 Signals. 1.2 Systems. 1.3 Classification of signals. 1.3.1 Multichannel And Multidimensional Signals 1.3.2 Continuous time Vs discrete time signals. 1.3.3 Continuous valued Vs discrete Valued signals. 1.3.4 Discrete Vs random signals. 1.3.5 Periodic Vs aperiodic signals 1.3.6 Symmetrical Vs Asymmetrical Signals 1.3.7 Energy and power signals 1.3.8 Odd and Even signals.	06	10
<i>ETF 407-2. Analyze different operations on signals.</i>			
<b>2.</b>	<b>Signal operations.</b> 2.1 Standard test signals. 2.2 Operations like shifting, folding, addition, folding-shifting & shifting -folding, Amplitude scaling, Multiplication, Sample rate increase & decrease operation Time scaling. (Numerical based on signal operations)	08	16
<i>ETF 407-3 State Sampling theorem and illustrate Reconstruction of signals and aliasing.</i>			
<b>3</b>	<b>Sampling, Aliasing &amp; Reconstruction</b> 3.1 Sampling, Aliasing 3.2 Sampling Theorem. 3.3 Reconstruction of Signals 3.4 Quantization of Signals. (Numerical based on sampling theorem)	10	14
	<b>TOTAL:</b>	<b>24</b>	<b>40</b>

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>ETF 407- 4 Identify different types of the systems.</i>			
<b>4</b>	<b>Systems</b> 4.1 Classification of systems 4.1.1 Static and dynamic systems 4.1.2 Time invariant and time variant system 4.1.3 Linear and Non linear system 4.1.4 Causal and Noncausal System 4.1.5 Stable and unstable system (Numerical based on identification of systems)	04	12
<i>ETF 407-5 Describe Linear time invariant systems using mathematical models and discuss their properties.</i>			
<b>5</b>	<b>Linear time Invariant system and convolution</b> 5.1 Convolution sum of LTI system 5.2 Properties of convolution sum 5.3 Tabular method 5.4 Concept of Correlation 5.3.1 Types of correlation- cross correlation and auto correlation. (Numerical based on Convolution sum and correlation )	10	14
<i>ETF 407-6 Define Z- Transform and specify properties of Z- Transform.</i>			
<b>6</b>	<b>Z-transform</b> 6.1 Introduction 6.2 ROC (Region of Convergence) 6.3 Properties of Z transform 6.4 Pole-zero plot 6.5 Inverse Z transform 6.5.1 Power series method 6.5.2 Partial Fraction Expansion (PFE) method (Numerical based on <b>Z-transform and</b> Inverse Z transform)	10	14
	<b>TOTAL:</b>	<b>24</b>	<b>40</b>

**Specification table for sETFTing question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Introduction to signals and systems	06	06	00	ETF 407-1	12
2	Signal operations.	02	10	04	ETF 407-2	16
3	Sampling, Aliasing & Reconstrucion	02	04	02	ETF 407-3	08
4	<b>Systems</b>	02	04	06	ETF 407-4	12
5	Linear time Invariant sytsem and convolution z-transform	08	02	06	ETF 407-5	16
6	Introduction to signals and systems	<b>06</b>	<b>04</b>	<b>06</b>	ETF 407-6	<b>16</b>
TOTAL		14	30	36	ETF 407-7	80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

## B) TERM WORK

### Practical Exercises and related skills to be developed :

The following practical exercises shall be conducted as Term Work as dailed in the *Laboratoty Manual for Applied Mechanics* developed by the Institute in practical sessions of batches of about 22 students:

Sr No.	Title of Practical Exercise	Skills / Comptencies to be developed	Course Outcome
1.	a).Introduction of Matlab environment b)To study CT signal and DT signal.	Matlab programming	ETF 407-1
2.	a)Generation of Waveform using Matlab.	Matlab programming	ETF 407-2
3.	a).Generation of Standard test signals b)To perform the basic operation on discrETFe time signal	Matlab programming	ETF 407-2
4.	To study Sampling process and sampling theorem	Matlab programming	ETF 407-3
5.	To study various types of systems on the basis of linearity, time invariance, memory ,stability, causality.	Matlab programming	ETF 407-4

6.	To study convolution sum of two sequences.	Matlab programming	ETF 513-1
7.	To study cross correlation of two sequences.	Matlab programming	ETF 407-5
8.	To study Auto correlation of two sequences.	Matlab programming	ETF 513-5
9.	a)To study Pole –zero plot in Z transform b) To verify Z transform	Matlab programming	ETF 513-6
10.	Case study of DSP Processor	Knowledge about processor	

**Criteria for Progressive Assessment of Tutorial and Skill Test**

**ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION**

**a) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks

**ii) Progressive Skill Test:**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted.

**b) Term End Oral Examination :**

Every student shall be assessed as per following criteria

Sr. no	Criteria	Marks allotted
1	Presentation	10
2	Conceptual Understanding	10
3	Overall Performance	05
	Total	25

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Demonstrations
2. Classroom practices

**Teaching and Learning resources:**

1. Chalk board
2. LCD presentations
3. Audio presentations

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**REFERENCE MATERIAL :**

**a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1	Ramesh Babu	‘Signals & system’	SciTech Publication
2	H.A HSU,	‘Signals & system’ (Schaum’s out lines)	Tata McGraw Hill
3	Shah & Bhagli	‘Signal & System’	Mahalaxmi Publication
4	Simon Haykin, Barry Van Veen-	‘Signals & system’	IIInd Edition Wiley publication
5	N.G.Palan	Digital signals and processing	Tech max publications.

**b) Websites**

i) [www.auupdates.com/.../ec2204-signals-and-systems](http://www.auupdates.com/.../ec2204-signals-and-systems)

\* \* \*



**COURSE ID:**

Course Name :SATELLITE COMMUNICATION  
Course Code :ETF 408  
Course Abbreviation :FSAT

**TEACHING AND EVALUATION SCHEME:**

Pre-requisite Course(s) : --

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)		*ORAL	
Marks	20	--	80	--	25I	125

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE:**

In the context of worldwide communication network, satellite communication systems are very important. Satellite communication links add capacity to existing communication capabilities and provide additional alternate routings for communication traffic. They have unique advantage over conventional long distance transmission. It is unaffected by propagation variation that interfere with hf radio.

**COMPETENCY**

Pertain basic knowledge about satellite types, orbits and applications.

**Cognitive:** Construct basic understanding of satellite communication techniques.

**Psychomotor:** Characterize basic idea related to satellites. .

**Affective:** Attitude of I) self-awareness ii) chastity iii) intuitive mind v) decision making

## COURSE OUTCOMES:

**ETF 408 -1.** Understand the fundamentals of satellite communication and list different parts and specify various orbits for satellite.

**ETF 408 -2.** Describe satellite signal stability by defining azimuth and elevation angle and tell various bands used for satellite communication.

**ETF 408 -3.** Understand uplink and downlink modules and discuss functions of its various parts.

**EIF 408 -4.** Relate various multiple access approaches to satellite communication and discuss downlink analysis.

**ETF 408 -5.** Identify different sub-systems of satellite communication and explain their roles.

**ETF 408 -6.** Illustrate different services offered by satellite communication in data and voice communication.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-”:no correlation]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency</b> Pertain basic knowledge about satellite types, orbits and applications	1	2	1	-	-	-	-	-	-	-	-	2
<b>EIF 408 -1</b>	2	1	1	-	-	-	-	-	-	-	-	-
<b>EIF 408 -2</b>	2	1	1	-	-	-	-	-	-	-	-	2
<b>EIF 408 -3</b>	2	2	1	-	-	-	-	-	-	-	-	-
<b>EIF 408 -4</b>	1	2	-	-	-	-	-	-	-	-	-	-
<b>EIF 408 -5</b>	1	2	-	-	2	-	-	-	-	1	-	2
<b>EIF 408 -6</b>	2	2	2	-	2	-	-	-	-	1	-	2



**CONTENT:**

**C) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>ETF 408 -1.</b> Understand the fundamentals of satellite communication and list different parts and specify various orbits for satellite			
<b>1</b>	<b>Fundamentals of Satellite communication</b> 1.1 Classification of satellite – synchronous, asynchronous, active, passive. 1.2 Kepler's law. 1.3 Satellite orbit and their operating frequencies- low earth orbit, Medium earth orbit, geosynchronous orbit. 1.4 Satellite orbital terms- apogee, perigee, major axis, minor axes. 1.5 Functions Of transponder, payload (bus), and ground station.	06	10
<b>ETF 408 -2.</b> Describe satellite signal stability by defining azimuth and elevation angle and tell various bands used for satellite communication.			
<b>2.</b>	<b>Satellite angles , controls and stabilization</b> 2.1 Sub satellite point. 2.2 Look angles- angle of elevation, azimuth angle. 2.3 Angle of inclination. 2.4 Different frequency band used in satellite and their uplink, downlink frequency C band, X band, Ku band, Ka band. 2.5 Terms used in Satellite - footprint, orbital drift, attitude control, propulsion control, eclipse effect, (E-W) station keeping, (S-N) station keeping. 2.6 Satellite stabilization- spins stabilization, three axis Stabilization. 2.7 Placement of satellite in geostationary orbit.	10	16
<b>ETF 408 -3.</b> Understand uplink and downlink modules and discuss functions of its various parts.			
<b>3</b>	<b>Satellite System model</b> 3.1 Block diagram of uplink model. 3.2 Block diagram of transponder. 3.3 Block diagram of down link model. 3.4 Block diagram of satellite sub system showing telemetry controls main rocket, auxiliary rocket, generation of power, transponders. 3.5 Block diagram of telemetry tracking unit. 3.6 Application and advantage of satellite communication.	10	14
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to			

attempt questions of the above allotted marks only.

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>ETF 408 -4. Relate various multiple access approaches to satellite communication and discuss downlink analysis.</b>			
<b>4</b>	<b>Interference and Satellite access</b> 4.1 Introduction 4.2 Interference between satellite circuits 4.3 Satellite access 4.4 Single access 4.5 Preassigned FDMA, SCPC (spade system), TDMA, pre-assigned TDMA, demand assigned TDMA, down link analysis 4.6 Comparison of uplink power requirements for TDMA & FDMA	10	14
<b>ETF 408 -5. Identify different sub-systems of satellite communication and explain their roles.</b>			
<b>5</b>	<b>Space segment</b> 5.1 Introduction 5.2 Power supply units 5.3 Altitude control 5.4 Station keeping 5.5 Thermal control 5.6 TT&C, transponders 5.7 Antenna subsystem.	10	14
<b>ETF 408 -6. Illustrate different services offered by satellite communication in data and voice communication.</b>			
<b>6</b>	<b>Satellite Services</b> 6.1 Packet satellite networks and services 6.2 fixed satellite services 6.3 broadcast satellite services 6.4 mobile satellite services- VSAT 6.5 global positioning satellite system 6.6 Maritime satellite services 6.6 gateways, ATM over satellite 6.7 role of satellite in future network.	08	12
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Fundamentals of Satellite communication	04	00	04	ETF 408-1	08
2	Satellite angles, controls and stabilization	06	04	06	ETF 408-2	16
3	Satellite System model	06	04	04	ETF 408-3	14
4	Interference and Satellite access	06	06	04	ETF 408-4	16
5	Space Segment	04	06	04	ETF 408-5	14
6	Satellite Services	04	04	04	ETF 408-6	12
	TOTAL	30	24	26		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**D) TERM WORK**  
(Minimum Eight From List)

**Practical Exercises and related skills to be developed:**

The following practical exercises shall be conducted on trainer kit/bread-board:-

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	To study concepts of Satellite Communication	Understanding different parts and types of Satellite Communication	ETF 408 -1
2.	To set up Direct link	Understanding different frequency such as uplink & downlink frequency.	ETF 408 -2
3.	To set up Active Satellite link	Understanding concept of active satellite link.	ETF 408 -2
4.	Study Satellite transponder	Understanding function of satellite transponder.	ETF 408 -2
5.	To set up Satellite communication link	Understanding concept of Satellite communication link	ETF 408 -3
6.	Study Audio-Video transmission through Satellite link	Transmission of audio – video data through satellite link	ETF 408 -3
7.	To transmit and receive function generator waveforms through Satellite link	Understanding the way how function generator waveform can be transfer & receive through satellite link.	ETF 408 -6
8.	To establish PC-to-PC	Established the communication link	ETF 408 -6

	Communication using Satellite Communication link	between PC to PC	
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### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

**x) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments:**

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Progressive Skill Test:**

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma VI*.

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

**y) Criteria for assessment at semester end ORAL exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Presentation	10
2	Conceptual Understanding	10
3	Overall Performance	05
	Total	25

Assessment at semester end ORAL exam as per Pro-forma II.

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Frenzel	Communication electronics	McGraw Hill international
2.	K.R.Botkar	Dr D. C Agarwal	Khanna Publications
3	Wayne Tomasi	Electronic communication	Tata McGraw-Hill
4	George Kennedy	Electronic communication system	Tata McGraw-Hill

**b) Websites:**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)
- 2) [www.onlinevideolecture.com](http://www.onlinevideolecture.com)
- 3) [www.satellite.com](http://www.satellite.com)

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# **LEVEL -V MANAGEMENT AND DIVERSIFIED COURSES**





**COURSE ID:**

**Course Name : MOBILE COMMUNICATION**  
**Course Code : ETF 501**  
**Course Abbreviation: FMCM**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : <Nil >**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Practical Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	--	*Oral	
Marks	20	--	80	--	25 E	125

\* Assessment as per pro-forma I

E – External Examination

**RATIONALE :**

The glorious 21st century marks the mobile radio communication industry by orders of magnitude. The recent exponential growth in cellular mobile communication needs more skilled technicians for operation, maintenance & servicing of mobile cellular system. This subject is classified under technology group and it is based on communication theory, which gives theoretical as well as practical knowledge of different cellular system. It covers digital cellular mobile system. It covers digital cellular mobile system such as GSM, IS – 95 standards, WLL, call processing & basic of mobile communication system.

**COMPETENCY**

Study different mobile systems and the working.

**Cognitive :** Acquire knowledge of different mobile systems.

**Psychomotor:** Discover and analyze different mobile systems.

**Affective :** Attitude of i)curiosityii)analyze

### COURSE OUTCOMES :

ETF 501-1 Compare operation of different mobile communication system

ETF 501-2.Acquire details of blocks mobile handset.

ETF 501-3.Gain the knowledge of coverage & capacity in cellular system

ETF 501-4.Study GSM system architecture

ETF 501-5.Discover CDMA system architecture

ETF 501-6 Gain knowledge of modern wireless communication systems.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Study different mobile systems and the working	2	1	1	-	-	-	-	1	-	-	-	2
<b>ETF 501-1</b>	2	-	1	-	-	-	-	1	-	-	-	-
<b>ETF 501-2</b>	2	-	1	-	-	-	-	1	-	-	-	-
<b>ETF 501-3</b>	2	1	2	-	-	-	-	2	-	-	-	-
<b>ETF 501-4</b>	2	1	-	-	-	-	-	-	-	-	-	2
<b>ETF 501-5</b>	2	1	-	-	-	-	-	-	-	-	-	2
<b>ETF 501-6</b>	2	1	1	-	-	-	-	1	-	2	-	2

**CONTENT :**

**E) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>ETF 501- 1 Compare operation of different mobile communication system</i>			
<b>1</b>	<b>Introduction to wireless communication system</b> 1.1 Evolution of mobile radio communication 1.2 Mobile radio system around the world. (Such as AMPS, N- AMPS, IS-95, GSM) 1.3 Related definition base station, control channel, forward channel etc. 1.4 Examples of wireless communication system such as cordless telephone system, cellular telephone system, how cellular telephone call is made	10	12
<i>ETF 501- 2. Acquire details of blocks mobile handset.</i>			
<b>2.</b>	<b>Mobile unit</b> 2.1 Block Diagram and operation of mobile unit 2.2 Block diagram and operation of 2.2.1 Transmitter 2.2.2 Receiver, 2.2.3 Frequency synthesizer 2.2.4 Logic unit 2.2.5 Control unit	08	10
<i>ETF 501- 3. Gain the knowledge of coverage &amp; capacity in cellular system</i>			
<b>3</b>	<b>The cellular concept.</b> 3.1 Introduction a basic cellular system. 3.2 Frequency reuse 3.3 Hand off, Type of hand off, hard hand off, soft Hand off, delayed and queued hand off 3.4 Interference & system capacity. 3.4.1 Co channel interference & system capacity. 3.4.2 Channel planning for wireless system. 3.4.3 Adjacent channel Interference. 3.4.4 Power control for reducing interference (Closed loop, Open loop) 3.5 Improving coverage and capacity in cellular system. 3.5.1 Cell splitting. 3.5.2 Sectoring. 3.5.3 Repeater for range extension. 3.5.4 Micro cell zone concept.	14	18
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>ETF 501- 4. Study GSM system architecture</i>			
<b>4</b>	<b>GSM mobile systems.</b> 4.1 G.S.M system architecture. 4.2 G.S.M services & features. 4.3 G.S.M radio subsystems. 4.4 G.S.M channel types. 4.5 Message & call processing in GSM 4.6 Privacy & security in GSM.	12	14
<i>ETF 501- 5. Discover CDMA system architecture</i>			
<b>5</b>	<b>CDMA mobile systems.</b> 5.1 CDMA digital cellular standard IS-95. 5.2 IS.95 frequency & channel specification 5.3 IS-95 system architecture. 5.4 IS-95 CDMA calls Processing. 5.5 Security & identification in IS-95 CDMA 5.6 Features of IS-95	12	14
<i>ETF 501- 6 Gain knowledge of modern wireless communication systems.</i>			
<b>6</b>	<b>Modern wireless communication system</b> 6.1 Bluetooth-architecture 6.2 Signal system no.7 (SS7)—performance services. 6.3 Mobile IP 6.4 3G W-CDMA (UMTS) (Universal mobile Telecommunication system.) 6.5 Next generation network (NGN) concept 6.6 Wireless local loop & LMDS (local multipoint distribution)	08	12
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Introduction to wireless communications system	4	8	-	ETF 501- 1	12
2	Mobile unit	4	6	-	ETF 501- 2	10
3	The cellular concept	4	6	8	ETF 501- 3	18
4	GSM mobile systems	2	4	8	ETF 501- 4	14
5	CDMA mobile systems	2	4	8	ETF 501- 5	14
6	Modern wireless communication system	6	-	6	ETF 501 - 6	12
TOTAL		22	28	30		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**F) TERM WORK**  
**(Minimum Eight From List)**

**Practical Exercises and related skills to be developed :**

The following practical exercises shall be conducted :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Study of SIM card	Understand differences between SIM sizes and their details	ETF 501- 2
2.	Study of transmission of file by using Bluetooth technology.	Understand of Bluetooth working.	ETF 501- 6
3.	Study of transmission of call from mobile.	Discover how channel formation is done for transmission of call	ETF 501-1
4.	Study of reception of call from mobile.	Discover how channel formation is done for reception of call	ETF 501-1
5.	Study of control signals used in mobile communication.	Discover working of control signals	ETF 501- 2
6.	Study of decision of hand off.	Gain knowledge types and conditions for hand-off	ETF 501- 3
7.	Study of cell site, distance coverage.	Understand cellular concept	ETF 501- 3
8.	Study of antenna used for mobile communication.	Working of different types of antennas	ETF 501- 3

9.	Study of 3G technology used for mobile communication.	Understand modern technologies	EIF 407- 3
10.	Study of different facilities provided by cellular company	Understand helpful applications	EIF 407- 3
11.	Seminar(Compulsory)	Confidence and discovery of new concepts	
12.	*Arrange a visit to BSNL exchange to study following practical.	Relate theory with actual working	

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### z) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma VI*.

#### aa) Criteria for assessment at semester end ORAL exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
2	Correct figures / diagrams	10
4	Result table / calculations / graphs	10
5	Presentation	05
	Total	25

Assessment at semester end practical exam as per Pro- forma I.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods:**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	T.S. Rappaport	Wireless Communication Principles & Practice	Pearson Education
2.	William Lee	Mobile Cellular Telecommunication	Tata McGraw Hill
3	Asoke Talukder & Roopa Yavagal	Mobile Computing	Tata McGraw Hill

**b) Websites:**

- i) <http://www.physicsclassroom.com>  
ii) <http://scienceworld.wolfram.com/physics/>  
iii) [www.pearsoned.co.in](http://www.pearsoned.co.in)

\* \* \*





**COURSE ID :**

**Course Name** : Optical fiber communication  
**Course Code** : ETF502  
**Course Abbreviation** : FOFC

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s)** : <>

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Practical	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	One practical (3 hours)	
Marks	20	25	80	50 I	150

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE :**

Now a days transmission Medias have become most important aspect of Telecommunication industry. Fiber optic Technology has become the backbone of the Telecommunication industry this development is going on so rapidly, to cope up with these latest technologies, students need to study the specialized subjects like fiber optic. They must understand the need of increasing data rate and lossless transmission medium.

**COMPETENCY:**

Analyze and develop optical fiber communication systems for different engineering applications.

**Cognitive :** Understanding basics of optical fiber communication systems, its components and losses occurring in OFC.

**Psychomotor :** Identify function different components used for building OFC systems .

**Affective :** Attitude of i) Logic ii) accuracy iii) precision iv) punctuality

### COURSE OUTCOMES :

**ETF502-1.** Recall the concepts of physics of light and interpret the basics of optical fiber communication.

**ETF502-2.** Classify and analyze the optical fiber types, structures, splicing techniques, connectors and coupling devices.

**ETF502-3** Analyze different types of losses in optical fiber communication and their measurement techniques.

**ETF502-4** Classify various types of optical sources, illustrate their principle of operation & explain their construction .

**ETF502-5** Classify various types of optical detector, illustrate their principle of operation & explain their construction.

**ETF502-6.** Analyze the optical communication system and its components.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Analyze and develop optical fiber communication systems for different engineering applications.	2	2	1	-	-	-	-	1	-	-	2	-
<b>ETF502-1</b>	2	-	1	-	-	-	-	1	-	-	1	1
<b>ETF502-2</b>	2	1	1	-	-	-	-	1	-	-	2	1
<b>ETF502-3</b>	2	1	1	-	-	-	-	1	-	-	2	1
<b>ETF502-4</b>	2	1	-	-	-	-	-	-	-	-	2	1
<b>ETF502-5</b>	2	2	1	-	-	-	-	1	-	-	2	1
<b>ETF502-6</b>	1	3	1	-	-	-	-	1	-	2	2	1

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lect ures (Hou rs)	Theory Evaluation (Marks)
<i><b>ETF502-1.</b> Recall the concepts of physics of light and interpret the basics of optical fiber communication.</i>			
<b>1</b>	<b>Physics of Light</b> 1.1 Spectrum of light. 1.2 Concept of photon, energy of photon. 1.3 Light intensity, its units, optical power. Reflection and refraction of light , Snell's Law. 1.4 Total internal reflection , acceptance angle Numerical aperture 1.6 Numerical based on above theory	08	08
<i><b>ETF502-2.</b> Classify and analyze the optical fiber types, structures, splicing techniques, connectors and coupling devices.</i>			
<b>2.</b>	<b>Optical fiber cable</b> 2.1 Advantages of optical fibers over coaxial Cable. 2.2 Construction Of Optical Fiber 2.3 Classification of optical fibers based on 2.3.1 Material used – Glass fibers, plastic fibers , 2.3.2 Index profile – Step index , Graded index, 2.3.3 Modes of propagation- single mode , Multimode fibers. 2.4 Light propagation in SI and GI fibers, Single and multimode fibers 2.5 Comparison of various fiber types 2.6. Optical Fiber cable Structure: Tight buffered, loose buffered 2.7Fiber splicing techniques. 2.8 Optical fiber connectors- requirements, Types 2.9 Optical Couplers and its types	12	16
<i><b>ETF502-3</b> Analyze different types of losses in optical fiber communication and their measurement techniques.</i>			
<b>3</b>	<b>Signal Degradation in Optical Fibers.</b> 3.1 Attenuation in optical fibers and its units. 3.2 Material absorption losses in silica glass Fibers- intrinsic and extrinsic absorption. 3.3 Scattering losses – linear and nonlinear 3.4 Fiber bend losses 3.5 Dispersion –intramodal , intermodal 3.6 Numerical based on above theory. 3.7Numerical aperture measurement 3.8 Attenuation Measurement: Cutback technique & insertion loss method 3.9 Eye patterns techniques	12	16
	<b>Total</b>	<b>32</b>	<b>40</b>

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>ETF502-4</b> Classify various types of optical sources illustrate their principle of operation & explain their construction.			
04	<b>Optical Sources.</b> 4.1 Types and requirements of optical sources. 4.2 Criteria for choosing material of source & Materials used for construction of optical Sources. 4.3 Direct and indirect Bandgap Semiconductor 4.4 LED: Concept Homojunction and heterojunction LED, materials used for construction of LED 4.4.1 Types of LED :- a) Planar LED: construction and working b) Surface emitting: construction and working c) Dome emitting: construction and working d) Edge emitting LED: construction and working 4.4.2 Characteristics of LED's: Optical output power, output spectrum, 1.4.3 Advantages, disadvantages & application of LED's 4.5 LASER :-Materials used for construction 4.5.1 Principle Of working :Absorption, Spontaneous, stimulated emission & population inversion of radiation 4.5.2 Characteristics of LASER: Monochromatic, Directional & coherent 4.5.3 Types Of LASER 4.5.4 Fabry –Perot Cavity Resonator- construction and working 4.5.5 Semiconductor injection laser- construction and working 4.5.6 Gain guides and index guided laser- construction and working 4.5.7 Advantages, Disadvantages and application of LASER 4.6 Comparison between LED and LASER	10	16
<b>ETF502-5</b> Classify various types of optical detector, illustrate their principle of operation & explain their construction.			
05	<b>Optical Detectors.</b> 5.1 Requirements of optical detectors 5.2 Principle of optical photo detectors 5.2 Types of photo detectors 5.3 Photo detector characteristics – quantum Efficiency, Responsivity 5.3 p-n Photodiode –	12	14

	5.4.1 construction and working, 5.4.2 Electrical Characteristic 1.4.3 Spectral response 5.4.4 Advantages, disadvantages, application 5.5 p-i-n photodiode – 5.5.1 Construction and working, 5.5.2 Electrical Characteristic , 5.5.3 Spectral response, 1.5.4 Speed of response, 5.5.5 Advantages, disadvantages, application 5.6 Avalanche photodiode – 5.6.1 Construction and working 5.6.2 Spectral response, 5.6.3 APD characteristics:- sensitivity, Operating speed ,noise 5.6.4 Advantages, disadvantages, application 5.7 Phototransistor – 5.7.1 Cross section 5.7.2 Electrical characteristics 5.8 Photo- Darlington. 5.8 Comparison of various photo detectors		
<b>ETF502-6. Analyze the optical communication system and its components.</b>			
<b>6</b>	<b>Optical fiber communication system</b> 6.1 Block diagram of optical fiber com. System 6.2 International Standards of OFC 6.3 The optical transmitter circuit- 6.3.1 Source Limitations , 6.3.2 LED drive circuits 6.3.3 Laser drive circuits. 6.4 The optical receiver circuit – 6.4.1 Block diagram 6.4.2 Preamplifier 6.4.3 AGC 6.4.4 Equalization. 6.5 Multiplexing – TDM ,FDM, WDM 6.6 Optical power budgeting. 6.7 Introduction to Optical Time Domain Reflectometer (OTDR) 6.8 Applications of OFC system – Telephone networks , Computer networks , military , consumer , Industrial	10	10
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Physics of light.	04	00	04	ETF502-1	08
2	Optical Fiber cable.	08	04	04	ETF502-2	16
3	Signal Degradation in optical fibers	06	06	04	ETF502-3	16
4	Optical Sources	06	06	04	ETF502-4	16
5	Optical Detectors	04	06	04	ETF502-5	14
6	Optical fiber comm. System	04	04	02	ETF502-6	10
	<b>TOTAL</b>	<b>32</b>	<b>26</b>	<b>22</b>		<b>80</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**

**Practical Exercises and related skills to be developed :  
(Minimum Eight From List)**

The following practical's should be performed on trainer kit which is available:

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Types of optical fiber cable.	i) Identify various types of optical fiber cables ii) Differentiate characteristics of it.	ETF502-2
2.	Fiber optics analog and digital transmission	Analyze and verify the relationship between input signal and received signal	ETF502-2
3.	Characteristic of fiber optic communication link	Verify and plot characteristics of fiber optic link i.e. $V_0/V_{in}$	ETF502-2
4.	LED and LASER-diode	i) Analyze and plot V-I characteristics of red, green and yellow color LED. ii) Analyze and plot V-I characteristics of Laser- diode	ETF502-4
5.	Numerical Aperture of fiber	Measure Numerical Aperture of optical fiber	ETF502-1
6.	Bending losses in optical fibers	Measure bending losses in various types of optical fibers	ETF502-3
7.	Propagation or attenuation loss in optical fiber	Measure attenuation in given optical fibers	ETF502-3
8.	Fiber voice link using intensity	Set up fiber optic voice link and verify input and output signals	ETF502-2
9.	Phototransistor	Verify and plot characteristics of Phototransistor	ETF502-5

10.	Modulation (any one type AM,FM,etc.)	Analyze ,verify & plot input signal and received signal	ETF502-6
11.	PIN, Photodiode and APD	Verify and plot characteristics of Photodiode , PIN Photodiode and APD	ETF502-5
12.	OTDR	Analyze construction and function Optical Time Domain Reflectometer front panel	ETF502-6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### bb) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>Total</b>		<b>25</b>

##### ii) Progressive Skill Test:

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma II*.

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	<b>Total</b>	<b>25</b>

##### cc) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	<b>Total</b>	<b>50</b>

Assessment at semester end practical exam as per Pro-forma II.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	John Senior	Optical Fiber Communication	PHI Publication
2.	Gerd Keiser	Optical Fiber Communication	TMH Publication
3	Deboo , Burros	Integrated circuits and semiconductor devices	Tata McGraw Hill

**b) Websites:**

- iv) <http://www.physicsclassroom.com>  
v) <http://scienceworld.wolfram.com/physics/>  
vi) [www.pearsoned.co.in](http://www.pearsoned.co.in)  
vii) [www.nptel.ac.in](http://www.nptel.ac.in)

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**COURSE ID :**

Course Name : Project - I  
Course Code : EIF503  
Course Abbreviation : FPR1

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s) : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	00	02
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Term Work*	Oral (Internal)	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-VI	As per Proforma-V	
Marks	--	--	NIL	50	25I	75

I – Internal Examination \* -

**RATIONALE :**

In the field of Electronics Technology various technologies (hardware and Software) needs to be integrated and proper paradigms needs to be implemented to develop any kind of electronic system . Hence it becomes essential to get hands on experience for developing industrial applications. This subject is essential to understand the implementation of the system development process i.e. analyze, design, coding, debugging and testing.

The project work should be undertaken by group of 3 students who will jointly work and implement the project with the approval of guide. The group of student should decide the area of proposed work as per requirement of Industry/community or environment and work together for hardware & software if necessary solution for that work.

The project work is divided into two phases. In the first phase the group is expected to submit a synopsis upon choosing a project work. The project should be selected within two weeks on the group formation and a brief synopsis of the project should be submitted to the HOD & guide .The synopsis report should include following points:

- Title of project
- Introduction
- Study of existing system
- Need of proposed work / Choice of topic with reasoning
- Literature review / Related work

- Hardware and software requirements
- Outline of proposed work
- Block diagram
- Expected schedule

Student should work on detailed system design and complete 30 to 40 percent of work out of complete project work as a part of term work submission in the form of joint report. The group should work every week in the project duration and appraise the guide about their work progress. Guide should closely monitor the work and help the students from time to time. The guide should also maintain a record of continuous assessment of project work progress on weekly basis.

The term work assignment should be carried out under the guidance of appointed project guide by Head of Dept. During Summer Break after IV semester (i.e. between IV and V Semester), Department would ensure mandatory placement of students for 4 weeks industrial training. Training should be relevant to the branch or discipline of engineering. This training would be evaluated during V semester as part of term work.

A seminar should be delivered by each student in group on the selected project topic as part of oral examination. The oral examination will be conducted by internal examiner as appointed by the Institute.

#### **COMPETENCY:**

Design and develop prototype for an identified engineering application or process.

**Cognitive:** Understands the steps involved in implementation of the electronics system development like analysis, design, coding, debugging and testing.

**Psychomotor:** Operate and Maintain of a prototype for an industrial applications

**Affective:** Attitude of i) Leadership ii) Innovativeness iii) Logic iv) accuracy v) precision v) punctuality

## COURSE OUTCOMES:

- EIF 503-1** Acquire practical knowledge to perform basic tasks in industry.  
**EIF 503-2** Identify/Discover short falls in industrial application or process & develop innovative ideas.  
**EIF 503-3** Work in group and plan, coordinate the work.  
**EIF 503-4** Provide solution by implementing the practical knowledge.  
**EIF 503-5** Write the project seminar work and industrial training report.  
**EIF 503-6** Represent the work done.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Design and develop prototype for an identified engineering application/process	-	1	1	1	1	-	1	1	-	1	1	1
<b>EIF 503-1</b>	-	1	2	2	1	-	2	2	-	1	-	-
<b>EIF 503-2</b>	-	1	-	-	-	-	2	2	-	1	-	2
<b>EIF 503-3</b>	-	1	2	-	-	-	2	-	-	1	2	2
<b>EIF 503-4</b>	-	1	3	3	-	-	-	3	-	1	3	3
<b>EIF 503-5</b>	-	1	-	1	-	-	-	-	-	1	-	-
<b>EIF 503-6</b>	-	1	-	1	-	-	-	-	3	1	-	-

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

- a) Term work :  
 i) Criteria for Continuous Assessment of Practical work and Progressive skill Test:

Domain	Particulars	Marks out of 25
Cognitive	Attendance and Technical preparedness	10
Psychomotor	Hardware/Software Designing	10
	Logical Thinking and Approach	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>50</b>

**b) Term-end Oral Examination :**

A seminar should be delivered by group of students on the selected project topic  
Every student shall be assessed by internal examiner as per the following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Selection of Topic	05
	Presentation Skill	05
Psychomotor	Hardware Designing	05
	Logical Thinking and Approach and Conceptual understanding	05
Affective	Discipline and punctuality	05
	Decency and presentation	
TOTAL		25

**REFERENCE MATERIAL :**

**a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1.	Walter C. Bosshart	Printed Circuit Boards	Tata McGraw Hill
2.	Williams	Build your own printed circuit board with CD	Tata McGraw-Hill

**b) Websites:**

- 1) [www.efy.com](http://www.efy.com)
- 2) [www.electronicshub.org](http://www.electronicshub.org)
- 3) [www.datasheet.com](http://www.datasheet.com)

**c) Magazines:**

- 1) Electronics for you
- 2) Digital Electronics
- 3) Electronics Design
- 4) Electropages

**Performa P-1**  
**PROJECT SHEET**  
(for each project)

**Programme :**  
**Title of Project :**  
**Rationale of Project :**

**Type of project :** (Product making / research / problem solving / industry based / etc.)

**Uniqueness of project :**

**Inter-disciplinary component of project :**

**Process of Identification and Finalization of Topic of Project :**

(Review of previous projects / Brain storming session for project ideas / Internet search for topic / Industry or field problem search, etc.)

**Project Outcomes (PROs)**

- 1.
- 2.
- 3.
- 4.

**PRO-PO Consistency Matrix :**

Project Outcomes (PROs)	Programme Outcomes POs and PSOs										PSO1	PSO2	PSO3	PSO4
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning				
1. ....														
2. ....														
3. ....														

**Details of Students' Group :** Project Batch - .....

Sr. No.	Full name of student (Beginning with surname)	Roll No.	Role in the project	
			General	Particular
1.				Leader
2.				
3.				
4.				
...				

**Detailed Planning of Project Work :**

S N	Activity	Details	Date of completion
1.	Finalization of students' groups and assignment of project guide (Performa P-1)	Policy to be decided by programme department	
2.	Identification and finalization of topic (Performa P-1)	<ul style="list-style-type: none"> <li>• Review of previous projects</li> <li>• Brain storming session for project ideas</li> <li>• Internet search for topic</li> <li>• Industry / field problem search</li> </ul>	
3.	Preparation and presentation of project synopsis including project completion plan (Performa P-2)	<ul style="list-style-type: none"> <li>• Synopsis ** to be submitted by group in printed form in prescribed format</li> <li>• Synopsis to be presented by group in ppt presentation in front of faculty dean and project guide</li> <li>• Assessment as per prescribed rubrics</li> </ul>	
4.	Demonstration-1 (term-1 end) (Performa P-3)	PowerPoint presentation to be assessed as per prescribed rubrics	
5.	Demonstration-2 (mid-term-2 end) (Performa P-4)	PowerPoint presentation to be assessed as per prescribed rubrics	
6.	Presentation of final project report (Performa P-5)	<ul style="list-style-type: none"> <li>• Submission of final project report with conclusion of project</li> <li>• PowerPoint presentation</li> <li>• Assessment as per prescribed rubrics</li> </ul>	
7.	Final examination	As per curriculum specifications	

\*\*Synopsis shall contain the following :

1. Cover page
2. Index
3. Project Sheet
4. Activity schedule for project work

Name and signature of Project Guide

Name and signature of Programme Dean

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**Performa P-2**  
**FINALIZATION OF PROJECT GROUPS, TOPICS AND**  
**GUIDES**

**Prograamme :**  
**Date :**

**Academic Year :**

**Class :**

S N	Project Group ID	Project Group		Title of Project	Name of Project Guide	Type of Project (Product making / research / problem solving / industry based / etc.)
		Roll No.	Names of Students			
1.						
2.						
3.						
4.						
5.						
6.						
7.						
...						

**Name and signature of Programme Dean**

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**Performa P-3**  
**ASSESSMENT RUBRICS FOR SYNOPSIS OF PROJECT**

S N	Assessme nt point	Performance grades and their meaning for each assessment point					Assessment point-wise score (out of 5) of each student in project group						
		Poo r (1)	Fair (2)	Goo d (3)	Very Good (4)	Excell ent (5)	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....
1													
2													
3													
4													
5													
6													
7													
8													
9													
10													
TOTAL SCORE >>													

**Prograamme :**

**Academic Year :**

**Title of Project :**

**Project Group ID :**

**Name of Project Guide :**

**Date :**

**Project Guide**

**Programme Dean**



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### Performa P-4 ASSESSMENT RUBRICS FOR DEMONSTRATION-1 OF PROJECT

S N	Assessme nt point	Performance grades and their meaning for each assessment point					Assessment point-wise score (out of 5) of each student in project group						
		Poo r (1)	Fair (2)	Goo d (3)	Very Good (4)	Excell ent (5)	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....
1													
2													
3													
4													
5													
6													
7													
8													
9													
10													
TOTAL SCORE >>													

**Prograamme :**

**Academic Year :**

**Title of Project :**

**Project Group ID :**

**Name of Project Guide :**

**Date :**

**Project Guide**

**Programme Dean**

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### Performa P-5 ASSESSMENT RUBRICS FOR DEMONSTRATION-2 OF PROJECT

S N	Assessment point	Performance grades and their meaning for each assessment point					Assessment point-wise score (out of 5) of each student in project group						
		Poor (1)	Fair (2)	Good (3)	Very Good (4)	Excellent (5)	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....
1													
2													
3													
4													
5													
6													
7													
8													
9													
10													
TOTAL SCORE >>													

Programme :

Academic Year :

Title of Project :

Project Group ID :

Name of Project Guide :

Date :

Project Guide

Programme Dean

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### Performa P-6 ASSESSMENT RUBRICS FOR FINAL PRESENTATION OF PROJECT

S N	Assessment point	Performance grades and their meaning for each assessment point					Assessment point-wise score (out of 5) of each student in project group						
		Poor (1)	Fair (2)	Good (3)	Very Good (4)	Excellent (5)	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....	Roll No.: ..... ....
1													
2													
3													
4													
5													
6													
7													
8													
9													
10													
TOTAL SCORE >>													

**Programme :**

**Academic Year :**

**Title of Project :**

**Project Group ID :**

**Name of Project Guide :**

**Date :**

**Project Guide**

**Programme Dean**



**COURSE ID :**

**Course Name** : Project - II  
**Course Code** : EIF 504  
**Course Abbreviation** : FPR2

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s)** : PROJECT - I (EIF503 FPR1)

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	00	04
Practical	04	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Term Work	Oral (Internal)	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-VI	As per Proforma IV	
Marks	--	--	NIL	50	75E	125

E – External Examination

**RATIONALE :**

In the field of Electronics Technology various technologies (hardware and Software) needs to be integrated and proper paradigms needs to be implemented to develop any kind of electronic system . Hence it becomes essential to get hands on experience for developing industrial applications. This subject is essential to understand the implementation of the system development process i.e. analyze, design, coding, debugging and testing.

The project work should be undertaken by group of 3 students who will jointly work and implement the project with the approval of guide. The student should decide the area of proposal work as per requirement of Industry / community or environment and work together for hardware / software solution for that work.

The project work is divided into two phases. In the second phase student should work on detailed system design, layout, construct hardware, test and produce satisfactory results by completing project work as a part of term work submission in the form of joint report.

The term work assignment should be carried out under the guidance of appointed project guide by Head of Dept. During Winter Break after V semester (i.e. between V and VI Semester), Department would ensure mandatory placement of students for 2 weeks industrial training. Training should be relevant to the branch or discipline of engineering. This training would be evaluated during VI semester as part of term work.

The group is expected to submit a report of a project work at the end of semester. The report should include following points:

- Title of project
- Introduction
- Study of existing system
- Need of proposed work / Choice of topic with reasoning
- Literature review / Related work
- Requirement Analysis
- Hardware and software requirements
- System design that includes details of modules of system along Block diagram, Circuit diagram, operation of circuit, description of components, algorithm of software, datasheets etc.
- Implementation details with snapshots
- Applications
- Conclusion and Future work
- Bibliography

These points are guidelines to the students. Students shall prepare a report containing these and additional points if any depending on the project as guided by the appointed project guide. The oral examination will be conducted by internal and external examiner as appointed by the Institute.

#### **COMPETENCY:**

Design and develop prototype for an identified engineering application or process.

**Cognitive :** Understands the steps involved in implementation of the electronics system development like analysis, design, coding, debugging and testing..

**Psychomotor :** Design and implement a prototype for an industrial applications

**Affective :** Attitude of i) Leadership ii) Innovativeness iii) Logic iv) accuracy v) precision v) punctuality

## COURSE OUTCOMES:

- EIF 504-1** Acquire practical knowledge to perform basic tasks in industry.  
**EIF 504-2** Identify/Discover short falls in industrial application or process & develop innovative ideas.  
**EIF 504-3** Work in group and plan, coordinate the work.  
**EIF 504-4** Provide solution by implementing the practical knowledge.  
**EIF 504-5** Write the project seminar work and industrial training report.  
**EIF 504-6** Represent the work done.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Design and develop prototype for an identified engineering application/process	-	2	1	1	-	-	1	1	-	1	1	1
<b>EIF 504-1</b>	-	2	2	2	-	-	2	2	-	1	-	-
<b>EIF 504-2</b>	-	2	-	-	-	-	2	2	-	1	-	2
<b>EIF 504-3</b>	-	2	2	-	-	-	2	-	-	1	2	2
<b>EIF 504-4</b>	-	2	3	3	-	-	-	3	-	1	3	3
<b>EIF 504-5</b>	-	2	-	1	-	-	-	-	-	1	-	-
<b>EIF 504-6</b>	-	2	-	1	-	-	-	-	-	1	-	-

## ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

### a) Term work :

#### i) Criteria for Continuous Assessment of Practical work and Progressive skill Test:

Domain	Particulars	Marks out of 25
Cognitive	Attendance and Technical preparedness	10
Psychomotor	Hardware/Software Designing	10
	Logical Thinking and Approach	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>25</b>

**b) Term-end Oral Examination :**

Every student shall be assessed by examiner as per the following criteria

**Criteria for assessment at semester end oral exam:**

Domain	Particulars	Marks out of 75
Cognitive	Conceptual Understanding	10
Psychomotor	PCB designing, Mounting components & soldering	15
	Logical Thinking and Approach	15
Affective	Discipline and Project Report submission punctuality	15
	Decency and Project presentation	20
<b>TOTAL</b>		<b>75</b>

**REFERENCE MATERIAL:**

**a) Books / Journals/ IS Codes**

Sr. No.	Author	Title	Publisher
1.	Walter C. Bosshart	Printed Circuit Boards	Tata McGraw Hill
2.	Williams	Build your own printed circuit board with CD	Tata McGraw-Hill

**b) Websites:**

- 1) [www.efy.com](http://www.efy.com)
- 2) [www.electronicshub.org](http://www.electronicshub.org)
- 3) [www.datasheet.com](http://www.datasheet.com)

**c) Magazines:**

- 1) Electronics for you
- 2) Digital Electronics
- 3) Electronics Design
- 4) Electropages

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**COURSE ID:**

Course Name : Radar & Navigation  
Course Code : ETF 505  
Course Abbreviation : FRDN

**TEACHING AND EVALUATION SCHEME:**

**Pre-requisite Course(s)** : <>

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Theory Examination	Term End Examination		Total
	Theory	Practical		Term Work	Oral Exam	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per proforma II	Oral exam	
Marks	20	--	80	--	25 I	125

I-Internal Assessment

\* Assessment as per Pro-forma II

**RATIONALE:**

Radar is high-end security system on which surveillance of world depends. Radars have been employed on ground, in air, on sea and on space for detecting and locating objects. Radar is not just the tool of professional mariners anymore. Today radars are available in price and size.

**COMPETENCY**

Pursue basic relationship between RADAR ranges to fit all types of vessels and navigation system and discuss the idea of modern navigation.

**Cognitive:** Understanding basics of RADAR and navigation.

**Psychomotor:** Develop basic knowledge of RADAR and navigation.

**Affective:** Attitude of i) Exactness ii) Direction sense iii) precision v) punctuality

### COURSE OUTCOMES:

**ETF 505 -1.**Rememberrng the basic of RADAR and its application, principle and operation.

**ETF 505-2**Develop the outline of CW and frequency modulated RADAR and relate its application.

**ETF 505-3**Illustrate MTI pulse,Doppler's RADAR and analyze its performance.

**ETF 5**

**05 -4**Observe and analyze tracking and imaging RADAR system and develop the understanding to SAR.

**ETF 505-5** Classify navigation method and examine the basic function.

**ETF 505-6**Define and distinguish modern navigation system.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[Note: Correlation levels:1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-”:no correlation]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operation and Maintenance	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Pursue basic relationship between RADAR and navigation system and discuss the idea of modern navigation.	1	2	2	-	-	-	-	1	-	1	-	-
<b>ETF 505 -1</b>	2	1	3	-	-	-	-	1	-	-	1	1
<b>ETF 505 -2</b>	1	2	2	-	-	-	-	1	-	-	1	1
<b>ETF 505 -3</b>	1	3	2	-	-	-	-	1	-	-	1	2
<b>ETF 505 -4</b>	-	3	1	-	-	-	-	1	-	1	1	1
<b>ETF 505 -5</b>	1	2	1	-	-	-	-	1	-	1	1	1
<b>ETF 505 -6</b>	1	2	1	-	-	-	-	1	-	1	1	1

**CONTENT:**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<b>ETF 505 -1.</b> <i>Rememberring the basic of RADAR and its application, principle and operation.</i>			
<b>1</b>	<b>Introduction of Radar</b> 1.1 The Origins of Radar 1.2 Basic Radar – Principle Of Operation 1.3 Radar Block Diagram and operation 1.4 Radar Frequencies 1.5 Types Of Radar 1.6 Applications of Radar 1.7 Radar range equation 1.8 Factors influencing maximum range Simple numerical examples based on range equations	<b>06</b>	<b>12</b>
<b>ETF 505-2</b> <i>Develop the outline of CW and frequency modulated RADAR and relate its application.</i>			
<b>2.</b>	<b>CW and Frequency Modulated Radar :</b>  2.1 Doppler Effect 2.2 CW Radar – Block Diagram and operation 2.3 Isolation between Transmitter and Receiver, 2.4 Non-zero IF Receiver, 2.5 Receiver Bandwidth Requirements, 2.6 Applications of CW radar 2.7 FM-CW Radar -Range and Doppler Measurement, Block Diagram and Characteristics (Approaching/ Receding Targets), 2.8 FM-CW altimeter, 2.9 Measurement Errors, 2.10 Multiple Frequency CW Radar	<b>10</b>	<b>14</b>
<b>ETF 505-3</b> <i>Illustrate MTI pulse, Doppler's RADAR and analyze its performance.</i>			
<b>3</b>	<b>MTI and Pulse Doppler Radar :</b> 3.1 Introduction, Principle, 3.2 MTI Radar with - Power Amplifier Transmitter and Power Oscillator Transmitter, 3.3 Delay Line Cancellers – Filter Characteristics, Blind Speeds, Double Cancellation, Staggered PRFs. 3.4 Range Gated Doppler Filters. 3.5 MTI Radar Parameters, 3.6 Limitations to MTI Performance.	<b>8</b>	<b>14</b>

	3.7 Non-coherent MTI, 3.8 MTI versus Pulse Doppler Radar		
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<b>ETF 505 -4</b> Observe and analyze tracking and imaging RADAR system and develop the understanding to SAR.			
<b>4</b>	<b>Tracking and imaging Radar</b> 4.1 Tracking with Radar 4.2 Sequential Lobing and Conical Scan Monopulse Tracking 4.3 Radar – Amplitude Comparison Monopulse (one- and two- coordinates) 4.4 Phase Comparison Monopulse 4.5 Target Reflection Characteristics and Angular Accuracy 4.6 Tracking in Range 4.7 Acquisition and Scanning Patterns. 4.8 Comparison of Trackers 4.9 Air Surveillance Radar 4.10 Introduction to Synthetic aperture radar(SAR)	<b>06</b>	<b>12</b>
<b>ETF 505-5</b> Classify navigation method and examine the basic function.			
<b>5</b>	<b>Navigation –</b> Four methods of Navigation 5.1 Radio Direction Finding - The Loop Antenna - Loop Input Circuits - An Aural Null Direction Finder 5.2 The Goniometer - Errors in Direction Finding 5.3 Adcock Direction Finders - Direction Finding at Very High Frequencies - Automatic Direction Finders - The Commutated Aerial Direction Finder - Range and Accuracy of Direction Finders 5.4 Radio Ranges - The LF/MF Four course Radio Range - VHF Omni Directional Range(VOR) - VOR Receiving Equipment - Range and Accuracy of VOR 5.5 Recent Developments Hyperbolic Systems of Navigation (Loran and Decca) - Loran-A -	<b>10</b>	<b>16</b>

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
	Loran-A Equipment - Range and precision of Standard Loran – 5.6 Loran-C - The Decca Navigation System - Decca Receivers - Range and Accuracy of Decca - The Omega System		
<b>ETF 505-6</b> Define and distinguish modern navigation system.			
<b>6</b>	<b>Modern Navigation :</b> 6.1 Aids to Approach and Landing:- 6.1.1 Instrument Landing System 6.1.2 Ground Controlled Approach System 6.1.3 Microwave Landing System(MLS) 6.2 Satellite Navigation System :- The Transit System – 6.2.1 Navstar Global Positioning System (GPS) 6.2.2 GPS principle of operation, 6.2.3 Position location determination, 6.2.4 Principle of GPS receiver.	8	12
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1	Introduction of Radar	04	04	04	ETF 505 -1	12
2	CW and Frequency Modulated Radar	06	06	02	ETF 505 -2	14
3	MTI and Pulse Doppler Radar	06	06	02	ETF 505 -3	14
4	Tracking and imaging Radar	04	04	04	ETF 505 -4	12
5	Navigation	06	06	04	ETF 505 -5	16
6	Modern Navigation	04	04	04	ETF 505 -6	12
TOTAL		22	28	30		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK**

**Practical Exercises and related skills to be developed:  
(Minimum Eight From List)**

The following practical exercises shall be conducted as Term Work as detailed in the *Laboratory Manual for Applied Mechanics* developed by the Institute in practical sessions of batches of about 22 students :

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Introduction to Radar Trainer kit	Understanding and analyzing the trainer kit	ETF 505 -1
2.	To become familiar with operating principle of Radar	Understanding operation principle	ETF 505 -1
3.	Study of the working of a Doppler Radar	Understanding Doppler Radar working principle	ETF 505 -1
4.	Study of determine the Velocity of the object moving in the Radar range	Calculating velocity of the object in Radar range	ETF 505 -1
5.	To Measure the Target Range	Measuring the target range	ETF 505 -1
6.	To Observe the Echo Signal from the Target	Measuring distance.	ETF 505 -1
7.	Study of understand the	Understanding the principle of Doppler	ETF 505 -2

	principle of Doppler Radar of Time and Frequency measurement with the help of a moving pendulum	Radar of Time and Frequency measurement	
8.	Analysis of Radar Equation	Analyzing factors affecting radar range equation	<b>ETF 505 -2</b>
9.	Determine the Rotation Per Minute (RPM) of a moving object (Fan)	Determining the Rotation Per Minute	<b>ETF 505 -3</b>
10.	Analysis of Radar Signal to Noise Ratio against target detection range for different values of target Radar cross section.	Calculating of Radar Signal to Noise Ratio against target detection range for different values of target Radar cross section	<b>ETF 505 -3</b>
11.	Analysis of Radar Signal to Noise Ratio (SNR) against target detection range for different values of Radar peak power	Calculating of Radar Signal to Noise Ratio (SNR) against target detection range for different values of Radar peak power	<b>ETF 505 -4</b>
12.	Study of the effect of different types of materials on Radar reception or detection	Analyzing effect of different types of materials on Radar reception or detection	<b>ETF 505 -5</b>
13.	Study of the Object Counting with the help of Radar	Calculating number of objects with the help of Radar	<b>ETF 505 -6</b>

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### dd) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test:

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-farma-II.*

**ee) Criteria for assessment at semester end ORAL exam :**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Presentation	10
2	Conceptual Understanding	10
3	Confidence and Overall Performance	05
	Total	25

Assessment at semester end practical exam as per Pro-forma IV.

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Skolnik, M.,	Introduction to Radar Systems	Tata McGraw-Hill, 3rd Edition, 2001
2.	N.S.Nagaraja	"Elements of Electronic Navigation Systems	Tata McGraw-Hill, 2nd Edition, 2000
3	G N Raju	Radar Engineering and Fundamental Navigational Aids	I.K.International
4	J.C Toomay,	Principles of Radar	2nd Edition –PHI, 2004
5	Peyton Z. Peebles	Radar Principles	John Wiley, 2004

**b) Websites:**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in) 2) [www.navy.mars.org](http://www.navy.mars.org) 3) [www.radartutorial.eu](http://www.radartutorial.eu)

\* \* \*



**COURSE ID :**

**Course Name : AUDIO VIDEO ENGINEERING**  
**Course Code : ETF 506**  
**Course Abbreviation : FAVE**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Oral	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Internal Oral Exam	--	
<b>Marks</b>	20	--	80	25 I	--	125

\* Assessment as per Pro-forma II      I – Internal Examination

**Rationale:**

The state of the art in Audio & Video system will enable the students to comprehend. The fact, concept, working principle and its application in various types of modern television system. The knowledge acquired by students will help them to be familiar with designing concepts and repairing of audio & video system.

The low cost video system VCR, cameras have brought about video revolution in the field of home entertainment, education, training, advertising and electronic newsgathering. Dramatic developments in flat panel display, reduction in the cost of image scanning system, LCD/ LED display and integrated subsystems is going to affect our communication capabilities and life-style in a big way. This revision had to take into account all these wider implications.

**COMPETENCY:**

Able to design audio video systems.

**1.Cognitive :** Understanding the fact,concept,working principle and application in various types of modern television system.

**2.Psychomotor :** Repairing of audio and video system.

**3.Affective :** Attitude of i) Fault finding ii) Accuracy iii) Precision iv) Analyze v) Testing vi) Designing

### COURSE OUTCOMES:

1. **ETF 506 -1** Describe the basic idea and fault finding in different types of audio amplifiers.
2. **ETF 506 -2** Identify/discover the architectural details of JPEG,MPEG and illustrate its functioning.
3. **ETF 506 -3** Acquire knowledge about different TV fundamentals and basic concepts.
4. **ETF 506 -4** Acquire knowledge about TV transmitter and receiver sections.
5. **ETF 506 -5** Analyze color television details and fault finding in color television system.
6. **ETF 506 -6** Illustrate cable TV types, plasma LCD TV system and flat panel display.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Able to design audio video systems	1	2	2	-	-	-	-	1	-	-	2	2
<b>ETF 506 -1</b>	1	2	1	-	-	-	-	1	-	-	2	2
<b>ETF 506 -2</b>	1	2	1	1	-	-	-	1	-	-	1	1
<b>ETF 506 -3</b>	2	-	2	-	-	-	-	1	-	-	2	3
<b>ETF 506 -4</b>	1	2	2	-	-	-	-	1	-	-	2	2
<b>ETF 506 -5</b>	2	2	2	-	-	-	-	1	-	-	3	3
<b>ETF 506 -6</b>	1	2	2	-	-	-	-	1	-	-	2	2

**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics Subtopics	Teaching (Hours)	Theory evaluation Marks
<b><i>ETF 506 -1 Describe the basic idea and fault finding in different types of audio amplifiers</i></b>			
1	<b>Hi Fi Audio Amplifier</b> 1.1 Introduction to Amplifiers: Mono, Stereo, Public Address. 1.2 Difference between stereo amplifier & Mono amplifier. 1.3 Block diagram of Hi Fi amplifier & explanation 1.4 Controls available on it & its function & other facility available on it like (Mic in, Aux.in, earphone in) 1.5 Graphic equalizer concept, circuit diagram and operation. (5 Point Circuit diagram) 1.6 Types of speaker – its comparison only I) woofer, II) Mid-range, III) TweETFr 1.7 Cross over network circuit & its function	08	10
<b><i>ETF 506 -2 Identify/discover the architectural details of JPEG,MPEG and illustrate its functioning</i></b>			
2	<b>Compression-Decompression and File Format</b> 2.1 Need and types 2.2 Introduction to Video compression technique and file format 2.3 Introduction to AVI 2.4 JPEG- Objectives and Architecture 2.5 MPEG- Objectives and Architecture 2.6 MPEG2 and MPEG4	06	10
<b><i>ETF 506 -3 Acquire knowledge about different TV fundamentals and basic concepts</i></b>			
3	<b>TV Fundamentals</b> 3.1 Concept & explanation of following: Aspect ratio, image continuity, interlace scanning, scanning periods – horizontal & vertical, vertical resolution, horizontal resolution. 3.2 Vestigial sideband transmission, bandwidth for Color signal, brightness, contrast, viewing distance luminance, hue, saturation, compatibility. 3.3 Color theory, primary colors & secondary colors, Grassman's law, additive Color mixing subtractive Color mixing. 3.4 Composite Video Signal explain with waveform: Pedestal height, Blanking pulse, Color burst, Horizontal sync pulse details, Vertical sync pulse detail, CCIR B standards for color signal transmission and reception.	10	20
	<b>Total</b>	24	40

### Section II

Sr. No.	Topics Subtopics	Teaching (Hours)	Theory evaluation Marks
<b>ETF 506 -4 Acquire knowledge about TV transmitter and receiver sections</b>			
4	<b>TV Transmitters &amp; Receiver</b> 4.1 Audio and Video signal transmission 4.2 Positive and Negative modulation 4.3 Merits and Demerits of Negative modulation 4.4 Color Picture tube (working & principle only). a) PIL b) Delta gun picture tube. 4.5 Block diagram of Colour TV transmitter.	04	10
<b>ETF 506 -5 Analyze color television details and fault finding in color television system</b>			
5	<b>Colour TV</b> 5.1 Block Diagram & operation of color TV receiver (PAL D type) 5.2 Block diagram of PAL-D decoder. with circuit diagram of chroma signal amplifier, Burst pulse blanking, Colour killer control, Basic Circuit for Separation of U & V signals. ACC Amplifier. color signal matrixing, RGB drive amplifiers. 5.3 EHT generation: circuit explanation for line output stage using transistor or IC in color TV.  5.4 Comparison between NTSC, PAL & SECAM Systems.	10	16
<b>ETF 506 -6 Illustrate cable TV types, plasma LCD TV system and flat panel display</b>			
6	<b>Cable Television</b> 6.1 Working principle & specification of following components: Dish antenna, balloon antenna LNBC, Multiplexer, Attenuators, Connectors (two way & three way), Amplifier & cable. 6.2 MATV, CATV & CCTV. 6.3 Design concept for cable TV network. 6.5 Direct to Home System (DTH) Introduction & Block Diagram 6.6 HDTV Standards & compatibility, paramETFrS of HDTV 6.7 LCD System: LCD Technology overview. 6.8 PLASMA TV: PLASMA & Conduction of charge ,Signal processing in PLASMATV	10	14
	<b>Total</b>	24	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification Table For Question Paper of Theory Examination:**

Section / Topic no.	Name Of the Topic	Distribution Of Marks (Level Wise)			Course Outcomes	Total Marks
		Knowledge	Comprehension	Applications		
I/1	Hi Fi Audio Amplifier	4	4	2	ETF 506 - 1	10
I/2	Compression- Decompression and File Format	4	4	2	ETF 506 - 2	10
I/3	TV Fundamentals	6	8	6	ETF 506 - 3	20
II/1	TV Transmitters & Receiver	6	4	--	ETF 506 - 4	10
II/2	Colour TV	8	4	4	ETF 506 - 5	16
II/3	<i>Cable Television</i>	4	4	6	ETF 506 - 6	14
				TOTAL		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

A) **TERM WORK** Term work shall consist of the following :

**(Minimum Eight From List)**

**Laboratory experiments and related skills to be developed :**

Sr. No.	Title of the Lab Work	Skills / Competencies to be developed	COURSE OUTCOMES
1.	Study and observe the given component layout of a Hi Fi amplifier system. a) Trace the output stage of given Hi Fi amplifier system. b) Voltage analysis of a given Hi Fi amplifier.	1) Understand the Hi Fi Amplifier system 2) Understand voltage analysis of Hi Fi Amplifier	ETF 506 -1

2.	Fault Finding (three different faults) in a Hi Fi Audio amplifier: a) By Signal injection method. b) Confirmation of faulty stage by voltage analysis method.	1) Understand the fault finding methods of Hi Fi Amplifier 2) Understand different faults in Hi Fi Amplifier	ETF 506 -1
3.	To understand simple video compression technique	1) Download and install any video compression software from internet 2) Observe the pre compressed and post compressed size of video	ETF 506 -2
4.	Tracing of chroma section in given TV receiver.	1) Understand chroma section of TV receiver	ETF 506 -5
5.	Tracing of picture tube and video amplifier in given TV receiver with multimETFr.	1) Understand picture tube and video amplifier in TV receiver 2) Tracing of this sections using multimETFr	ETF 506 -4
6.	Tracing of horizontal section in given TV receiver with multimETFr.	1) Understand horizontal section in given TV receiver 2) Understand tracing of it with multimETFr	ETF 506 -3
7.	Voltage analysis of picture tube, chroma section and horizontal section.	1) Understand picture tube, chroma section in given TV receiver 2) Understand voltage analysis of it	ETF 506 -3,4,5.
8.	Fault finding in given Colour TV : a) No color b) Red Colour only c) Blue color only d) Green color only. e) Magenta color only f) Cyan only g) Yellow only h) No raster. i) No Sound.	1) Understand different types of faults in TV receiver 2) Understand finding these faults	ETF 506 -3,5
9.	a) Fault in HSYNC section. b) Fault in VSYNC section.	1) Understand different types of faults in HSYNC and VSYNC sections 2) Understand finding these faults	ETF 506 -3
10.	Fault in SYNC separator.	1) Understand different types of faults in SYNC separator 2) Understand finding these faults	ETF 506 - 5
11.	Installation of DTH System.	1) Understand DTH system 2) Understand installation of DTH system	ETF 506 -6
12.	Estimate the cost, layour of Cable TV	1) Understand Cable TV system 2) Understand Layout and cost of Cable TV system	ETF 506 -6
13.	Collect information about Set Top box used for Cable TV at home	1) Understand set top box 2) Understand its use at home	ETF 506 -6

**Criteria for assessment at semester end oral exam :**

Every student shall be assessed as per following criteria.

**ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION**

Sr. no	Criteria	Marks allotted
1	Presentation	10
2	Conceptual Understanding	10
3	Overall Performance	05
	Total	25

**a) Assessment Criteria for Term work :**

**i) Continuous Assessment of Practical Assignments :**

Every practical assignment shall be assessed for 25 marks as per given criteria .

**ii) Progressive Skill Test :**

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

**Criteria for Continuous Assessment of Practical work and Progressive skill Test :**

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods :**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	A.M Dhake	Television & Radio Engineering	Tata McGraw-Hill
2.	R.G Gupta	Television Engg and Video System	Tata McGraw-Hill
3	R.G Gupta	Audio Video Systems	Tata McGraw-Hill
4	R.R Gulati	Modern TV Practice	New age International
5	S. Sharma	Basic Radio and Television	Tata McGraw-Hill
6	R.R Gulati	Colour Television Principles and Practice, ,	New age International
7	Tay Vaughan	Multimedia	Tata McGraw-Hill
8	Buyord J.K	Multimedia System	Pearson Education

**b) Websites**

a) [www.nptel.ac.in](http://www.nptel.ac.in)

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**Course ID :**  
**Course Name :** Energy Conservation  
**Course Code :** EIF 507  
**Course Abbreviation :** FECN  
**Pre-requisites :** Nil

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	03	05
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination			Total
	Theory	Practical	Theory Examination	Term Work	Practical Examination (External)	
Details of Evaluation	Average of two tests of 20 marks each	i.25 marks for continuous assessment ii.One PST of 25 marks	Term End Theory Exam (03 hours)	-	As per Performa II	
Marks	20		80	-	25 I	125

**Rationale:**

The pressure on the natural resources of energy such as water, coal, gas, wood and other fuels has lead to the increasing costs of energy around the world. Efficient and judicious use of the available energy would lead to the easing of such pressures and drastic decrease in the operating costs of the organizations and industries. Thus it is necessary to save and conserve energy to the maximum possible extent. The process of energy audit will help to identify the various possible avenues in which savings of energy can be effectively affected. This course makes the diploma holder well conversant in the techniques of energy conservation in the fields of engineering. It also introduces him to the energy audit procedures.

**Course Competency:** Use environment friendly energy conservation techniques and assist in energy audits.

## Course Outcomes

EIF 507-1 Identify present environmental effects and suggest use of Non Conventional Energy sources.

EIF 507-2 Use Co-generation Systems

EIF 507-3 Implement energy conservation techniques in electrical machines

EIF 507-4 Implement energy conservation techniques in electrical lighting systems

EIF 507-5 Implement energy conservation techniques in electrical power distribution system

EIF 507-6 Assist in energy audit works

## Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/ps) matrix

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), "0" ]

Competency and COs	Programme Outcomes (POs)											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO1 Electrical Equipment	PSO2 Electrical power systems
Competency: Use environment friendly energy conservation techniques and assist in energy audits.	1	1	1	-	-	1	-	1	-	-	-	-
EIF 507-1 : Identify present environmental effects and suggest use of Non Conventional Energy sources	1	-	1	-	1	1	-	1	-	-	-	-
EIF 507-2 : Use cogeneration system	1	1	1	-	-	2	-	1	-	-	-	-
EIF 507-3 : Implement energy conservation techniques in electrical machines	-	2	1	-	-	1	-	1	-	-	-	-
EIF 507-4 : Implement energy conservation techniques in lighting systems	-	2	1	-	-	1	-	1	-	-	-	-
EIF 507-5 : Implement energy conservation techniques in electrical power distribution systems	1	2	1	-	-	1	-	1	-	-	-	-
EIF 507-6 : Assists in energy audit works	2	1	1	-	-	1	-	1	-	-	-	-

CONTENTS:  
THEORY

**Section I**

Sr. no.	Topics / sub-topics	Teaching (Hours)	Theory evaluation Marks
<b><i>EIF 507-1 Identify present environmental effects and suggest use of Non Conventional Energy sources.</i></b>			
1	<b>Present Energy Scenario and environment</b> Review of various energy sources Classification of Energy: Primary and secondary energy, Commercial and non-commercial energy, Renewable and Non-renewable energy. Energy production and needs of growing economy like India, energy pricing in India, energy sector reforms, Salient features of Electricity Act 2003. Relation between Energy and Environment Issues of global environment Causes of Ozone layer depletion and its effects and measures to counter it. Global warming implications with special emphasis on green house effect, steps to reduce it.	08	12
<b><i>EIF 507-2 Use Co-generation Systems</i></b>			
2	<b>2.1 Thermal Energy and Co- Generation:</b> Thermal energy of fuels, latent & sensible heat Evaporation, condensation, humidity, heat transfer. Need for Cogeneration Classification of cogeneration systems on the basis of the Sequence of energy use. Classification of cogeneration systems on the basis of the technologies. Steam turbine cogeneration, Advantages of cogeneration <b>2.2 Tariff::</b> Energy cost and recent MSEDCL tariff Applications of tariff system to reduce Energy bill Energy conservation by improving load factor and power factor Simple numerical	08	16
<b><i>EIF 507-3 Implement energy conservation techniques in electrical machines</i></b>			
3	Energy conservation in Electrical Machines and their controls Energy efficient transformers: amorphous core transformers, epoxy resin cast transformers, specialty of cores and windings of such transformers. Energy conservation techniques in electric motors : Power flow diagram of induction motor, improving power quality, matching motor and load, operating in star mode, rewinding, minimizing idle and redundant running of motors, soft starters, VFD and improving mechanical power transmission efficiency . Energy efficient motors (EEM): features	08	12
<b>Total</b>		24	40

**Section II**

Sr. no.	Topics Subtopics	Teaching (Hours)	Theory evaluation Marks
<b>EIF 507-4 Implement energy conservation techniques in electrical lighting systems</b>			
4	<b>Energy conservation in lighting system:</b> Assessing existing lighting system, replacement of light sources, light control gears, motion detectors, separate transformer/ servo stabilizer for lighting ,regular survey and adequate maintenance programs, use of solid state devices such as microprocessors etc.	08	12
<b>EIF 507-5 Implement energy conservation techniques in electrical power distribution system</b>			
5	<b>Energy conservation in distribution systems</b> Distribution losses at global, national and state level, commercial and technical losses. <b>Energy conservation techniques</b> Reducing line losses, compensating reactive power flow, optimizing distribution voltage , balancing phase currents and using energy efficient transformers . Reducing commercial losses.	08	14
<b>EIF 507-6 Assist in energy audit works</b>			
6	<b>Energy audit</b> Energy flow diagrams with significance, audit instruments, questionnaire and ABC analysis. Walkthrough audit and detailed audit Calculations of simple payback period	08	14
Total		<b>24</b>	<b>40</b>

**Specification table for setting question paper for semester end theory examination**

Section / Topic no.	Name of topic	Distribution of marks (level wise)			CO	Total marks
		Remember	Understand	Application		
I/1	Present Energy Scenario and environment	2	4	6	EIF 507 -1	12
I/2	Thermal Energy and Co-Generation	4	4	8	EIF 507 -2	16
I/3	Energy conservation in Electrical Machines and their controls	2	4	6	EIF 507 -3	12
II/4	Energy conservation in lighting system	2	4	6	EIF 507 -4	12
II/5	Energy conservation in distribution systems	2	4	8	EIF 507 -5	14
II/6	Energy audit	2	4	8	EIF 507-6	14

### TERMWORK

**Practicals / experiences and related skills developed.**

Sr. no	Practical works	Skills/Information developed	
1	Energy conservation building code 2007	Conservation in buildings	EIF 507 -3, EIF 507 -4
2	Conduct Energy survey	1) Identify the various types of energy consumptions 2) Classify them and mark out their percentages in an industry.	EIF 507 -1 to EIF 507 -6
3	Data collection of consumers	Data collection of various consumers e.g. industries, commercials , agriculture and data analysis	EIF 507 -1 to EIF 507 -6
4	Compare different types of lamps (input power and output light)	Conduct practical to measure their input power and output light of various lamps	EIF 507 -4
5	Co generation	1) Visit a plant of cogeneration e.g. sugar industries , spinning mills , Heat processing unit 2) Prepare report on the cogeneration containing energy flow diagrams , economics and tariffs structure	EIF 507 -2
6	Environment issue	1) Collect data of pollution e.g. air, sound, water etc 2) Prepare report on it	EIF 507 -1
7	Case study.	Case study of any type small industry which has implemented energy conservation and auditing techniques.	EIF 507 -1 to EIF 507 -6

Criteria for Continuous Assessment of Practical work and Progressive Skill Test:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	5
2	Correct figures / diagrams	10
3	Presentation	10
	Total	25

### Instructional strategies:

- 1) Lectures and discussions.
- 2) Industrial visit / survey, experiences and interactive sessions.
- 3) Time bound assignments.
- 4) Case studies.

### Books

- b) Hand book by Bureau of Energy efficiency.
- c) India: The Energy Sector, P. H. Henderson, Oxford University press.
- d) Industrial Energy Conservation, D. A. Ray, Pergaman Press.
- e) Energy Management Handbook, W. C. Turner, Wiley Press.



**COURSE ID :**

**Course Name** : INDUSTRIAL ORGANIZATION AND MANAGEMENT  
**Course Code** : CCF501  
**Course Abbreviation** : FIOM

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s)** : <nil>

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	03
Practical	--	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Term Work	
Details of Evaluation	Average of two tests of 20 marks each	1. 25 marks for each practical 2. One PST of 25 marks	Term End Online Theory Exam	As per Proforma-III	
Marks	20	--	80	-	100

**RATIONALE :**

Management ability is a higher-grade ability, which every successful engineer must possess. This science has been developed in those days when it was treated as an art in earlier stages. It is impossible for an individual though technically sound to achieve goals of the organizations. Effective implementation of management policies is a tough task. The Diploma holder should learn these principles of management and various techniques.

**COMPETENCY:** Plan and implement managerial and administrative strategies.

**Cognitive :**Use management principles and techniques.

**Psychomotor :**i) Apply management principles ii) Control inventory iii) Use personal protective devices for safety

**Affective :**Attitude of i) precision ii) accuracy iii) safety iv) punctuality v) aesthetic presentation

**COURSE OUTCOMES :**

**CCF 502.1** Apply principles of management and carry out various functions of management.

**CCF 502.2** Prepare organization structure for small and medium scale industry.

**CCF 502.3** Perform duties of stores in-charge, material and finance manager.

**CCF 502.4** Practice industrial safety rules, codes, practices and acts.

**CCF 502.5** Apply various modern management techniques.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and COs	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Maintain Electrical equipment	PSO 2 Maintain Electrical power systems
Competency: Plan and implement managerial and administrative strategies	-	-	-	-	1	-	1	1	1	-	2	2
CCF501.1 Apply principles of management and carry out various functions of management.	-	-	-	-	1	-	1	1	1	-	1	1
CCF502.2 Prepare organization structure for small and medium scale industry.	-	-	-	-	1	1	-	3	1	-	0	0
CCF502.3 Perform duties of stores in-charge, material and finance manager.	-	-	-	-	-	-	1	1	2	-	2	2
CCF502.4 Practice industrial safety rules, codes, practices and acts.	-	-	-	-	1	-	1	1	2	-	3	3
CCF502.5 Apply various modern management techniques	-	-	-	-	1	-	1	3	2	-	2	2



**CONTENT :**

**C) THEORY :**

**SECTION -I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>CCF 502.1 Apply principles of management and carry out various functions of management.</i>			
<b>1</b>	<b>PRINCIPLES OF MANAGEMENT</b> 1.1 Concept of management 1.2 Principles of management 1.3 Objectives of management 1.4 Scope and importance of management 1.5 Levels of management 1.6 Managerial competencies : Communication, Planning and Administration, Team work, Strategic action and General awareness	<b>06</b>	<b>10</b>
<b>2</b>	<b>FUNCTIONS OF MANAGEMENT</b> 2.1 Planning: Forms of planning, Strategic levels and Planning, Phases of Planning 2.2 Decision Making: Decision making conditions, Basic types of Decisions 2.3 Organizing: Introduction to Organization design, basic types of Departmentalization, Co-ordination, Authority 2.4 Motivation: Work Motivation, Three approaches to Motivation, 2.5 Leadership: Leadership and Power, Leadership Development 2.6 Communication: The Communication process, Impact of Information Technology, Hurdles to effective communication 2.7 Controlling: Foundations of control, creative Effective control, Primary methods of control	<b>08</b>	<b>12</b>
<b>3</b>	<b>HUMAN RESOURCE MANAGEMENT(Personnel Management)</b> 3.1 Definition and concept, 3.2 Aim, Objectives and functions of HR dept. 3.2 Principles of personnel policy, details recorded in policy 3.3 Recruitment and selection of employees 3.4 Training : Objectives, benefits, types and methods 3.5 Workers Participation in Management	<b>06</b>	<b>10</b>
<i>CCF 502.2 Prepare organization structure for small and medium scale industry.</i>			
<b>4</b>	<b>FORMS OF BUSINESS ORGANISATION</b> 4.1 Types of industrial sectors 4.2 Forms of business organization 4.3 Individual Proprietorship 4.4 Partnership 4.5 Joint stock companies 4.6 Co-operatives 4.7 Public sectors 4.8 Government undertakings.	<b>04</b>	<b>08</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**SECTION II**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i>CCF 502.3 Perform duties of stores in-charge, material and finance manager.</i>			
<b>5</b>	<b>MATERIALS MANAGEMENT</b> 5.1 Importance of purchase 5.2 Functions and Objectives 5.3 Duties of purchasing officer 5.4 Methods of purchasing and procedure 5.5 Scope and importance of material management 5.6 Objectives of material management 5.7 Duties of Material manager 5.8 Concept of supply chain management 5.9 Modern trends in material management : MRP,ERP	<b>06</b>	<b>10</b>
<b>6</b>	<b>FINANCIAL MANAGEMENT</b> 6.1 Concept, Scope and Importance 6.2 Functions of financial management 6.3 Types of capital: Fixed, working 6.4 Factors affecting Working capital 6.5 Capitalization : over, under 6.6 Sources of Finance 6.7 Industrial taxation	<b>04</b>	<b>08</b>
<i>CCF 502.4 Practice industrial safety rules, codes, practices and acts.</i>			
<b>7</b>	<b>INDUSTRIAL ACT &amp; SAFETY</b> 7.1 Factory Act, Boiler Act, Workmen Compensation Act, ESI Act, pollution Control Act 7.2 Accidents: Economic aspects, direct and indirect cost of accidents Causes, Types, Remedies, Personal Protective Equipments (PPE), Reporting & Investigation of accidents 7.3 Safety management: safety in industry, committees, programs, Safety codes, Safety training, 7.4 Occupational Safety and Health Administration – Promoting, norms and standards 7.5 Housekeeping: definition, concept, necessity, advantages, procedure	<b>08</b>	<b>12</b>
<i>CCF 502.5 Apply various modern management techniques.</i>			
<b>8</b>	<b>MODERN MANAGEMENT TECHNIQUES</b> 8.1 PERT & CPM 8.2 Various terms related with network analysis 8.3 Various Time estimates 8.4 Construction of Network Diagram 8.5 Computation of Critical Path	<b>06</b>	<b>10</b>
	<b>Total</b>	<b>24</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Apply		
1	Principles Of Management	02	04	04	CCF501.1	10
2	Functions Of Management	02	04	06	CCF501.1	12
3	Human Resource management	04	04	02	CCF501.1	10
4	Forms Of Business organization	02	04	02	CCF501.2	08
5	Materials Management	04	02	04	CCF501.3	10
6	Financial Management	02	02	04	CCF501.3	08
7	Industrial Act & Safety	04	04	04	CCF501.4	12
8	Modern Management Techniques	02	02	06	CCF501.5	10
TOTAL		22	26	32		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods :**

1. Lectures cum Demonstrations
2. Classroom practices

**Teaching and Learning resources :**

1. Chalk board
2. LCD presentations
3. Audio presentations
4. Item Bank

**REFERENCE MATERIAL :**

**a) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1	Bangaand Sharma	Industrial Organisation& Management	Khanna Publisher
2	O P Khanna	Industrial Engg. & Management	DhanpatRai& sons New Delhi
3	P.C. Pandey & C. K. Sing	Management Science	DhanpatRai& sons New Delhi
4	Industrial Organisation	P.T. Ghan	Tata McGraw Hill
5	Management Information System	Waman S. Jawadekar	Tata McGraw Hill
6	P.C. Pandey&C.K.Sing	Management Science	DhanpatRai& sons New Delhi

**b) Websites**

- i) [nptel/iitm.ac.in](http://nptel/iitm.ac.in)
- ii) <http://iite.ac.in/subjects/amindustry/Mgmt.htm>

\* \* \*



**COURSE ID :****Course Name : MARKETING MANAGEMENT****Course Code : EIF509****Course Abbreviation : FMRM****TEACHING AND EVALUATION SCHEME :****Pre-requisite Course(s) : Nil****Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	03
Practical	NIL	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Practical (3 hours)	--	
<b>Marks</b>	20	--	80	--	--	100

Cognitive :-To-understand-about-marketing-,market-,functions-,marketing of industrial products, planning,& advertising

Psychomotor :-to understand marketing management ,to do max. sale of products & to earn max. profit.

Affective :Attitude of i) Logic ii) accuracy iii) precision v)punctuality vi)hard working

**COURSE OUTCOMES:**

EIF 509-1- to understand marketing ,product selling.

EIF 509-2- to study market, its types, government policy.

EIF 509-3- to understand marketing functions & marketing managers duties.

EIF 509-4- to understand how to do marketing of industrial products.

EIF 509-5- to understand marketing planning, pricing, buying behavior of customer.

EIF 509-6- to understand how to do advertising of product to do capture market.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :**

[ **Note : Correlation levels** :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency: to do best marketing management</b>	-	-	-	-	1	-	1	1	1	-	3	3
<b>EIF 509-1</b>	-	-	-	-	1	-	1	1	1	-	2	1
<b>EIF 509-2</b>	-	-	-	-	1	-	1	1	1	-	2	1
<b>EIF 509-3</b>	-	-	-	-	1	-	1	1	1	-	3	2
<b>EIF 509-4</b>	-	-	-	-	1	-	1	1	1	-	3	3
<b>EIF 509-5</b>	-	-	-	-	1	-	1	1	1	-	3	3
<b>EIF 509-6</b>	-	-	-	-	1	-	1	1	1	-	3	3

**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
EIF 509-1- to understand marketing ,product selling.			
01	<b>Marketing</b>  Meaning and significance of marketing, marketing system 1.1 Concept of marketing, product selling. 1.3 Trends in modern Marketing. 1.2 Difference between sales and marketing.	08	14
EIF 509-2- to study market, its types, government policy.			
02.	<b>Markets</b>  2.1 Meaning of market. 2.2 Types of markets. 2.3 Government and Industrial market.	06	12
EIF 509-3- to understand marketing functions & marketing managers duties.			
03.	<b>Marketing Functions And Management</b> 3.1 Market functions, meaning of marketing management 3.2 Functioning & Types of marketing organizations. 3.3 Marketing Manager and his duties.	10	14
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
EIF 509-4- to understand how to do marketing of industrial products.			
04	<b>Marketing Of Industrial Products</b> 4.1 Types of Industrial products 4.2 characteristics of marketing	10	15
EIF -509-5- to understand marketing planning, pricing, buying behavior of customer.			
05	<b>Important Concepts</b>  5.1 Considerations. Marketing planning, products decision, pricing decision. 5.2 Marketing strategy Marketing mix market survey, marketing 5.3 Information systems, buying behaviors.	08	15
EIF 509-6- to understand how to do advertising of product to do capture market.			
06	<b>Role Of Advertising</b> 6.1 Role of advertising in Marketing	06	10
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			



Specification table for question paper of theory examination:

Topic No.	Topic Name	Distribution of Marks (level wise)			Course outcome	Total marks
		Knowledge	Comprehension	Application		
1	Marketing	10	04	0	EIF 509-1	14
2	Markets	08	04	0	EIF 509-2	12
3	Marketing Function & Management	10	04	0	EIF 509-3	14
4	Marketing Of Industrial Products	10	05	0	EIF 509-4	15
5	Important Concepts	10	05	0	EIF 509-5	15
6	Role Of Advertising	6	4	0	EIF 509-6	10

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

#### **INSTRUCTIONAL STRATEGIES :**

##### **Instructional Methods:**

1. Lectures cum Discussions
2. Regular Home Assignments.
3. Laboratory work

##### **Teaching and Learning resources:**

1. Chalk board
2. Video clips
3. Slide
4. Question Bank
5. Charts

#### **REFERENCE MATERIAL :**

##### **a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Condif and Still	Basic Marketing	Prentice-Hall
2.	R.S.Davar	Marketing Management	
3.	Satynarayana.	Salesmanship, Sales management and Advertising	
4.	R.S.Davar	Modern Marketing Management	
5.	J.C.Sinha.	Marketing and Salesmanship	R. Chand
6.	Dholkia, Khurana	Marketing Management Cases and Concepts	

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**COURSE ID :**

**Course Name : Entrepreneurship Development**  
**Course Code : EIF 510**  
**Course Abbreviation : FETD**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : Nil**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	03	03
Practical	NIL	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End			Total
	Theory	Practical	Theory	Practical *	TW	
<b>Detailsof Evaluation</b>	Average of two tests of 20 marks each each)	i. 25 marks for each practical ii. One PST of 25 marks	One paper (3 hour)	Practical (3 hours)	--	
<b>Marks</b>	<b>20</b>	<b>--</b>	<b>80</b>	<b>NIL</b>	<b>--</b>	<b>100</b>

**RATIONALE :**

Globalization, liberalization & privatization along with revolution in Information Technology, have thrown up new opportunities that are transforming lives of the masses. Talented and enterprising personalities are exploring such opportunities & translating opportunities into business ventures such as- BPO, Contract Manufacturing, Trading, Service sectors etc. The student community also needs to explore the emerging opportunities. It is therefore necessary to inculcate the entrepreneurial values during their educational tenure. This will help the younger generation in changing their attitude and take the challenging growth oriented tasks instead of waiting for whitecollar jobs. The educational institutions should also demonstrate their uniqueness in the creation of enterprising personalities in their colleges. This subject will help in developing the awareness and interest in entrepreneurship and create employment for others

**Cognitive: - To develop awareness & interest to become entrepreneur, to do entrepreneurship, in younger students.**

**Psychomotor:-To start & run own enterprise through study of entrepreneurship successfully to creat employment for others.**

**Affective: Attitude of i) taking risk of business ii) accuracy iii) precision v)punctuality vi)hard working**

### COURSE OUTCOMES:

EIF 510--1- Study about entrepreneur, rural, women entrepreneurship & its growth,  
EIF 510 -2- Motivate students for entrepreneurship, to understand small & large enterprises, & advantages of entrepreneur  
EIF 510 -3- Understand about project selection, project preparation, its appraisal & growth of business  
EIF 510 -4- Understand financing of enterprise, capitals, loan, & source of finance.  
EIF 510 -5- Study about institutional support, marketing of product, human resource.  
EIF 510 -6- Study about problems of small industries as power, finance, raw material, marketing, regulation & motivation training

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work :	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency: to become successful entrepreneur.</b>	-	-	-	-	1	-	-	2	-	-	3	3
<b>EIF 510-1</b>	-	-	-	-	1	-	1	3	1	-	2	1
<b>EIF 510-2</b>	-	-	-	-	1	-	1	3	1	-	2	1
<b>EIF 510-3</b>	-	-	-	-	1	1	-	3	-	-	3	2
<b>EIF 510-4</b>	-	-	-	-	1	-	-	-	-	-	3	3
<b>EIF 510-5</b>	-	-	-	-	1	-	-	-	-	-	3	3
<b>EIF 510-6</b>	-	-	-	-	1	1	1	2	1	-	3	3

**CONTENT :**

**A) THEORY :**

**Section I**

Sr. No.	TOPIC/ SUB TOPIC	No. (Hours)	Evaluation Marks
<b><i>EIF 510--1- Study about entrepreneur, rural, women entrepreneurship &amp; its growth,</i></b>			
<b>1.</b>	<b>Introduction: ENTREPRENEUR:</b> 1.1 Evolution, Characteristics, Types, Functions of Entrepreneur 1.2 Distinction between an Entrepreneur and a Manager, Concept, Growth of Entrepreneurship in India, Role of Entrepreneurship in Economic Development. 1.3 Rural Entrepreneurship: Concept, Need, Problems, Rural Industrialization in Retrospect, How to Develop Rural Entrepreneurship, NGOs and Rural Entrepreneurship 1.4 Women Entrepreneurship - Concept, functions, Growth of Women Entrepreneurs, Problems, Development of Trends,	10	14
<b><i>EIF 510 -2-Motivate students for entrepreneurship,to under stand small &amp;large enterprises, &amp; advantages of entrepreneur</i></b>			
<b>2.</b>	<b>MOTIVATION</b> 2.1 Entrepreneurial Motivation Concept, Theories, factors, Entrepreneurial Competencies Concept, Major Entrepreneurial Competencies 2.2 Small Enterprises: Definition, Characteristics, Relationship between Small and Large Units, Rationale, 2.3 Objectives, Scope, Opportunities for an Entrepreneurial Career, Role of small Enterprise in Economic development, Problems of SSIs	06	12
<b><i>EIF 510 -3- Under stand about project selection, project preparation, its appraisal &amp;growth of business</i></b>			
<b>3.</b>	<b>Project Identification And Selection (PIS)</b> 3.1 Meaning of Project, Project Identification, Project Selection, 3.2 Project Formulation: Meaning, Significance, Contents, Formulation, Planning Commission's Guidelines for Formulating a Project Report, Specimen of a Project Report, 3.3 Network Analysis, Common Errors in Project Formulation. 3.4 Project Appraisal Concept, Methods of project appraisal, Growth of Business Ideas, Intellectual Property.	08	14

## Section-II

<b><i>EIF 510 -4- Understand financing of enterprise, capitals, loan, &amp; source of finance.</i></b>		
<b>Financing Of Enterprises</b> 4.1 Need for Financial Planning, Sources of finance, Capital Structure, Term-loan, Sources of Short-Term Finance, Capitalization, Venture capital, Export Finance, Institutional 4.2 Finance To Entrepreneurs, Preparation of Business Plans, Commercial Banks, Other financial institutions like IDBI, IFCI, ICICI, IRBI, LIC, UTI, SFCs, SIDCs, SIDBI, EXI Bank	<b>10</b>	<b>14</b>
<b><i>EIF 510 -5- Study about institutional support, marketing of product, human recourse</i></b>		
<b>Institution Support To Entrepreneurs :</b> 5.1 Need for Institutional support - Small Entrepreneurs: NSIC, SIDO, SSIB, SSICS, 5.2 SISI, DICs, Industrial Estates Specialized Institutions, TCOs 5.3 Brief introduction about Marketing of products and services. 5.4 Human resource issues, Total quality management issues for small enterprises, Growth strategies in small businesses. 5.5 Sickness in small businesses, small enterprises in international business	<b>10</b>	<b>16</b>
<b><i>EIF 510 -6- Study about problems of small industries as power, finance, raw material, ,marketing, regulation &amp; motivation training</i></b>		
<b>Problems of Small Industries</b> 6.1 Power shortages, Project planning, Finance, Raw materials, Production constraints, Marketing, ,Personal constraints, Regulation 6.2 Entrepreneurial Motivation Training, Motivating factors of Entrepreneurs, Achievement Motivation, Institutions assisting entrepreneurs	<b>04</b>	<b>10</b>
	<b>48</b>	<b>100</b>

**Specification table for setting question paper for semester end theory examination :**

Section / Topic no.	Name of topic	Distribution of marks			Total marks
		Knowledge	Comprehension	Application	
I/1.	Introduction: ENTREPRENEUR:	06	04	04	14
I/2	MOTIVATION	04	06	02	12
I/3	Project Identification And Selection (PIS)	06	04	04	14
II/1	Financing Of Enterprises	04	06	04	14
II/2	Institution Support To Entrepreneurs	04	04	08	16
II/3	Problems of Small Industries	02	04	04	10
	Total	26	28	26	<b>80</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**REFERENCE MATERIAL :**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	Dr. O.P. Khanna	Industrial Engg & Management	Dhanpal Rai & sons New Delhi
2.	Dr. S.C. Saksena	Business Administration & Management	Sahitya Bhavan Agra
3	W.H. Newman E.Kirby Warren Andrew R. McGill	The process of Management	Prentice- Hall of India Pvt. Ltd. New Delhi - 110001

**b) web-site :**

[www.ediindia.org](http://www.ediindia.org)

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**COURSE ID :**

**Course Name : PLC & Drives**  
**Course Code : ETF 511**  
**Course Abbreviation : FPLD**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : <>**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Practical	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	One practical (3 hours)	
Marks	20	--	80	50E	150

E-External Assessment

\* Assessment as per Pro-forma I

**RATIONALE :**

Today electric motors are the main source of driving equipment. Electric motors are available in very wide range of power rating from few watts to many hundreds of Mega watts.

The subject is classified under applied technology group. It teaches the students Programmable Logic Controller (PLC) system used in automation industries for application such as pick and place, welding, spray painting, cutting, drilling, transportation of the objects etc. This subject will explore what a PLC is , operation, usage, Instructions, hardware selection and configuration , applications, introductory programming examples and exercises and some troubleshooting hints of PLC system.

**COMPETENCY:**

Design industrial automation circuitry based on PLC for different engineering applications and develop methods of controlling AC and DC drives.

**Cognitive :** Understanding the principle of controlling electric drives and basics of PLC.

**Psychomotor :** Write a PLC program for wide range of industrial applications.

**Affective :** Attitude of i) Logic ii) accuracy iii) precision v) punctuality

### COURSE OUTCOMES :

**ETF511-1.** Acquire the concepts of basic elements of electric drives.

**ETF511-2.** Analyze and examine various methods of speed control of AC drives.

**ETF511-3** Analyze various methods of speed control of DC drives and examine various industrial applications of drives.

**ETF511-4** Acquire the knowledge of PLC architecture and I/O modules of it.

**ETF511-5** Analyze different types of instructions set used for PLC.

**ETF511-6** Develop and verify ladder diagrams for various industrial and engineering applications.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ **Note : Correlation levels :** 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency :</b> Design industrial automation circuitry based on PLC for different engineering applications and develop methods of controlling AC and DC drives.	1	3	2	3	-	-	-	2	-	-	3	3
<b>ETF511-1</b>	2	1	1	-	-	-	-	-	-	-	1	1
<b>ETF511-2</b>	1	2	2	-	-	-	-	-	-	-	1	1
<b>ETF511-3</b>	1	3	-	-	-	-	-	-	-	-	1	1
<b>ETF511-4</b>	2	1	-	1	-	-	-	1	-	-	3	3
<b>ETF511-5</b>	2	1	2	2	-	-	-	1	-	-	3	3
<b>ETF511-6</b>	-	3	2	3	-	-	-	2	-	1	3	3

**CONTENT :**

**C) THEORY :**

**Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
<i>ETF511-1. Acquire the concepts of basic elements of electric drives</i>			
1	<b>Introduction of drives</b> (Only Review of construction and working principle of DC motor and AC motor and its classification—No theory questions expected) 1.1 Basic elements of drive 1.2 Adjustable speed drive & its requirement 1.3 DC drive & AC drive & comparison 1.4 Stability of drive 1.5 Factors considered for drive selection 1.6 Load torque-speed characteristics :DC shunt motor, DC series motor, DC compound motor	04	08
<i>ETF511-2. Analyze and examine various methods of speed control of AC drives.</i>			
2.	<b>AC Drives</b> 2.1 Advantages & disadvantages of Induction Motor over DC motor 2.2 Equivalent Circuit Of Induction motor 2.3 Speed – torque characteristics of IM. 2.4 Different methods of speed control of Induction motors 2.41 Stator voltage control 2.42 Frequency control 2.43 Stator voltage & frequency control 2.44 Stator current control 2.45 Voltage, current & frequency control 2.46 Frequency control Inverter using Power MOSFET(single phase RL load) 2.47 Slip energy recovery system-Basic Kramer and Scherbius system 2.48 Chopper controlled resistance in rotor circuit	14	16
<i>ETF511-3 Analyze various methods of speed control of DC drives and examine various industrial applications of drives</i>			

<b>3</b>	<b>DC Drives and drives for specific application</b> 3.1 Advantages and function of Microprocessor based drives 3.2 Application areas of microprocessor based control 3.3 Microcontroller based stepper motor control 3.4 Microprocessor based DC motor control 3.5 Block diagram, Sequence of stages & drives Required at each stage for following applications. Textile Mills Steel Rolling Mills Cement Mills Paper Mills Sugar Mills	14	16
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

### Section II

Sr. No.	Topics / Sub-topics	Lectures (Hours)	Theory Evaluation (Marks)
<i><b>ETF511-4</b> Acquire the knowledge of PLC architecture and I/O modules of it.</i>			
<b>4</b>	<b>PLC fundamentals</b> 4.1 Evolution of PLC in automation, 4.2 Advantages and disadvantages of PLC 4.3 PLC Classification based on Type and Size: 4.4 Fixed PLC and Modular PLC (nano/pico , mini, micro, medium, large) 4.5 Difference Between relay control and PLC Control. 4.6 Block diagram and description of different parts: CPU –function, scanning cycle, speed of Execution. Power supply- function, Block diagram. Memory – function & organization of ROM & RAM Input modules- function, diff. input devices used with PLC(only name & their uses) Output modules- function, diff. output devices used with PLC(only name & their uses) 4.7 Specialty I/O modules: communication module, high speed encoder, RTD input module, stepper Motor Control module, thermocouple module. Redundancy in PLC modules	12	14
<i><b>ETF511-5</b> Analyze different types of instructions set used for PLC.</i>			
<b>5</b>	<b>PLC Instruction Set</b>	12	12

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Theory Evaluation (Marks)
	5.1 I/O addressing of PLC. Relay type instructions - NO, NC, One shot, Latch, And Unlatch. 5.2 Timer instructions - On delay timer, off delay Timer, Retentive timer, and Timer reset. 5.3 Counter instructions - up counter, down counter, High speed counter, counter reset. 5.4 Comparison instructions – Equal, Not equal, Greater, Greater than equal, Less, Less than equal. 5.5 Arithmetic Instruction: ADD,SUB,MUL,DIV,NEG 5.6 Data handling instructions – Move, Masked Move and Limit test. 5.7 Logical instructions – AND, OR, EX-OR, NOT. 5.8 Miscellaneous instructions – Sequencer instructions, scale with parameter, subroutine and PID instructions.		
<i>ETF511-6 Develop and verify ladder diagrams for various industrial and engineering applications.</i>			
<b>6</b>	<b>PLC Programming and Applications</b> 6.1 Different PLC programming languages (only introduction) - FBD, Instruction list, structured text, sequential Function chart, and ladder logic. 6.2 Simple programming examples using ladder programming language based on relay, timer, counter, logical, comparison, Data handling and Miscellaneous instruction. 6.3 Application development based on description such as Motor sequence control. Traffic light control. Elevator control. Tank level control. Reactor control. Conveyor system. Stepper motor control Speed Control of AC/ DC Motor using Programmable Drives.	10	14
	<b>Total</b>	<b>32</b>	<b>40</b>
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination :**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Application		
1.	Introduction of drives	03	03	02	ETF511-1	08
2.	AC Drives	06	06	04	ETF511-2	16
3.	DC Drives and drives for specific application	06	06	04	ETF511-3	16
4.	PLC fundamentals	04	06	04	ETF511-4	14
5.	PLC Instruction Set	04	04	04	ETF511-5	12
6.	PLC Programming and Applications	02	04	08	ETF511-6	14
	<b>TOTAL</b>	<b>25</b>	<b>29</b>	<b>26</b>		<b>80</b>

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**D) TERM WORK**

**Practical Exercises and related skills to be developed:**

The following practical exercises shall be conducted as

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1.	Load torque speed characteristics of DC motor	Verify and Plot characteristics of DC motor	ETF511-1
2.	Speed control of IM by stator voltage control	Analyze and verify relation between stator voltage & speed of IM.	ETF511-2
3.	V/F speed control method for Induction motor.	Analyze and Verify V/F speed control method for Induction motor	ETF511-2
4.	The speed of Induction motor using variable frequency control method.	Analyze and Verify variable frequency control method speed control method for Induction motor	ETF511-2
5.	Logic gates by using PLC.	Write and Verify truth table of Logic gates by execution of ladder program	ETF511-4
6.	Boolean Algebra	Verify Boolean equations by execution of	ETF511-

		ladder program	5
7.	Blinking of LED's	Write and verify ladder program for blinking by using timer.	ETF511-5
8.	Sequential ON-Off control of Lamps	Write and verify ladder program for traffic signal control for two directions	ETF511-6
9.	Elevator Control	Write and verify ladder program for elevator control	ETF511-6
10.	Tank Level controller	Write and verify ladder program for tank level control	ETF511-6
11.	Use of counters for pulse counting using limit switch/ proximity sensor	Write and verify ladder program for object counter using counter	ETF511-6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### ff) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

##### ii) Progressive Skill Test :

One mid-term *Progressive Skill Test* of 25 marks shall be conducted as per criteria given  
Final marks of term work shall be awarded as per *Assessment Pro-forma I*.

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

#### gg) Criteria for assessment at semester end practical exam :

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Sr. no	Criteria	Marks allotted
1	Preparedness for practical	10
2	Correct figures / diagrams	10
3	Observation tables	10
4	Result table / calculations / graphs	10
5	Safety / use of proper tools	10
	<b>Total</b>	<b>50</b>

Assessment at semester end practical exam as per Pro-forma I.

### INSTRUCTIONAL STRATEGIES :

#### Instructional Methods :

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

#### Teaching and Learning resources:

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

### REFERENCE MATERIAL :

#### a) Books / Codes

Sr. No.	Author	Title	Publisher
1.	Gary Dunning-	Intro. To Programmable logic control	Delmar Publishers,
2.	F.D. Petruzella-	Programmable logic controllers	Example Product Manufacturer; 3rd edition (2004)
3	S.K.Bhattacharya & S. Chatterjee	Industrial Electronics & Control	Tata McGraw-Hill.
4	Vedam Subrahmanyam	Electric drives	Tata McGraw-Hill.

#### b) Websites:

- 1) [www.allthingsplc.info](http://www.allthingsplc.info)
- 2) [www.inmplc.com](http://www.inmplc.com)
- 3) [www.rurelec.com](http://www.rurelec.com)
- 4) [www.yaskawa.com](http://www.yaskawa.com)
- 5) [www.cgglobal.com](http://www.cgglobal.com)
- 6) [www.controltechniques.com](http://www.controltechniques.com)

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**COURSE ID :**

**Course Name : PIC MICROCONTROLLER**  
**Course Code : EIF 512**  
**Course Abbreviation : FPIC**

**TEACHING AND EVALUATION SCHEME :**

**Pre-requisite Course(s) : NIL**

**Teaching Scheme :**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme :**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Practical	
Details of Evaluation	Average of two tests of 20 marks each	iii. 25 marks for each practical iv. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-I	
Marks	20	--	80	50E	150

E-External Examination

**RATIONALE:**

The subject is an extension of concepts covered in digital techniques. PIC microcontroller architecture, peripheral interfacing, assembly language programming is covered in this subject.

PIC Microcontroller is heart of all domestic, industrial, consumer goods and other high end products. The student will gain the knowledge of peripheral interfacing and programming them. PIC controller is in built element of embedded system. The subject will help the students to study concepts of embedded system. It will also help to understand design of simple microcontroller systems.

**COMPETENCY:**

Build PIC18F microcontroller based systems for different engineering applications.

**Cognitive :** Understanding PIC18F microcontroller architecture, working and instruction set.

**Psychomotor :** Write assembly language programs for wide range of applications.

**Affective :** Attitude of i) Logic ii) accuracy iii) precision v) punctuality

## COURSE OUTCOMES:

**EIF 512-1** Illustrate RISC/Harvard architecture of PIC18F microcontroller and discover its features.

**EIF 512-2** Use instructions from the instruction set of PIC18F to write basic assembly language programs to develop logic.

**EIF 512-3** Explore programming skills for I/O ports, Timers/Counters of PIC18F

**EIF 512-4** Explore programming skills for Serial ports and CCP/ECCP modes.

**EIF 512-5** Design interfacing of ADC, DAC, LCD, Keyboards etc. with PIC18F.

**EIF 512-6** Design interfacing of DC motor, Stepper motor, Relay etc.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX:

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Build PIC18F microcontroller based systems for different engineering applications	1	2	1	1	-	-	-	1	-	1	2	1
<b>EIF 512-1</b>	2	-	-	-	-	-	-	-	-	1	-	-
<b>EIF 512-2</b>	2	-	2	1	-	-	-	1	-	-	-	-
<b>EIF 512-3</b>	1	2	-	-	-	-	-	-	-	-	1	-
<b>EIF 512-4</b>	1	2	-	-	-	-	-	1	-	-	3	2
<b>EIF 512-5</b>	-	2	2	1	-	-	-	1	-	1	3	2
<b>EIF 512-6</b>	-	3	1	1	-	-	-	1	-	1	3	2

**CONTENT:**

**A) THEORY :**

**SECTION- I**

Sr. No.	TOPIC/ SUB TOPIC	No. (Hours)	Evaluation Marks
<b>EIF 512-1 Illustrate RISC/Harvard architecture of PIC18F microcontroller and discover its features.</b>			
01	<b>PIC Architecture :</b> 1.1 RISC Architectural feature of PIC 1.2 Feature of PIC18f 1.3 Pin diagram 1.4 Architecture : Working Register (WREG), status register, Special function registers (SFRs) 1.5 PIC file register 1.6 PIC data format & directives 1.7 Bank Switching in PIC18	07	12
<b>EIF 512-2 Use instructions from the instruction set of PIC18F to write basic assembly language programs to develop logic.</b>			
02	<b>PIC Instruction &amp; assembly Language Programming:</b> 2.1 Instruction size of PIC18 2.2 Addressing Mode 2.3 Instruction set: Data transfer instruction, Arithmetic instruction logical instruction, Control instruction using branch & Call, Bit oriented instruction, Table processing instruction 2.4 Simple programs	12	14
<b>EIF 512-3 Explore programming skills for I/O ports, Timers/Counters of PIC18F</b>			
03	<b>I/o Programming &amp; Timer/counter of PIC:</b> 3.1 I/O port Programming 3.2 I/O bit manipulation Programming 3.3 Timer & Programming : Timer0, Timer1, Timer3 Timer4 3.4 Counter & Programming	13	14
	<b>TOTAL:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

## SECTION- II

Sr. No.	TOPIC/ SUB TOPIC	Teaching (Hours)	Theory evaluation Marks
<b>EIF 512-4</b> Explore programming skills for Serial ports and CCP/ECCP modes			
04	<b>Serial Port &amp; CCP,ECCP Programming of PIC:</b> 4.1 Interrupt & Programming 4.2 Serial Port Programming 4.3 Stand & enhance CCP module 4.4 Compare mode 4.5 Capture mode 4.6 ECCP mode	10	14
<b>EIF 512-5</b> Design interfacing of ADC, DAC, LCD, Keyboards etc. with PIC18F			
05	<b>External Interfaces I:</b> 5.1 ADC Programming 5.2 DAC interfacing and programming 5.3 LCD interfacing and programming 5.4 Keyboard interfacing and programming 5.5 SPI Interfacing and MSSP module programming 5.5 DS 1306 RTC interfacing and programming	12	14
<b>EIF 512-6</b> Design interfacing of DC motor, Stepper motor, Relay etc			
06	<b>External Interfaces II:</b> 6.1 Relay and Opto-isolator interfacing 6.2 Stepper motor interfacing 6.3 DC motor interfacing 6.1.1 L293 motor driver IC 6.1.2 Unidirectional Control 6.1.3 Bidirectional control 6.4 Bidirectional speed control of DC motor with CCP -PWM mode	10	12
	<b>TOTAL:</b>	32	40
Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.			

**Specification table for setting question paper for semester end theory examination:**

Topic No.	Name of topic	Distribution of marks (Cognitive level-wise)			Course Outcome	Total Marks
		Remember	Understand	Applica-tion		
1.	PIC Architecture	04	04	04	EIF512-1	12
2.	PIC Instruction & assembly Language	04	06	04	EIF512-2	14

	Programming					
3.	I/O Programming & Timer/counter of PIC	06	04	04	EIF512-3	14
04.	Serial Port & CCP,ECCP Programming of PIC	04	06	04	EIF512-4	14
05.	External Interfaces I	04	06	04	EIF512-5	14
06.	External Interfaces II	04	04	04	EIF512-6	12

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

B) **TERM WORK** Term work shall consist of the following:

**Practical Exercises and related skills to be developed:**

Sr No.	Title of Practical Exercise	Skills / Competencies to be developed	Course Outcome
1	Introduction to MPLAB software	To know the details of MPLAB software	EIF5-1
2	Addition & subtraction of 8 bit numbers	Understand the concept & use of instructions : MOVLW, ADDWF SUBWF, SUBFWB	EIF512-2
3	Addition & Subtraction of 16 bit numbers	Understand the concept & use of instructions : MOVLW, ADDWF, SUBWF, SUBFWB	EIF512-2
4	Addition of 2 digit & 4 digit BCD numbers	Understand the concept & use of instructions: DAW	EIF512-1-2
5	Multiplication of 8 bit number	Understand the concept & use of instructions : MOVLW, MULWF	EIF512-1-2
6	Block transfer in forward & reverse direction	Understand the concept of indirect addressing & use of instructions LFSR F, k	EIF512-1-2
7	Block exchange	Understand the concept of indirect addressing & use of instructions LFSR F, k	EIF512-1-2
8	Addition of hexadecimal number in array	Understand the concept of indirect addressing & use of instructions : MOVLW, ADDWF	EIF512-1-2

9.	Addition of BCD number in array	Understand the concept of indirect addressing & use of instructions : MOVLW, ADDWF, DAW	EIF512-1-2
10.	Find smallest number in array	Understand the concept of indirect addressing & use of instructions : MOVLW, BC,BNC	EIF512-1-2
11	Find an largest number in an array	Understand the concept of indirect addressing & use of instructions : MOVLW, BC, BNC	EIF512-1-2
12	Find positive & negative number in an array	Understand the concept of indirect addressing & use of instructions : BTFSC, BTf	EIF512-1-2
13	Study of keyboard interfacing	Understand the concept of matrix keyboard	EIF512-3-5
14	Study of dc motor interfacing	Understand the concept of dc motor interfacing	EIF512-3-6
15	Study of stepper motor interfacing.	To learn concept of stepper motor interfacing	EIF512-3-6

### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

#### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks as per given criteria .

##### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

#### Criteria for Progressive Assessment of Practical and Skill Test

Domain	Particulars	Marks out of 25
Cognitive	Technical preparedness for practical	05
Psychomotor	Algorithm /Flowchart	05
	Program/Logic	05
Affective	Discipline and punctuality	05
	Decency and presentation	05
<b>TOTAL</b>		<b>25</b>

**b) Criteria for assessment at semester end practical exam:**

Every student has to perform one practical within 3 hours at semester end practical exam which shall be assessed as per following criteria.

Domain	Particulars	Marks out of 50
Cognitive	Technical Ability	05
	Logical Approach	05
Psychomotor	Presentation/ Algorithm and Flowchart	10
	Programming skill	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
<b>TOTAL</b>		<b>50</b>

**INSTRUCTIONAL STRATEGIES :**

**Instructional Methods:**

1. Lectures cum Discussions
2. Regular Home Assignments
3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board
2. Video clips
3. Slides
4. Item Bank
5. Charts

**REFERENCE MATERIAL:**

**e) Books / Journals / IS Codes**

Sr. No.	Author	Title	Publisher
1.	Muhmed ali Mazidi	PIC Microcontroller & embedded system	Pearson edition publication.
2.	Peatmann	PIC microcontroller programming.	Tata McGraw-Hill

**b) Websites**

- 1) [www.nptel.com](http://www.nptel.com)
- 2) [www.datasheet.com](http://www.datasheet.com)
- 3) [www.pic.com](http://www.pic.com)

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**COURSE ID:**

Course Name : VLSI  
Course Code : EIF 513  
Course Abbreviation : FVLS

**TEACHING AND EVALUATION SCHEME:**

Pre-requisite Course(s) : < nil >

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	04	06
Practical	02	

**Evaluation Scheme:**

Mode of Evaluation	Progressive Assessment		Term End Examination		Total
	Theory	Practical	Theory Examination	Practical	
Details of Evaluation	Average of two tests of 20 marks each	i. 25 marks for each practical ii. One PST of 25 marks	Term End Theory Exam (03 hours)	As per Proforma-I	
Marks	20	--	80	50E	150

\* Assessment as per Pro-forma I

**RATIONALE:**

The influence of integrated-circuit technology in the past few years on our society has been pervasive, in area ranging from consumer products to business management to manufacturing control. The driving force behind this pervasiveness is that the functional capability of modern integrated circuitry has increased in scope and complexity exponentially with time over the past 20 years. The designers of modern integrated circuitry have continually endeavored to provide more computational speed with less dissipated electrical power and less circuit board area, while maintaining a low failure rate and an aggressive cost. The complexity and speed is finding ready application for VLSI systems in digital processing. Although silicon MOS-based circuitry will meet most requirements in such systems. The student can acquire knowledge in the design skill of combinational and sequential circuit with the help of VHDL and CMOS Logic circuit processing operation, student can use this knowledge as technician, supervisor and programmer in different sections of industry

**COMPETENCY:**

Acquire design skill of FPGA based digital circuits with the help of VHDL for various electronic applications.

**Cognitive: Understanding architecture** and design concepts based on FPGA/CPLD.

**Psychomotor :** Write VHDL programs targeted towards FPGA for wide range of applications.

**Affective:** Develop the skill of i) Logical thinking ability ii) Design within time constraints.

## COURSE OUTCOMES:

**EIF 513-1** Understand fundamental issues VLSI technology and constraints imposed by it on design.

**EIF 513-2** Explore the various construction processes in CMOS technology and implementation of finite state machine (FSM).

**EIF 513-3** Understand the architectural details of FPGA/CPLD.

**EIF 513-4** Develop the programming skills using VHDL language.

**EIF 513-5** Design combinational and sequential circuits using VHDL.

**EIF 513-6** Develop the skills for designing digital circuits using different modeling styles in VHDL.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX :

[ Note : Correlation levels :1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation]

Competency and Cos	Programme Outcomes POs and PSOs											
	PO 1 Basic knowledge	PO 2 Discipline knowledge	PO 3 Experiments and practice	PO 4 Engineering Tools	PO 5 The engineer and society	PO 6 Environment and sustainability	PO 7 Ethics	PO 8 Individual and team work:	PO 9 Communication	PO 10 Life-long learning	PSO 1 Operate and Maintain	PSO 2 Supervision and Providing Solution
<b>Competency:</b> Develop the FPGA based systems for digital electronics applications	2	1	2	2	-	-	-	1	-	1	3	2
<b>EIF 513-1</b>	2	-	1	-	-	-	-	1	-	-	1	1
<b>EIF 513-2</b>	2	1	-	-	-	-	-	-	-	-	2	1
<b>EIF 513-3</b>	2	1	1	2	-	-	-	1	-	1	2	2
<b>EIF 513-4</b>	1	2	1	2	-	-	-	1	-	1	3	3
<b>EIF 513-5</b>	1	2	3	3	-	-	-	1	-	1	3	3
<b>EIF 513-6</b>	1	2	2	3	-	-	-	2	-	2	3	3

**CONTENT:**

**A) THEORY :                      Section I**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Marks
<b><i>EIF 513-1 Understand fundamental issues VLSI technology and constraints imposed by it on design.</i></b>			
01	<b>Very Large Scale Integration (VLSI) Technology</b> 1.1 CMOS Logic Basic Gates using NMOS 1.2 CMOS Logic Basic Gates using PMOS 1.3 Switch, ParamETFr measurements. 1.4 VLSI and its use in electronics	06	08
<b><i>EIF 513-2 Explore the various construction processes in CMOS technology and implementation of finite state machine (FSM).</i></b>			
02	<b>VLSI Design Concepts</b> 2.1 MOS circuit characterization and performance Estimation. 2.2 CMOS Technology- P Well process , N Well process, Twin tube process 2.3 Circuit elements - Resistors and capacitors 2.4 Finite state machines (FSM) 2.5 Moore and Mealey machines: Implementation of circuits using Moore and Mealey machines.	12	16
<b><i>EIF 513-3 Understand the architectural details of FPGA/CPLD.</i></b>			
03	<b>Architecture of ASIC and PLD</b> 3.1 CPLD -Xilinx architecture, 3.2 Details of internal block diagram 3.3 Atmel series architecture, 3.4 Details of internal block diagram 3.5 Introduction to FPGA like Xilinx (FPGA), 3.6 Introduction to FPGA SPARTAN 3s series, 3.7 Introduction to FPGA Atmel.	14	16
	<b>TOTAL:</b>	32	40

**Section-II**

Sr. No.	Topics / Sub-topics	Lecture s (Hours)	Marks
<b>EIF 513-4 Develop the programming skills using VHDL language.</b>			
04	<b>Hardware Description Language (HDL)</b> 4.1 Features of Verilog- 4.2 Entity, Architecture 4.3 Configuration 4.4 Package, Bus 4.5 Driver, Attributes, Process 4.6 Behavioral Modeling 4.7 Sequential Processing 4.8 Data types, Configurations.	12	14
<b>EIF 513-5 Design combinational and sequential circuits using VHDL.</b>			
05	<b>Simulation, Testing and Synthesis using VHDL</b> 5.1 Simulation Issues 5.2 Testing Issues 5.3 Synthesis Issues	08	14
<b>EIF 513-6 Develop the skills for designing digital circuits using different modeling styles in VHDL.</b>			
06	<b>Hardware Modeling examples (operation &amp; block Testing)</b> 6.1 Different styles of modeling 6.2 Modeling simple elements 6.3 Modeling conditional operators 6.4 Modeling combinational logic 6.5 Modeling regular structure 6.6 Modeling synchronous logic	12	12
	<b>TOTAL:</b>	32	40

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**Specification Table For Question Paper of Theory Examination:**

Section / Topic no.	Name Of the Topic	Distribution Of Marks (Level Wise)			Course Outcome	Total Marks
		Knowledge	Comprehension	Applications		
I/1	Very Large Scale Integration (VLSI) Technology	6	2	0	EIF 513-1	8
I/2	VLSI Design Concepts	4	8	4	EIF 513-2	16
I/3	Architecture of ASIC and PLD	8	4	4	EIF 513-3	16
II/4	Hardware Description Language (HDL)	4	2	8	EIF 513-4	14
II/5	Simulation, Testing and Synthesis using VHDL	2	4	8	EIF 513-5	14
II/6	Hardware Modeling examples (operation & block Testing)	2	2	8	EIF 513-6	12
				TOTAL		80

Semester end exam question paper should be such that total marks of questions on each topic is one and half times the marks allotted above but the candidates are able to attempt questions of the above allotted marks only.

**B) TERM WORK** Term work shall consist of the following :

**Laboratory experiments and related skills to be developed(based on any software or hardware as per availability)**

The term work shall consist of a journal containing write ups by students on the following assignments conducted in practical sessions of batches of about 22 students :

Sr No.	Title of Tutorial Exercise	Skills / Competencies to be developed	COURSE OUTCOMES
1.	Basic gates using VHDL (Any Two)	Design ,verify, test, Synthesize basic gates using VHDL (Any Two)	EIF 513-1
2.	synchronous counter using FPGA	Design ,verify, test, Synthesize synchronous counter using FPGA	EIF 513-5
3.	Scrolling of data on seven segment display using FPGA	Design ,verify, test, Synthesize Scrolling of data on seven segment display using FPGA	EIF 513-5

4.	Interface ADC-DAC using FPGA	Design ,verify, test, Synthesize interface ADC-DAC	EIF 513-6
5.	Generation of Ramp using DAC using FPGA	Verify and observe Ramp generated using DAC in FPGA	EIF 513-3,4,5
6.	Temperature sensing using ADC-DAC using FPGA	Design temperature sensing using ADC-DAC and verify output	EIF 513-3,4,5
7.	8:1 multiplexer using FPGA	Design ,verify, test, Synthesize 8:1 multiplexer using FPGA	EIF 513-3,4,5,6
8.	2:4 Decoder using FPGA	Design ,verify, test, Synthesize 2:4 Decoder using FPGA	EIF 513-3,4,5,6
9.	8:3 Encoder using FPGA	Design ,verify, test, Synthesize 8:3 Encoder using FPGA	EIF 513-3,4,5,6
10.	4 bit ALU using FPGA	Design ,verify, test, Synthesize 4 bit ALU using FPGA	EIF 513-3,4,5,6
	Practical oriented miniproject	Group of 3 students should build a mini project under guidance of teacher	EIF 513-1,2,3,4,5,6

#### ASSESSMENT CRITERIA FOR TERM WORK AND PRACTICAL EXAMINATION

##### a) Assessment Criteria for Term work :

##### i) Continuous Assessment of Practical Assignments:

Every practical assignment shall be assessed for 25 marks

##### ii) Progressive Skill Test:

One mid-term Progressive Skill Test of 25 marks shall be conducted as per criteria given below

##### Criteria for Continuous Assessment of Practical work and Progressive skill Test :

Sr. No.	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram.	05
2	Observations & Result Table	05
3	Sample Calculations with relevant Formulae.	05
4	Proper Graphs & Procedure / workmanship Safety measures	05
5	Oral Based on Term Work	05
	Total	25

**b) Criteria for assessment at semester end practice exam:**

Every student shall be assessed as per following criteria

Domain	Particulars	Marks out of 50
Cognitive	Technical Ability	05
	Logical Approach	05
Psychomotor	Presentation/ Algorithm and Flowchart	10
	Programming skill	10
Affective	Discipline and punctuality	10
	Decency and presentation	10
TOTAL		50

**INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Lectures cum Discussions      2. Regular Home Assignments.      3. Laboratory work

**Teaching and Learning resources:**

1. Chalk board    2. Video clips    3. Slides    4. Item Bank    5. Charts

**REFERENCE MATERIAL:**

**a) Books / Codes**

Reno.	Author	Title	Publisher
1	Douglas A. Pucknell, Kamran Eshraghian	Basic VLSI Design	Prentice Hall of India
2	Douglas Perry	VHDL	McGraw Hill
3	Xilinx	Xilinx Manual	www.xilinx.com
4	John f. Wakerly	Digital Design	Prentice Hall of India

**b) Websites**

- 1) [jntuh.ac.in/new/bulletin\\_board/VLSI.pdf](http://jntuh.ac.in/new/bulletin_board/VLSI.pdf)
- 2) <http://www.xilinx.com>
- 3) <http://www.atmel.com>

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(ANNEXURE)

(BRIDGE COURSE FOR DIRECT SECOND YEAR)

**COURSE ID:**

**Course Name : BASIC ELECTRONICS**

**Course Abbreviation : BBTX**

**TEACHING AND EVALUATION SCHEME:**

**Prerequisites : NIL**

**Teaching Scheme:**

Scheme Component	week	Hours
Theory	02 Week	12*2=24

**Evaluation Scheme :-** Writing assignment on each chapter consisting of 7 Questions.

**RATIONALE:**

All direct second year admitted electronics group students need, a grasp of certain fundamental principles and concepts are essential pre- requisitions for it. This subject deals with the most basic devices and circuits on which the further development of subject depends.

**COURSE AIMS:**

State the principles and operations of various electronic devices.

- 1) Use electronic devices in different circuits
- 2) To built up simple electronic circuits
- 3) Draw the characteristics of basic components like transistor etc.
- 4) Test transistors and FET.
- 5) Read the data sheets of transistors and FET.

**CONTENTS:**

**A) THEORY:**

**SECTION-I**

Sr. No.	Topics	Teaching hours
<b>1</b>	<b>Rectifier, Filter</b> 1.0 Half wave rectifier - working, waveforms 1.1 Full wave rectifier - working, waveforms 1.2 Filter - study of shunt capacitor, series inductor, LC, Filter.,	<b>04</b>
<b>2</b>	<b>Bipolar Junction Transistor(BJT)</b> 2.0 Introduction. 2.1 Constructional features. 2.2 Operating principles of NPN 2.3 Transistor configurations & Modes of operation only CE configuration. 2.4 Specifications of transistor and relation between $\alpha$ & $\beta$	<b>05</b>
<b>3</b>	<b>Biassing of transistor and Single amplifier</b> 3.1 Q Point & stability 3.3.1 Voltage Divider Bias Circuit 3.2 Study of single stage amplifier. 3.2.1 Diagram 3.2.2 Working 3.2.3 Input Output Waveform	<b>05</b>

<b>4</b>	<b>Field Effect Transistor (FET)</b> 4.0 Classification of FET 4.1 Study of JFET 4.1.1 Construction details. 4.1.2 Working principle 4.1.3 Characteristics 4.1.4 JFET parameters and relation between $\mu_r$ & $g_m$ 4.2 Comparison between JFET and BJT 4.3 Study of MOSFET:-Types,symbol,working principle, applications 4.4 Specifications of FET	<b>05</b>
<b>5</b>	<b>Regulated Power Supply</b> 5.1 Block diagram of Regulated power supply. 5.2 Zener diode as a voltage regulator Emitter follower regulator. 5.3 Study of IC 78xx & IC 79xx series of voltage regulators 5.4. Study of a LM317 internal block diagram and pinout features.	<b>05</b>

#### REFERENCE MATERIAL :

##### a) Books / Codes

Sr. No.	Author	Title	Publisher
1.	A. Motershed	Electronics Devices & Circuits	PHI Publication
2.	Malvino	Electronics Principles	McGraw Hill
3	V. K. Mehta	Principles of Electronics	S.Chand
4	G. K. Mithal	Applied Electronics	Khanna Publication
5	B. L. Theraja	Basic Electronics	S.Chand

##### c) Websites:

- 1) [www.allaboutcircuits.com](http://www.allaboutcircuits.com)
- 2) [www.electronicstheory.com](http://www.electronicstheory.com)
- 3) [www.electronicstutorial.com](http://www.electronicstutorial.com)

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