Faculty Profile

1. Name of Faculty: Dr. Dattatray M. Garge
2. Present designation: HOD, Electronics, I/C Principal
3. Office Address   : Government Polytechnic, Kolhapur
4. Residential Address: Rajendra Nagar, Kolhapur
5. Mobile No.   :   9922404552, 9420459552
6. E-Mail Id   : dattagarge@gmail.com
7. Date of joining (Govt of Maharashtra) : 23/07/2001
                    (Govt. Poly. Kolhapur) : 21/09/2019
8. Total Teaching Experience in years: 23

9. Memberships of professional organizations/Bodies:
     ISTE – LM31467

QUALIFICATIONS: (Graduation and Onwards)

Sr. No.	Degree	University	Class Award	Year of Passing
01	**PhD (Engg)**	**Shivaji University, Kolhapur**	-	**June, 2013**
02	**Master of Engineering**	**Shivaji University, Kolhapur**	**First class (67%)**	**April 1996**
03	**Bachelor of Engineering**	**Shivaji University, Kolhapur**	**First class (63%)**	**June 1993**
04	**Diploma**	**VJTI, Mumbai**	**First class with Distinction (88%)**	**June 1990**

CAREER DETAILS:

Sr. No.	Organization	Designation	Duration	Period	Work Done in Brief
01	Government Polytechnic, Kolhapur	HOD, Electronics	21/09/2019 till today	03 Years	Teaching-Learning Process, Administration of Department work
02	Government Polytechnic, Amravati	HOD, Electronics	10/08/2016 to 20/09/2019	03 Years	Teaching-Learning Process, Administration of Department work
03	Government Polytechnic, Kolhapur	Lecturer in Electronics	01/08/2015 to 03/08/2016	01 Year	Teaching-Learning Process, Department work as per directions of HOD
04	Government Polytechnic, Ratnagiri	Lecturer in Electronics	01/01/2012 to 31/07/2015	3.5 Years	Teaching-Learning Process, Department work as per directions of HOD
05	Government Polytechnic, Kolhapur	Lecturer in Electronics	01/08/2003 to 31/12/2011	8.5 Years	Teaching-Learning Process, Department work as per directions of HOD
06	Government Polytechnic, Latur	Lecturer in Electronics	23/07/2001 to 31/07/2003	02 Years	Teaching-Learning Process, Department work as per directions of HOD
07	Godavari Engineering College, Jalgaon	Lecturer / Vice-Principal	04/08/1999 to 22/07/2001	02 Years	Teaching-Learning Process, Administration of institute work, to administer academic progress of students

TRAINING COURSES ATTENDED:

Sr. No.	Course Name	Duration	Place	Training Agency	Remarks
	Effective use of ICT Tools in TLP	08/06/2020 to 12/06/2020	Government Polytechnic, Malvan	Industry person	DTE, MSBTE approved (online)
	Overview of NBA norms and Preparation for Accreditation	18/05/2020 to 22/05/2020	Government Polytechnic, Sakoli	Local Experts from Academics	AICTE, DTE, MSBTE approved (online)
	E-Governance	02/05/2019 to 04/05/2019	YASHADA, Pune	Experts from industry, administration and academics	DTE approved
	Foundation course	06/12/2018 to 16/01/2019	YASHADA, Pune	Experts from industry, administration and academics	DTE approved
	Innovative Teaching Methodologies & Professional Skills Development	08-01-2018 to 12-01-2018	Government Polytechnic, Kolhapur	Industry person	AICTE-ISTE approved Refresher programme
	VLSI Design	19/05/2014 to 23/05/2014	NITTTR Ext. Centre, Pune	NITTTR, Bhopal	AICTE recognised
	Design of Low Power Embedded System	30/12/2013 to 10/01/2014	Gramin Polytechnic, Nanded	Industry persons	AICTE sponsored
	Multi-sim and Labview Computer Software for Electrical & Electronics	03/12/2012 to 07/12/2012	NITTTR, Bhopal	NITTTR, Bhopal	
	Advanced Embedded System Design – PIC, ARM & RTOS	14/05/2012 to 25/05/2012	Government Polytechnic, Kolhapur	Kanad Electronics Sangli	ISTE approved
	Disaster Management	12/03/2012 to 16/03/2012	Government Polytechnic, Kolhapur	Corporate trainer	MSBTE approved
	Induction Programme	31/01/2011	NITTTR Ext.	NITTTR, Bhopal	

Phase-II	to 11/02/2011	Centre, Pune		
Preparing LRs using 2D Animation	01/02/2010 to 05/02/2010	NITTTR, Bhopal	NITTTR, Bhopal	
PC Hardware Maintenance and Networking	29/05/2006 to 23/06/2006	Government Polytechnic, Kolhapur	Vishvanet Computers, Kolhapur	ISTE approved
Industrial Training to Academics	02/01/2006 to 14/01/2006	HAL Nasik	HAL Nasik	
Wireless & Mobile Communication	17/05/2004 to 28/05/2004	Government Polytechnic, Pune	Industry person	AICTE-ISTE sponsored
Induction Programme Phase-I	17/06/2002 to 12/07/2002	Government Polytechnic, Pune	NITTTR, Bhopal	
DSP Design & Development Tools	11/06/2001 to 23/06/2001	PICT Pune	Local Experts from Industry & Academics	AICTE-ISTE sponsored

Publications:

Sr. No.	Title of Paper	Name of conference/journal	Published year
01	Development Of Multiwavelet And Its Applications – A Review	National Conference On Trends In Advanced Computing	Dec.2007
02	A Low Cost Wavelet Based Mammogram Image Processing For Early Detection Of Breast Cancer	Indian Journal Of Science And Technology, Vol. 2, No.9, Pp 63-65	Sept.2009
03	Multi-Wavelet Based Mammogram Image Processing For Early Detection Of Breast Cancer	International Conference On Emerging Trends In Engineering	Feb. 2010
04	Multi-Wavelet Based Mammogram Image Processing For Early Detection Of Breast Cancer	International Journal Of Biological Sciences And Engineering, ISSN 0976-1519, Vol.01, No. 02, Pp. 111-118	April 2010
05	Feature Extraction Using Balanced Multi-Wavelets For Classification Of Micro-Calsification Clusters With Application In Breast Cancer Diagnostic	International Ournal Of Bioscience And Technology, ISSN 0974-3987, Vol. 4, No. 5, Pp 29-35	2011
06	Balanced Multi-Wavelet Based Mammogram Image Processing	International Journal Of Latest Trends In Network & Communication, Vol.01, No.01, Pp 1-5	June 2011
07	Application Of Multiwavelet Transform In Determining Ration Of Blood Cells For Leukemia Detection	International Journal Of Engineering And Innovative Technology ISSN 2277-3754, Vol.3, No. 6 , Pp 205-209	Dec. 2013

> **Specialization/ Area of Interest: MEDICAL IMAGE PROCESSING**

> **Portfolios allotted at institute level (2022-23): I/C Principal**

> **Portfolios allotted at department level (2022-23): NBA work administration**